

AUTOMOTIVE

COMPLIANT

HALOGEN

FREE GREEN

(5-2008)



DESCRIPTION

PLCC-2 package.

detector operation.

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High Speed Infrared Emitting Diode, 890 nm



VSMF9700X01 is a high speed infrared emitting diode in

GaAlAs double hetero (DH) technology in a miniature

VSMF9700X01 is dedicated to emitter operation and

FEATURES

Package type: surface-mount

• Package form: PLCC-2

• Dimensions (L x W x H in mm): 3.5 x 2.8 x 1.75

Peak wavelength: λ_p = 890 nm

High reliability

· High radiant power

· High radiant intensity

• Angle of half sensitivity: $\varphi = \pm 60^{\circ}$

· Low forward voltage

· Suitable for high pulse current operation

• Floor life: 168 h, MSL 3, according to J-STD-020

· Lead (Pb)-free reflow soldering

AEC-Q101 qualified

 Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- · Automotive sensors
- Rain sensor
- Infrared high speed remote control and free air data transmission systems

PRODUCT SUMMARY				
I _e (mW/sr)	φ (°)	$\lambda_{\mathbf{p}}$ (nm)	t _r (ns)	
8	± 60	890	50	
	I _e (mW/sr)	· (······		

Note

· Test condition see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VSMF9700X01-GS08	Tape and reel	MOQ: 7500 pcs, 1500 pcs/reel	PLCC-2		
VSMF9700X01-GS18	Tape and reel	MOQ: 8000 pcs, 8000 pcs/reel	PLCC-2		

Note

MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	5	V	
Forward current		I _F	100	mA	
Surge forward current	t _p = 100 μs	I _{FSM}	200	mA	
Power dissipation		P _V	170	mW	
Junction temperature		Tj	110	°C	
Operating temperature range		T _{amb}	-40 to +95	°C	
Storage temperature range		T _{stg}	-40 to +110	°C	
Soldering temperature	Acc. figure 8, J-STD-020	T _{sd}	260	°C	
Thermal resistance junction-to-ambient		R _{thJA}	400	K/W	



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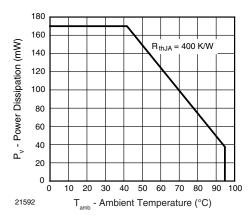


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

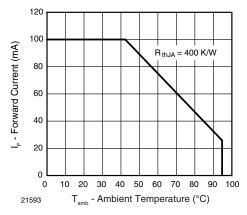


Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	V_{F}	=	1.6	1.8	V
	$I_F = 200 \text{ mA}, t_p = 100 \mu \text{s}$	V _F	=	1.8	2.1	V
Temperature coefficient of V _F	I _F = 100 mA	TK _{VF}	=	-2.1	-	mV/K
Reverse current	V _R = 5 V	I _R	=	-	10	μΑ
Junction capacitance	$V_R = 0 \text{ V, f} = 1 \text{ MHz, E} = 0$	C _j	-	160	-	pF
Radiant intensity	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	l _e	5	8	-	mW/sr
Radiant power	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	фe	=	40	-	mW
Temperature coefficient of φ _e	I _F = 100 mA	TKφ _e	-	-0.35	-	%/K
Angle of half intensity		φ	=	± 60	-	0
Peak wavelength	I _F = 100 mA	λ_{p}	=	890	-	nm
Spectral bandwidth	I _F = 100 mA	$\Delta\lambda_{1/2}$	-	50	-	nm
Temperature coefficient of λ_p	I _F = 100 mA	TKλ _p	-	0.25	-	nm/K
Rise time	I _F = 100 mA	t _r	-	50	-	ns
Fall time	I _F = 100 mA	t _f	-	50	-	ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

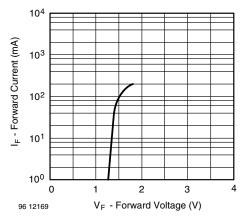


Fig. 3 - Forward Current vs. Forward Voltage

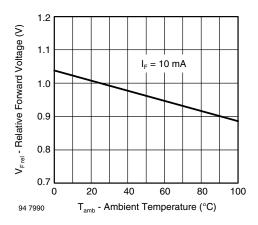


Fig. 4 - Relative Forward Voltage vs. Ambient Temperature



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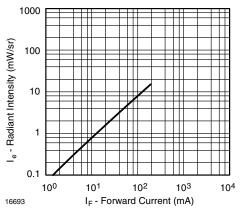


Fig. 5 - Radiant Intensity vs. Forward Current

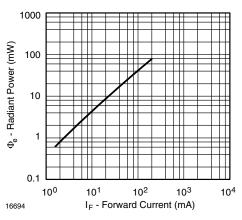


Fig. 6 - Radiant Power vs. Forward Current

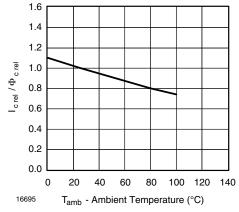


Fig. 7 - Relative Radiant Intensity/Power vs. Ambient Temperature

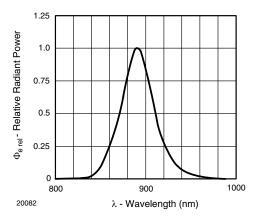


Fig. 8 - Relative Radiant Power vs. Wavelength

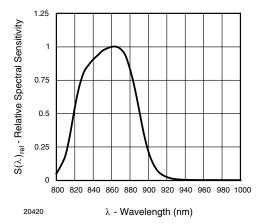


Fig. 9 - Relative Spectral Sensitivity vs. Wavelength

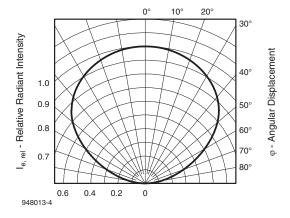
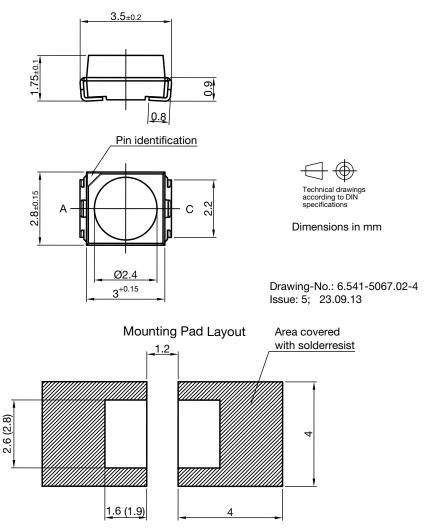


Fig. 10 - Relative Radiant Intensity vs. Angular Displacement



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PACKAGE DIMENSIONS in millimeters



Dimensions: Reflow and vapor phase (wave soldering)

REFLOW SOLDER PROFILE

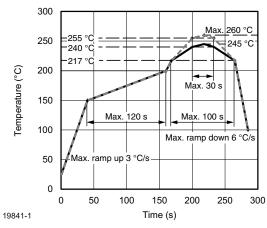


Fig. 11 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 3, acc. to J-STD-020.

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.

VSMF9700X01



TAPE AND REEL

PLCC-2 components are packed in antistatic blister tape (DIN IEC (CO) 564) for automatic component insertion. Cavities of blister tape are covered with adhesive tape.

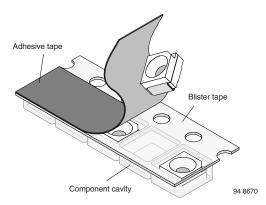


Fig. 12 - Blister Tape

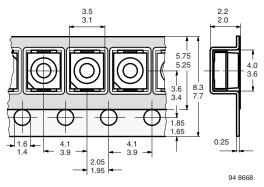


Fig. 13 - Tape Dimensions in mm for PLCC-2

MISSING DEVICES

A maximum of 0.5 % of the total number of components per reel may be missing, exclusively missing components at the beginning and at the end of the reel. A maximum of three consecutive components may be missing, provided this gap is followed by six consecutive components.

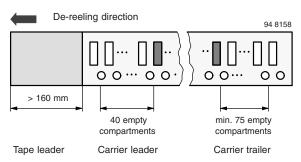


Fig. 14 - Beginning and End of Reel

The tape leader is at least 160 mm and is followed by a

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carrier tape leader with at least 40 empty compartements. The tape leader may include the carrier tape as long as the cover tape is not connected to the carrier tape. The least component is followed by a carrier tape trailer with a least 75 empty compartements and sealed with cover tape.

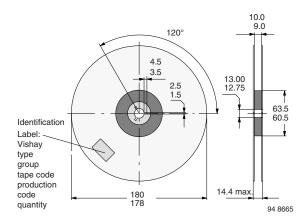


Fig. 15 - Dimensions of Reel-GS08

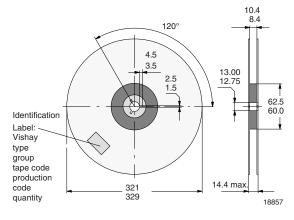


Fig. 16 - Dimensions of Reel-GS18

COVER TAPE REMOVAL FORCE

The removal force lies between 0.1 N and 1.0 N at a removal speed of 5 mm/s. In order to prevent components from popping out of the blisters, the cover tape must be pulled off at an angle of 180° with regard to the feed direction.



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