

User Registration

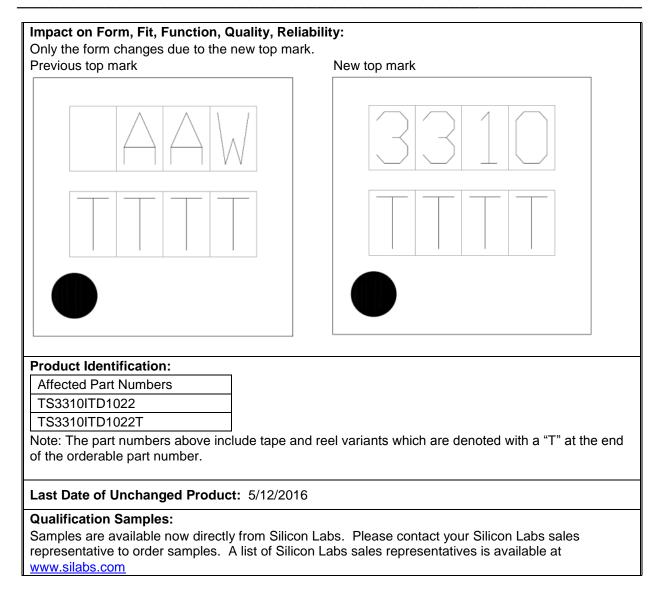
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PCN Date: 2/8/2016		Effective Date: 5/12/	/2016			
Title: TS3310 Top Mark and Datasheet change						
Originator: Rick Bye	Pho	ne: 512-532-5740	Dept: IoT Marketing			
Customer Contact: Kathy Hagg	jar Pho	ne: 512-532-5261	Dept: Sales			
PCN Type:						
☑ Datasheet	□ Foundry		Packing			
□ Product Revision	⊠ Assembly	, 🛛 🛛 L	abeling			
Discontinuance	□ Test		Other			
Last Order Date: Not Applicable						
PCN Details						
Description of Change: The top marking on the TS3310 package has been changed from "AAW" to "3310". This top mark change is documented in a new revision, 1.1, of the TS3310 datasheet. Other changes to the revised datasheet are: Increase V _{IN} (max) to 5.0V. Increase the recommended value of C _{STORE} , to be 22µF for circuit A, or 2.2µF for circuit B. Updated package outline drawing (dimensions unchanged, slightly different pin appearance).						
Reason for Change: The top mark was changed to be user readable and to be compatible with other TS331x devices. The C_{STORE} value was increased to prevent the possibility of V_{STORE} exceeding V_{PROG} by more than the 3% maximum specified in the datasheet when V_{IN} approaches V_{STORE} . V_{IN} (max) was increased to reflect the true capabilities of the device and the ways that customers need to use it. The package drawing						

was updated to show the correct appearance of the pins.



Process Change Notice #1602081





Specific conditions of acceptance of this change will be considered on a case by case basis if written notice is submitted within 30 days of this notice. To request further data or inquire about this notification, please contact your local Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <u>www.silabs.com</u>.

In some cases rejection of a change notice may impact Silicon Labs product pricing, delivery, quality, or reliability.

Customer Early Acceptance Sign Off:

Customers may approve early PCN acceptance by completing the information below:

Early Acceptance: Date: _____

Name:

Company:

Email your early Acceptance approval to: <u>katherine.haggar@silabs.com</u>



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est Name	Test Condition	Qualification	Start	End	Notes	Summary	Status		
	elerated Environment Stress	s Tests				and the second second second			
HAST	JA110								
	130°C, 85%RH	1 lot, N=>25	Q036583	0/30	2	1 lots	Pass		
	Vcc=3.6V, 96 hours	·				0/30			
HAST	JA110								
	130°C, 85%RH	1 lot, N=>25	Q036580	0/30		1 lots	Pass		
	96 hours, unbiased					0/30			
Moisture/Reflow	JA113		T I						
Sensitivity (MSL1)	Reflow 3x @ 260°C	1 lot, N=>75	Q036440	0/150	1, 2	1 lots	Pass		
	MSL1					0/150			
Temp Cycle	JA104								
	Cond C: -65°C to 150°C	1 lot, N=>25	Q036579	0/30	2	1 lots	Pass		
	500 cycles					0/30			
LTSL	JA103								
	-55°C, 1000hr	1 lot, N=>25	Q036581	0/30		1 lots	Pass		
						0/30			
HTSL	JA103								
	150°C, 1000hr	1 lot, N=>25	Q036584	0/30	2	1 lots	Pass		
						0/30			
	celerated Lifetime Simulation	Tests							
HTOL	JA108								
	T _J ≥ 125°C, Dynamic	1 lot, N=>77	Q037059	0/100	2	1 lots	Pass		
	Vcc=3.6V, 1000 hours					0/100			
ELFR	JA108								
	T _J ≥ 125°C, Dynamic	1 lot, N=>500	Q037058	0/500	2	1 lots	Pass		
	Vcc=3.6V, 48 hours					0/500			
Test Group E - Ele			_						
ESD-HBM	JA114								
		1 lot, N=>3	Q037053	0/12	2500V	1 lot	Class 2		
						0/12			
ESD-CDM	JC101								
		1 lot, N=>3	Q037055	0/12	1000V	1 lot	Class C3		
						0/12			
Latch Up	JESD78								
	±100mA	1 lot, N=>3	Q037057 Q037056	80 °C 25 °C			Pass Pass		
	Overvoltage = 3.6V	1 lot, N=>3	0037056	25 C			Pass		

W7206F1 Process Change Notice Form rev AW



Process Change Notice #1602081

Part Rev C, TSMC Fabrication, UNISEM Assembly except as noted						
est Name	Test Condition	Qualification	Start	End Notes	Summary	Status
	This	report applies to the	following part	numbers:		
	TS3310-ITD (TS3310ITD1022)			33310-ITDT \$3310ITD1022T)		

W7206F1 Process Change Notice Form rev AW