



DMT6004LPS

PowerDI5060-8

Product Summary

BV _{DSS}	Ros(ом) Мах	I _D Max T _C = +25°C (Note 7)
co)/	3.1mΩ @ V _{GS} = 10V	100A
60V	4.5mΩ @ V _{GS} = 4.5V	100A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Primary switches in isolated DC-DC
- Synchronous rectifiers
- Load switches

Features

 100% Unclamped Inductive Switching – Ensures More Reliable and Robust End Application

60V N-CHANNEL ENHANCEMENT MODE MOSFET

- Low RDS(ON) Minimizes Power Losses
- Low Qg Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

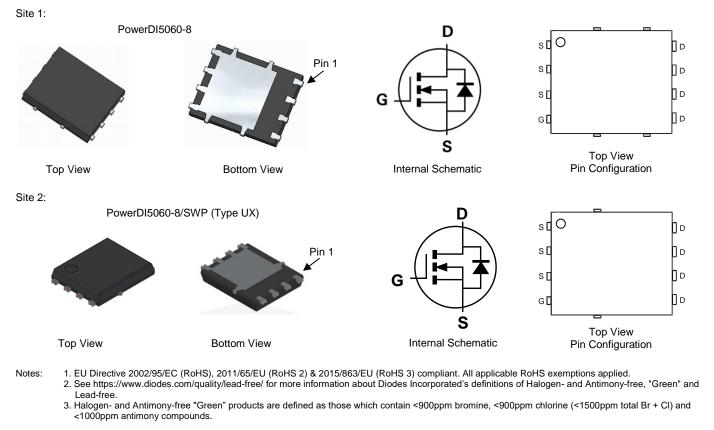
https://www.diodes.com/products/automotive/automotiveproducts/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (@3)
- Weight: 0.097 grams (Approximate)



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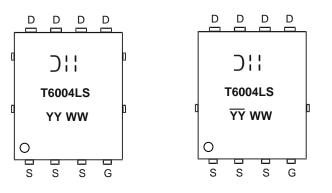


Ordering Information (Note 4)

Part Number	Backage	Packing		
Fait Nulliber	Package	Qty.	Carrier	
DMT6004LPS-13	PowerDI5060-8	2,500	Tape & Reel	
DIM18004EF3-13	PowerDI5060-8/SWP (Type UX)	2,500	Tape & Reel	

Note: 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



);; = Manufacturer's Marking T6004LS = Product Type Marking Code YYWW = Date Code Marking YY or \overline{YY} = Year (ex: 23 = 2023) WW = Week (01 to 53)

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		Vdss	60	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 5)	T _A = +25°C T _A = +70°C	ID	22 16	A
Continuous Drain Current (Notes 6 & 7)	T _C = +25°C T _C = +70°C	ID	100 100	A
Maximum Continuous Body Diode Forward Current (Note 6)		ls	100	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	400	А
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		lsм	400	А
Avalanche Current, L = 0.2mH		las	40	A
Avalanche Energy, L = 0.2mH		Eas	160	mJ

Thermal Characteristic

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	2.5	W
Thermal Resistance, Junction to Ambient (Note 5)		Reja	47	°C/W
Total Power Dissipation (Note 6)	Tc = +25°C	PD	139	W
Thermal Resistance, Junction to Case (Note 6)		Rejc	0.9	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1 inch square copper plate.

6. Thermal resistance from junction to soldering point (on the exposed drain pad).

7. Limited by package.

Notes:

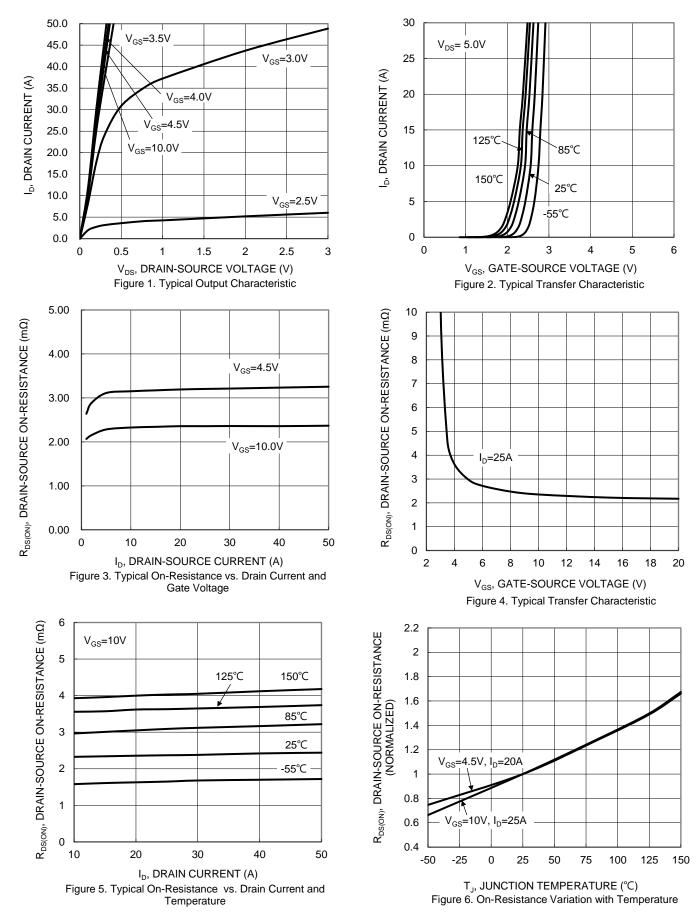


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	1 -						
Drain-Source Breakdown Voltage	BVDSS	60			V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS	_	—	1	μA	V _{DS} = 48V, V _{GS} = 0V	
Gate-Source Leakage	lgss	_		±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	Vgs(th)	1	_	3	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	Desser	_	2.5	3.1	mΩ	$V_{GS} = 10V, I_D = 25A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	3.3	4.5	mΩ	$V_{GS} = 4.5 V, I_D = 20 A$	
Diode Forward Voltage	Vsd	_	_	1.3	V	V _{GS} = 0V, I _S = 25A	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	_	5399	—	pF	$V_{DS} = 30V, V_{GS} = 0V$ f = 1MHz	
Output Capacitance	Coss	_	1306	—			
Reverse Transfer Capacitance	Crss	_	92	—			
Gate Resistance	Rg	_	0.64	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg	_	78.3	_			
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	38.5	_	nC	Vdd = 30V, Id = 25A	
Gate-Source Charge	Qgs		10.2	—	nc		
Gate-Drain Charge	Q _{gd}	_	20.4	—			
Turn-On Delay Time	td(on)	_	9.9	_			
Turn-On Rise Time	tR	_	17.7	_	ns	$\label{eq:VDD} \begin{array}{l} V_{\text{DD}} = 30V, \ V_{\text{GS}} = 10V \\ I_{\text{D}} = 25A, \ R_{g} = 3.5\Omega \end{array}$	
Turn-Off Delay Time	tD(OFF)	_	53.5	_			
Turn-Off Fall Time	tF		32.9	_			
Body Diode Reverse Recovery Time	trr		49.7	_	ns		
Body Diode Reverse Recovery Charge	Q _{RR}		78.9	_	nC	IF = 25A, dl/dt = 100A/µs	

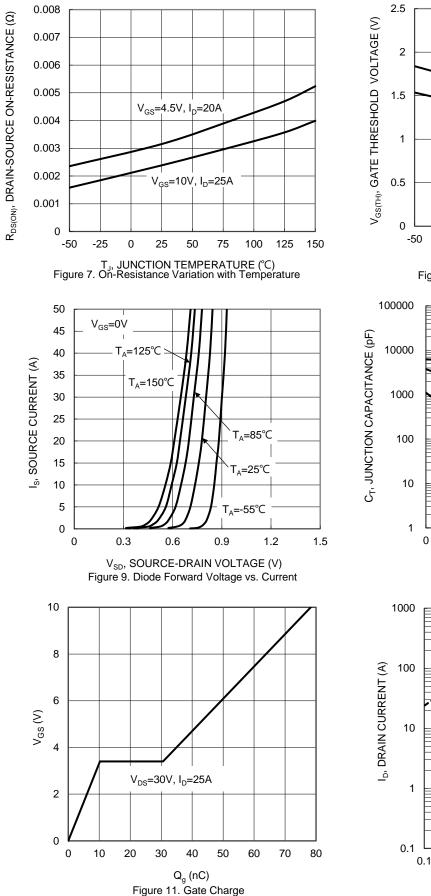
Notes: 8 .Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to production testing.





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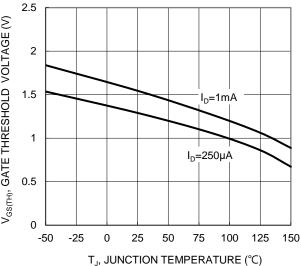


Figure 8. Gate Threshold Variation vs. Temperature

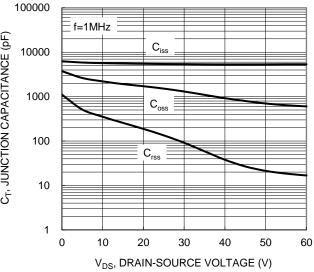
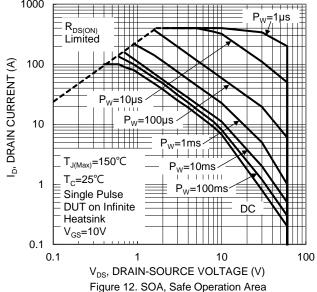
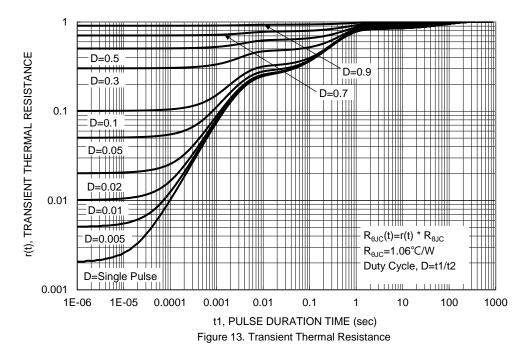


Figure 10. Typical Junction Capacitance



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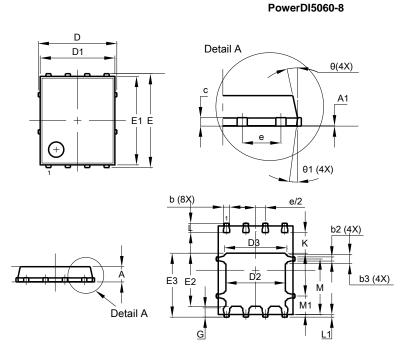




Package Outline Dimensions

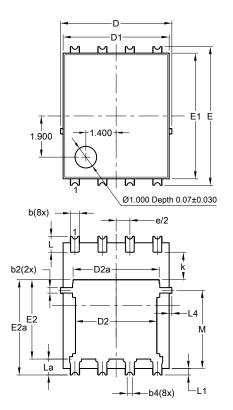
Please see http://www.diodes.com/package-outlines.html for the latest version.

Site 1:

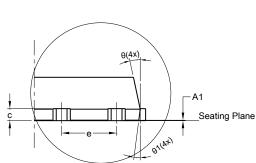


	PowerDI5060-8				
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0.00	0.05	-		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
c	0.230	0.330	0.277		
D		5.15 BSC			
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
ш	(6.15 BSC			
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
ĸ	0.51	-	-		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
М	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
Θ	10°	12°	11°		
Θ1	6°	8°	7°		
All Dimensions in mm					

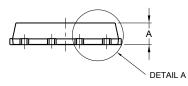
Site 2:



PowerDI5060-8/SWP (Type UX)



DETAIL A



PowerDI5060-8/SWP (Type UX)				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0	0.05		
b	0.30	0.50	0.41	
b2	0.20	0.35	0.25	
b4	(.25REF	-	
С	0.230	0.330	0.277	
D		.15 BS0	2	
D1	4.70	5.10	4.90	
D2	3.56	3.96	3.76	
D2a	3.78	4.18	3.98	
Е	6	.40 BS0	2	
E1	5.60	6.00	5.80	
E2	3.46	3.86	3.66	
E2a	4.195	4.595	4.395	
е	1	.27BSC)	
k	1.05			
L	0.635	0.835	0.735	
La	0.635	0.835	0.735	
L1	0.200	0.400	0.300	
L1a	0	.050RE	F	
L4	0.025	0.225	0.125	
М	3.205	4.005	3.605	
θ	10°	12°	11°	
θ1	6°	8°	7°	
All	All Dimensions in mm			

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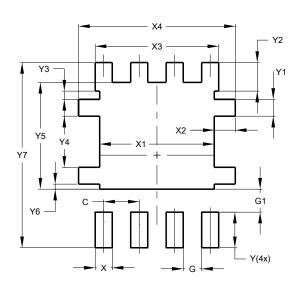


Suggested Pad Layout

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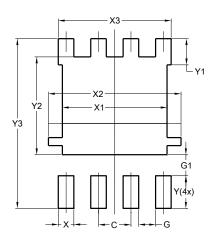
PowerDI5060-8



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

Site 2:

PowerDI5060-8/SWP (Type UX)



Value
(in mm)
1.270
0.660
0.820
0.610
4.100
5.190
4.420
1.270
1.020
3.810
6.610



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