PCN Number:		202	221216012.2					CN	Date:	December 21, 2022				
Title: Qu	alificatior	n of RF،	AB as an additional Fab site option						n for select LBC8 devices					
Customer Co	ntact:		PCN Manager						:	Quality Services				
Proposed 1 st	Ship Date	e:	Jun 2	20, 2023		Samp accep				Jan 20, 2023*				
*Sample requ		eived	after	January 2	0, 2023	will no	t be	e su	pported					
Change Type				1										
Assembly	Site			Assembly						nbly Materials				
Design				Electrical	-					anical Specification				
Test Site			닏	Packing/S			g	<u> </u>		Process				
Wafer Bu				Wafer Bu						Bump Process				
🛛 🛛 Wafer Fa	b Site			Wafer Fa					Wafer	Fab Process				
				Part num		-								
Description		-		PCI	l Deta	lis								
Description o				ounce the	aualifiaa	tion of	ita) fabrica	tion facility as an				
										ected" section.				
	Current F	ab Sit	е			Α	ddit	iona	al Fab S	ite				
Current Fat	Proc	cess	V	Vafer	New	ew Fab			ess	Wafer				
Site			Dia	meter	Site					Diameter				
MIHO8	LB	C8	20	00mm	RFA	В		LBC	8	300mm				
Qual details ar	e provide	d in the	e Qual	Data Sect	ion.									
Reason for Cl	nange:													
Continuity of s	upply.													
Anticipated in	npact on	Fit, Fo	orm, I	Function,	Quality	or Reli	abil	ity (positiv	e / negative):				
None.	•	-												
Changes to p	roduct id	entific	ation	resulting	from th	is PCN								
Fab Site Info														
Chip Site		Site O	riain (Code (20L)	Chin	Site Co	unt	ny C	ode (211) Chip Site City				
MIHO8					Criip		JPN			Ibaraki				
RFAB			MH8 RFB				US			Richardson				
Sample product TEXAS INSTRUMEN MADE IN: Mai 2DC: MSL '2 /260C, MSL 1 /235C, OPT: ITEM: LBL: 5A	TS Laysia 20: /1 YEAR S /UNLIM 0	EAL DT 3/29/04		ctual produ		(1P) S (Q) 2 (31T)1	00(KY () : 3 (1T	(D) 959047) 7523	0336 MLA 483512 099317 100 1054				
Product Affect	ted:													
ISO6731FQD	VRQ1	IS067	740FO	DWRQ1	ISO67	41FQD	NRO	1	ISO6	742FQDWRQ1				
ISO6731QDW	IS067	-	_		41QDW			-	ISO6742QDWRQ1					

Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines)

Approve Date 09-December-2022

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

	Туре	#	Tort Spor		SS / Lot	Test Name (Condition		Qual Device: ISO6741QDWRQ1	Qual Device: ISO6731QDWRQ1	Qual Device: ISO6740QDWRQ1	Qual Device: <u>ISO6742</u> Q <u>DWRQ1</u>	QBS Reference: <u>UCC23513</u> Q <u>DWYQ1</u>	QBS Reference: <u>ISO6741</u> Q <u>DWQ1</u>
÷	Test Group A	- Acceler	ated Environment !	Stress Te	ists									
	PC	A1	JEDEC J- STD-020 JESD22A113	3	77	Preconditioning	MSL2 260C	1 Step	-	-	-	-	No Fails	No Fails
	HAST	A2	JEDEC JESD22- A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	-	-	-	3/231/0	3/231/0
	ac/uhast	A3	JEDEC JESD22A102/JEDE JESD22A118	с 3	77	Autoclave	121C/15psig	96 Hours	-	-	-	-	3/231/0	3/231/0
	тс	A4	JEDEC JESD22A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	-	-	-	3/231/0	3/231/0
	HTSL	A6	JEDEC JESD22A103	1	45	High Temperature Storage Life	150C	1000 Hours	-	-	-	-	-	3/135/0
	HTSL	A6	JEDEC JESD22A103	1	45	High Temperature Storage Life	175C	500 Hours	-	-	-	-	3/135/0	-

Test Group B	st Group B - Accelerated Lifetime Simulation Tests												
HTOL	В1	JEDEC JESD22A108	1	77	Life Test	125C	1000 Hours	-	-	-	-	3/231/0	3/231/0
ELFR	B2	AEC Q100008	1	77	Early Life Failure Rate	125C	48 Hours	-	-	-	-	3/2400/0	-
Test Group C	est Group C - Package Assembly Integrity Tests												
WBS	C1	AEC Q100001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	-	-	-	3/90/0	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	-	-	-	3/90/0	3/90/0
SD	СЗ	JEDEC JESD22B102	1	15	PB Solderability	>95% Lead Coverage	-	-	-	-	-	1/15/0	1/15/0
SD	C3	JEDEC JESD22- B102	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-	-	-	1/15/0	1/15/0
PD	C4	JEDEC JESD22B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	1/10/0	-	-	-	3/30/0	3/30/0
Test Group D) - Die Fab	rication Reliability Te	sts										

EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
нсі	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Test Group E	st Group E - Electrical Verification Tests												
ESD	E2	AEC Q100002	1	3	ESD HBM	-	2000 Volts	1/3/0	-	-	-	1/3/0	1/3/0
ESD	E3	AEC Q100011	1	3	ESD CDM	-	500 Volts	1/3/0	-	-	-	1/3/0	1/3/0
LU	E4	AEC Q100004	1	6	Latch-Up	Per AEC Q100- 004	-	1/6/0	-	-	-	1/6/0	1/6/0
ED	E5	AEC Q100009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	1/30/0	-	1/30/0	1/30/0	3/90/0	3/90/0

QBS: Qual By Similarity

Devices gualified at MSL2 260C: ISO6741QDWRQ1, ISO6741FQDWRQ1, ISO6731QDWRQ1, ISO6731FQDWRQ1, ISO6740QDWRQ1, ISO6742QDWRQ1, ISO6742QDWRQ1, ISO6742FQDWRQ1

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40C to +150C

• Grade 1 (or O): -40C to +125C

Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hat/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

For questions regarding this notice, e-mails can be sent to the contact shown below or your local Field Sales Representative.

Location	E-Mail					
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