

# Product Change Notification - SYST-05PREF081

#### Date:

06 Aug 2019

#### Product Category:

Clock and Timing - Clock and Data Distribution

#### Affected CPNs:

7 🔁

#### Notification subject:

Data Sheet - SY100EL11V 5V/3.3V ECL 1:2 Differential Fanout Buffer

#### Notification text:

SYST-05PREF081 Microchip has released a new DeviceDoc for the SY100EL11V 5V/3.3V ECL 1:2 Differential Fanout Buffer of devices. If you are using one of these devices please read the document located at <u>SY100EL11V 5V/3.3V ECL 1:2 Differential Fanout Buffer</u>.

### Notification Status: Final

### **Description of Change**:

1) Updated minimum values for Common Mode Range voltage in PECL DC Electrical Characteristics table and NECL DC Electrical Characteristics table.

2) Updated two Conditions values for tSKEW in AC Electrical Characteristics table.

3) Minor stylistic updates to align data sheet with current style.

### Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

#### Change Implementation Status: Complete

### Date Document Changes Effective: 06 Aug 2019

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

## Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):

SY100EL11V 5V/3.3V ECL 1:2 Differential Fanout Buffer

Please contact your local <u>Microchip sales office</u> with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

SY100EL11VZG SY100EL11VZG-TR