PCN Number:	202	230208000.2		: Fo	ebruary 08, 2023						
Title: Qualification of RFAB as an additional Fab site option for select LBC8 devices											
Customer Co	ntact:	PCN Manager		Dept:		Quality Services					
Proposed 1 st S	Ship Date:	Aug 8, 2023		nple requests epted until:	Ma	Mar 8, 2023*					
*Sample requests received after March 8, 2023 will not be supported.											
Change Type:											
Assembly	Site	Assembl	y Process	☐ As	sembly	Materials					
Design			al Specification			al Specification					
Test Site			Shipping/Labe		Test Process						
☐ Wafer Bul		Wafer B Wafer F		Wafer Bump Process Wafer Fab Process							
	Site		ab Materials nber change	□ I VV	Wafer Fab Process						
		•	cation Deta	ile							
Description of	Change:	Hochik	acion beta	113							
•		o announce the	e qualification	of its RFAB fab	rication	facility as an					
		or the selected									
	Current Fab Sit	:e		Additional Fa	b Site						
Current Fab Site	Process	Wafer Diameter	New Fab Site	Process		Wafer Diameter					
MIHO8	LBC8	200 mm	RFAB	LBC8		300 mm					
Qual details are provided in the Qual Data Section.											
Reason for Ch	ange:										
Continuity of s	upply.										
Anticipated in	npact on Fit, F	orm, Function	, Quality or R	eliability (pos	itive /	negative):					
None.											
Changes to p	oduct identific	ation resulting	g from this PC	:N:							
J F											
Fab Site Infor											
Chip Site	Chip Site C	rigin Code (20L) Chip Site	Country Code	(21L)	Chip Site City					
MIHO8		MH8		JPN		Ibaraki					
RFAB		RFB		USA		Richardson					
TEXAS INSTRUMENT MADE IN: Mal	aysia 20: 1 YEAR SEAL DT UNLIM 03/29/0	4	(1P) (Q) (31T (4W) (P) (2P) (20L)	TKY(1T) 75	D) 033 047ML	A 3SI2 317 USA					
ISO6720BODD		721 PODDO1	ISO6721DB	ODBO1 IC	206721	DEPODDO1					

ISO6720FBQDRQ1

ISO6721FBQDRQ1

Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines)

Approve Date 26-January-2023

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: ISO6721BQDRQ1	Qual Device: ISO6720BQDRQ1	QBS Reference: ISO6721BQDRQ1	QBS Reference: UCC23513QDWYQ1
Test Group	Fest Group A - Accelerated Environment Stress Tests										
PC	A1	JEDEC J- STD-020 JESD22- A113	3	77	Preconditioning	MSL1 260C	1 Step		-	No Fails	-
PC	A1	JEDEC J- STD-020 JESD22- A113	3	77	Preconditioning	MSL2 260C	1 Step	-	-	-	No Fails
HAST	A2	JEDEC JESD22- A110	3	77	Biased HAST	130C/85%RH	96 Hours		-	3/231/0	3/231/0
AC/UHAST	А3	JEDEC JESD22- A102/JEDEC JESD22- A118	3	77	Autoclave	121C/15psig	96 Hours	-	-	3/231/0	3/231/0
тс	A4	JEDEC ESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	-	3/231/0	3/231/0
HTSL	A6	JEDEC JESD22- A103	1	45	High Temperature Storage Life	175C	500 Hours		-	3/135/0	3/135/0
Test Group	Test Group B - Accelerated Lifetime Simulation Tests										
HTOL	B1	JEDEC JESD22- A108	1	77	Life Test	125C	1000 Hours		-	3/231/0	3/231/0
ELFR	B2	AEC Q100- 008	1	77	Early Life Failure Rate	125C	48 Hours	-	-	-	3/2400/0
Test Group	Test Group C - Package Assembly Integrity Tests										
WBS	C1	AEC Q100- 001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	-	3/228/0	3/90/0

Туре	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device	Qual Device	QBS Reference	QBS Reference
Additional 1	Additional Tests										
ED	E5	AEC Q100- 009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	1/30/0	1/30/0	3/90/0	3/90/0
LU	E4	AEC Q100- 004	1	6	Latch-Up	Per AEC Q100-004	-	1/6/0	-	1/6/0	1/6/0
ESD	E3	AEC Q100- 011	1	3	ESD CDM	-	500 Volts	1/3/0	-	1/3/0	1/3/0
ESD	E2	AEC Q100- 002	1	3	ESD HBM	-	2000 Volts	1/3/0	-	1/3/0	1/3/0
Test Group	E - Elect	trical Verification	n Tests						· .	· ·	
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Test Group	D - Die F	abrication Relia	ability Te	sts							
PD	C4	JEDEC JESD22- B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	1/10/0	-	3/30/0	3/30/0
SD	С3	JEDEC J- STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-	1/15/0	1/15/0
SD	СЗ	JEDEC J- STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	-	1/15/0	1/15/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	-	3/228/0	3/90/0

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Ambient Operating Temperature by Automotive Grade Level:

- Grade 0 (or E): -40C to +150C
- Grade 1 (or Q): -40C to +125C
- Grade 2 (or T): -40C to +105C
- Grade 3 (or I): -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold : HTOL, ED
- Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

- QBS: Qual By Similarity
- Devices qualified at MSL2 260C: ISO6720BQDRQ1, ISO6720FBQDRQ1, ISO6721BQDRQ1, ISO6721FBQDRQ1, ISO6721RBQDRQ1, ISO6721RFBQDRQ1

Affected ZVEI IDs: SEM-PW-13, SEM-PW-02

For questions regarding this notice, e-mails can be sent to the contacts shown below or your local Field Sales Representative.

Location	E-Mail
WW Change Management Team	PCN ww admin team@list.ti.com

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