



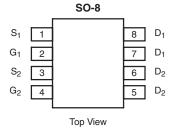
# N- and P-Channel 2.5-V (G-S) MOSFET

PRODUCT SUMMARY				
	V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	
N-Channel	20	$0.025$ at $V_{GS} = 4.5 \text{ V}$	7.1	
		$0.035$ at $V_{GS} = 2.5 \text{ V}$	6.0	
P-Channel	- 20	$0.033$ at $V_{GS} = -4.5 \text{ V}$	- 6.2	
r-Griannei	- 20	0.050 at V <sub>GS</sub> = - 2.5 V	- 5.0	

#### **FEATURES**

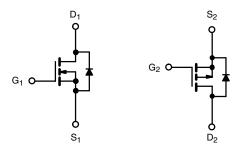
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET: 2.5 Rated
- · Compliant to RoHS directive 2002/95/EC





Ordering Information: Si4562DY-T1-E3 (Lead (Pb)-free)

Si4562DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted						
Parameter		Symbol	N-Channel	P-Channel	Unit	
Drain-Source Voltage		$V_{DS}$	20	- 20	V	
Gate-Source Voltage		$V_{GS}$	± 12		]	
Continuous Drain Current (T <sub>.I</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	I_	7.1	- 6.2		
Continuous Drain Current (1) = 150 °C)	T <sub>A</sub> = 70 °C	ID	5.7	- 4.9	A	
Pulsed Drain Current		I <sub>DM</sub>	40	- 40	1 ^	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	1.7	- 1.7		
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.0		w	
Maximum Fower Dissipation	T <sub>A</sub> = 70 °C	' D	1.3			
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 1	°C		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	N- or P-Channel	Unit		
Maximum Junction-to-Ambient <sup>a</sup>	R <sub>thJA</sub>	62.5	°C/W		

Notes:

a. Surface Mounted on FR4 board,  $t \le 10 \text{ s.}$ 

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Parameter	Symbol	Test Conditions		Min.	Тур.	Max.	Unit
Static			<u> </u>				
Gate Threshold Voltage	l	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	N-Ch	0.6		1.6	V
	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	P-Ch	- 0.6		- 1.6	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$	N-Ch			± 100	nA
			P-Ch			± 100	
Zero Gate Voltage Drain Current		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$	N-Ch			1	
		$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$	P-Ch			- 1	
	I <sub>DSS</sub>	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$	N-Ch			5	μΑ
		$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$	P-Ch			- 5	
On-State Drain Current <sup>b</sup>		$V_{DS} \ge 5 V$ , $V_{GS} = 4.5 V$	N-Ch	20			А
	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	P-Ch	- 20			
Drain-Source On-State Resistance <sup>b</sup>		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 7.1 A	N-Ch		0.019	0.025	Ω
	5	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 6.2 A	P-Ch		0.027	0.033	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 6.0 A	N-Ch		0.025	0.035	
		V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 5.0 A	P-Ch		0.040	0.050	
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 7.1 A	N-Ch		27		S
		V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 6.2 A	P-Ch		20		
h	V <sub>SD</sub>	I <sub>S</sub> = 1.7 A, V <sub>GS</sub> = 0 V	N-Ch			1.2	.,
Diode Forward Voltage <sup>b</sup>		I <sub>S</sub> = - 1.7 A, V <sub>GS</sub> = 0 V	P-Ch			- 1.2	V
Dynamic <sup>b</sup>	1						
			N-Ch		25	50	
Total Gate Charge	$Q_g$	N-Channel	P-Ch		22	35	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 7.1 \text{ A}$ $ P\text{-Channel} $ $V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -6.2 \text{ A} $	N-Ch		6.5		nC
			P-Ch		7		
Gate-Drain Charge			N-Ch		4		
			P-Ch N-Ch		3.5 40	60	
Turn-On Delay Time	t <sub>d(on)</sub>	N-Channel	P-Ch		27	50	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 10 $\Omega$	N-Ch		40	60	
		$I_D \cong 1 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 6 \Omega$	P-Ch		32	50	
Turn Off Dolay Time	t	P-Channel	N-Ch		90	150	
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{DD}$ = - 10 V, $R_L$ = 10 $\Omega$	P-Ch		95	150	ns
Fall Time	t <sub>f</sub>	$I_D\cong$ - 1 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 6 $\Omega$	N-Ch		40	60	
	1		P-Ch		45	70	
Sorce-Drain Reverse Recovery Tme	t <sub>rr</sub>	$I_F = 1.7 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$	N-Ch		40	80	
		$I_F = -1.7 \text{ A}, dI/dt = 100 \text{ A/}\mu\text{s}$	P-Ch		40	80	

#### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

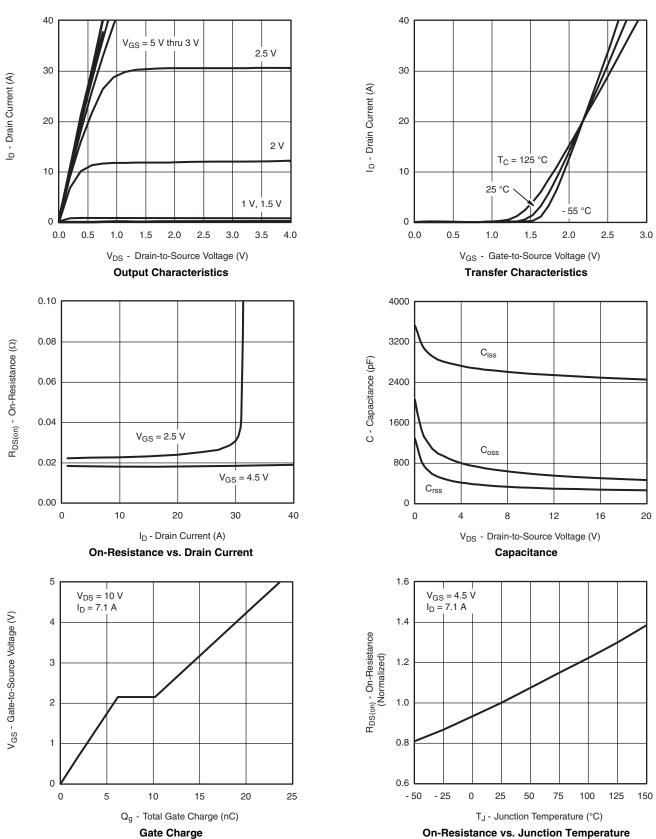
a. For design aid only; not subject to production testing. b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.







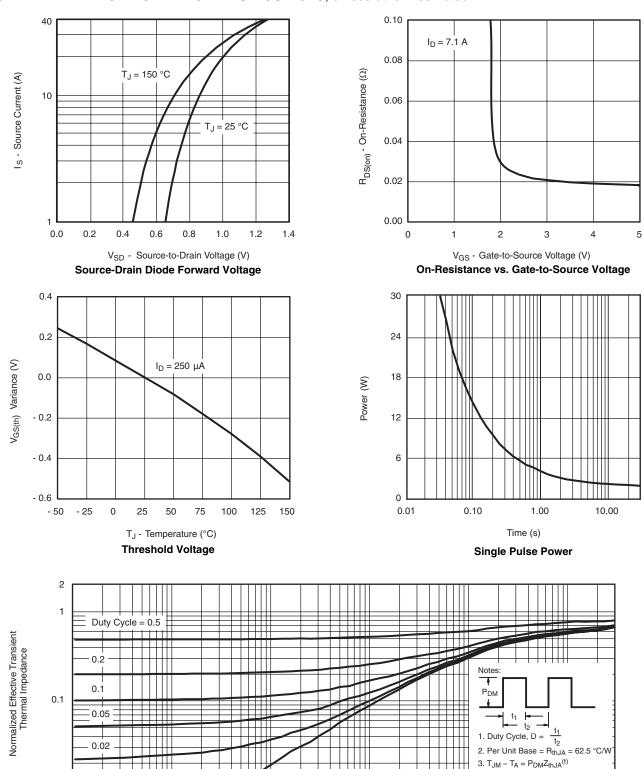
#### N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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#### N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Square Wave Pulse Duration (s)

Normalized Thermal Transient Impedance, Junction-to-Ambient

10<sup>-1</sup>

10<sup>-2</sup>

Single Pulse

10-3

0.01

10-4

30

10

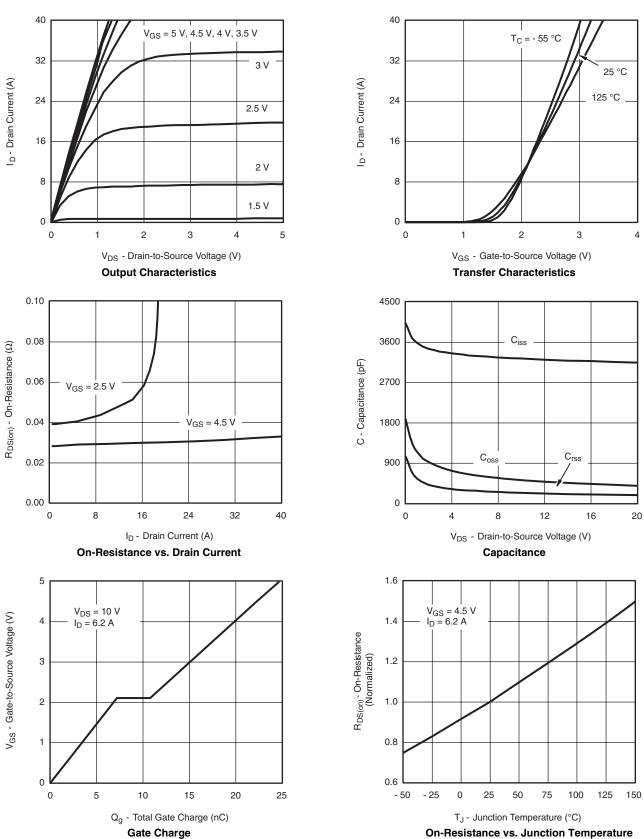
4. Surface Mounted







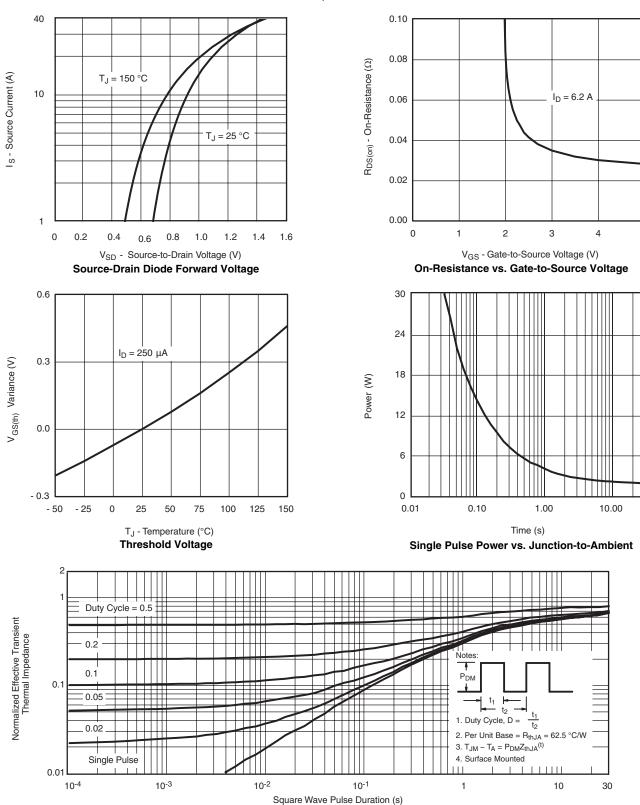
#### P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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#### P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

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