

PRODUCT/PROCESS CHANGE NOTIFICATION

PCN MMS-MIC/13/8160 Dated 18 Oct 2013

PDIP16 Transfer assembly site from ST Longgang (China) to Amkor ATP1 (Philippines)

Table 1. Change Implementation Schedule

Forecasted implementation date for change	23-Jan-2014
Forecasted availability date of samples for customer	13-Dec-2013
Forecasted date for STMicroelectronics change Qualification Plan results availability	23-Dec-2013
Estimated date of changed product first shipment	23-Jan-2014

Table 2. Change Identification

Product Identification (Product Family/Commercial Product)	PDIP 16 package products
Type of change	Package assembly location change
Reason for change	ST PDIP production rationalization
Description of the change	PDIP 16 package assembly will be transferred from ST Longgang (China) to Amkor ATP1 (Philippines). Bill of Materials is modified as described below.
Change Product Identification	see indicated below
Manufacturing Location(s)	

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Table 3. List of Attac	hments
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Customer Part numbers list	
Qualification Plan results	

Customer Acknowledgement of Receipt	PCN MMS-MIC/13/8160
Please sign and return to STMicroelectronics Sales Office	Dated 18 Oct 2013
□ Qualification Plan Denied	Name:
□ Qualification Plan Approved	Title:
	Company:
☐ Change Denied	Date:
□ Change Approved	Signature:
Remark	
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DOCUMENT APPROVAL

Name	Function
Colonna, Daniel	Marketing Manager
Buffa, Michel	Product Manager
Narche, Pascal	Q.A. Manager

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PRODUCT/PROCESS CHANGE NOTIFICATION

PDIP16 Transfer assembly site from ST Longgang (China) to Amkor ATP1 (Philippines)

MMS - Microcontrollers Division (MCD)

Dear Customer,

In order to sustain the demand on products assembled in PDIP 16 package, ST General Purpose microcontroller Division (MCD) is announcing the transfer of PDIP 16 package assembly site from ST Longgang (China) to Amkor ATP1 (Philippines).

What is the change?

PDIP 16 package assembly will be transferred from ST Longgang (China) to Amkor ATP1 (Philippines). New Bill of Materials is described in the below table:

Assembly site	ST Longgang (China)	Amkor ATP1 (Philippines)
Glue	ABLESTIK QMI168	ABLESTIK 8390
Wire	Gold 1.0 Mil	Gold 1.0 Mil
Molding compound	KCC KTMC1000 1030SL	Panasonic CK5000A
Lead Finishing	NiPdAu (e4)	Mat Tin (e3)

PDIP 16 package assembled in Amkor ATP1 (Philippines) remains compliant with the JEDEC standard.

Why?

ST PDIP production is rationalized. Consequently, PDIP 16 package will be permanently transferred to new assembly site (Amkor ATP1), in order to guaranty the product continuation.

When?

The production on the new assembly site will start Week 04 2014.

How will the change be qualified?

This change will be qualified using the standard STMicroelectronics Corporate Procedures for Quality and Reliability, in full compliancy with the JESD-47 international standard. You can find below Qualification Plan.

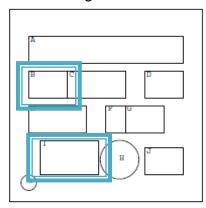
What is the impact of the change?

Form: no changeFit: no changeFunction: no change

How can the change be seen?

Traceability of the change is ensured by ST internal tools.

The marking instruction indicated on the products is changing from:



B: Assembly plant change from G4 to 7B

I : Country Of Origin change from CHN to PHL

We remain available to discuss any concern that you may have regarding this Product Change Notification.

With our sincere regards.

Michel Buffa

Microcontroller Division General Manager



RERMCD 1314 RELIABILITY PLAN

Qualification of:

PDIP16 transfer for ST6/ST7 legacy devices

Qualification Reference: RERMCD1314

Issued on: Sept 24, 2013

Assembly Plant: AMKOR Philippines

Test Plant: ST LGG

Assembly Line: PDIP

Devices: ST6/ST7

Package / Process: PDIP16



Purpose

Qualification of new Assembly site for PDIP 16 package.

Test & Finishing plant for PDIP16 is unchanged.

Test Vehicles:

Device/ Die	Number of Lots		
•ST62T01/ die 221	1		
•ST6201/ die 201	1		
•ST7FLITE/ die 819	1		

Package Reliability Trials:

Reliability	Trial	Test Conditions	Pass Criteria	Unit per Lot	Lot nb
AC	Autoclave	121°C, 100% RH, 2 Atm	96h	77	3
	JESD22 A102				
	Thermal Cycling	-65°C, +150°C	500cy	77	3
TC	JESD22 A104				
THS/	Temperature Humidity	85°C, 85% RH, no bias/ No bias	1000h	77	3
THB	Storage or Biased				
	JESD22 A110- A101				
	High Temperature Storage	150°C- no bias	1000h,	77	3
HTSL	Life				
	JESD22 A103				

Reliability tests description

Package oriented tests/ Trials description

1. Autoclave (AC)

The device is stored in saturated steam, at fixed and controlled conditions of pressure and temperature.

Purpose: to investigate corrosion phenomena affecting die or package materials, related to chemical contamination and package hermeticity.

To point out critical water entry paths with consequent electrochemical and galvanic corrosion.

2. Temperature Cycling (TC)

The device is submitted to cycled temperature excursions, between a hot and a cold chamber in air atmosphere (thermal gradient typical 10 C/min).



Purpose: to investigate failure modes related to the thermo-mechanical stress induced by the different thermal expansion of the materials interacting in the die-package system.

Typical failure modes are linked to metal displacement, dielectric cracking, moulding compound delamination, wire-bonds failure, die-attach layer degradation.

3. Temperature Humidity Bias (THB)

The device is biased in static configuration minimizing its internal power dissipation, and stored at controlled conditions of ambient temperature and relative humidity.

The Temperature Humidity Bias follows the same method than HAST at lower temperature.

Purpose: to investigate failure mechanisms activated in the die-package environment by electrical field and wet conditions.

Typical failure mechanisms are electro-chemical corrosion and surface effects related to the molding compound.

The package moisture resistance with electrical field applied is verified, both electrolytic and galvanic corrosion are put in evidence.

Conditions:

- > Ta=85°C; R.H.=85%;
- \triangleright Power supply voltage less or equal to max operative voltage to not exceed T_i = 95 °C.

4. Temperature Humidity Storage (THS)

The Temperature Humidity Storage is stored at controlled conditions of high temperature and relative humidity.

The Temperature Humidity Storage follows the same method than Unbiased HAST at lower temperature.

Purpose: to evaluate the reliability of non-hermetic packaged solid-state devices in humid environments. It is a highly accelerated test which employs temperature and humidity under non-condensing conditions to accelerate the penetration of moisture through the external protective material (encapsulant or seal) or along the interface between the external protective material and the metallic conductors which pass through it.

Bias is not applied in this test to ensure the failure mechanisms potentially overshadowed by bias can be uncovered (e.g. galvanic corrosion). This test is used to identify failure mechanisms internal to the package.

- > Test conditions: 85°C / 85% RH.
- ➤ No power supply

5. High Temperature Storage Life (HTSL)

The device is stored in unbiased condition at the max. temperature allowed by the package materials, sometimes higher than the max. operative temperature.

Purpose: to investigate the failure mechanisms activated by high temperature, typically wire-bonds solder joint ageing, data retention faults, metal stress-voiding.

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