

Information note

N° 006/22

Dear customer,

With this Infineon Technologies AG information note, we would like to inform you about the following

Data sheet update for products IPZ40N04Sx, IAUZ40N06Sx, IAUC60N06Sx and IAUC41N06S5N102



On 16 April 2020, Infineon acquired Cypress.
We are now in the process of merging and consolidating our tools and processes for PCN, Information Notes, Errata and Product Discontinuance.
For further details, please visit our website:
<https://www.infineon.com/cms/en/about-infineon/company/cypress-acquisition/>

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Information note

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► Products affected

Please refer to attached affected product list
“INF_006-22_[customer-no].pdf”

► Detailed change information

Subject Data sheet update for products IPZ40N04Sx, IAUZ40N06Sx, IAUC60N06Sx and IAUC41N06S5N102

Reason Correction of Pin layout for all affected products on page 1 in data sheet and update of R_thJA value for IAUZ40N06S5N105, IPZ40N04S5-3R9, IPZ40N04S5L-3R6, IAUZ40N06S5L050

| Description | Old | New |
|-------------|-----------------------------|-----------------------------|
| Data Sheet | ■ IAUC41N06S5N102: Rev. 1.0 | ■ IAUC41N06S5N102: Rev. 1.1 |
| Data Sheet | ■ IAUC60N06S5N074: Rev. 1.0 | ■ IAUC60N06S5N074: Rev. 1.1 |
| Data Sheet | ■ IAUC60N06S5L073: Rev. 1.1 | ■ IAUC60N06S5L073: Rev. 1.2 |
| Data Sheet | ■ IAUZ40N06S5L050: Rev. 1.2 | ■ IAUZ40N06S5L050: Rev. 1.3 |
| Data Sheet | ■ IAUZ40N06S5N105: Rev. 1.1 | ■ IAUZ40N06S5N105: Rev. 1.2 |
| Data Sheet | ■ IPZ40N04S5-3R9: Rev. 1.1 | ■ IPZ40N04S5-3R9: Rev. 1.2 |
| Data Sheet | ■ IPZ40N04S5L-3R6: Rev. 1.2 | ■ IPZ40N04S5L-3R6: Rev. 1.3 |

► Product identification

Not applicable (for documentation only)

► Impact of change

Not applicable (for documentation only)

Information note

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► **Attachments**

| | |
|---------------------------------------|------------------------------|
| INF_006-22_[<i>customer-no</i>].pdf | affected product list |
| 3_cip00622 | customer information package |
| 4_cip00622 | data sheets |

► **Intended start of delivery**

Not applicable (for documentation only)

If you have any questions, please do not hesitate to contact your local sales office.

Data sheet update for products IPZ40N04Sx, IAUZ40N06Sx, IAUC60N06Sx and IAUC41N06S5N102

Customer Information Package
Info Note 006/22



Agenda

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| 2 | Change overview | 5 |
| 3 | Change details | 7 |
| 4 | Conclusion | 11 |

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Motivation of the change

› Update of data sheet:

- The image of pin layout for package provided on page 1 of data sheet does not correspond to the defined counter clockwise marking of pins (pin 5 to 8)
- Update value for typically thermal resistance junction to ambient in chapter 'Thermal characteristics' on page 4

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Change over view of datasheet

| Description | Old | New |
|-----------------|----------|----------|
| IAUC41N06S5N102 | Rev. 1.0 | Rev. 1.1 |
| IAUC60N06S5N074 | Rev. 1.0 | Rev. 1.1 |
| IAUC60N06S5L073 | Rev. 1.1 | Rev. 1.2 |
| IAUZ40N06S5L050 | Rev. 1.2 | Rev. 1.3 |
| IAUZ40N06S5N105 | Rev. 1.1 | Rev. 1.2 |
| IPZ40N04S5-3R9 | Rev. 1.1 | Rev. 1.2 |
| IPZ40N04S5L-3R6 | Rev. 1.2 | Rev. 1.3 |

Change Details

| | | |
|---|--------------------------|----------|
| 1 | Motivation of the change | 3 |
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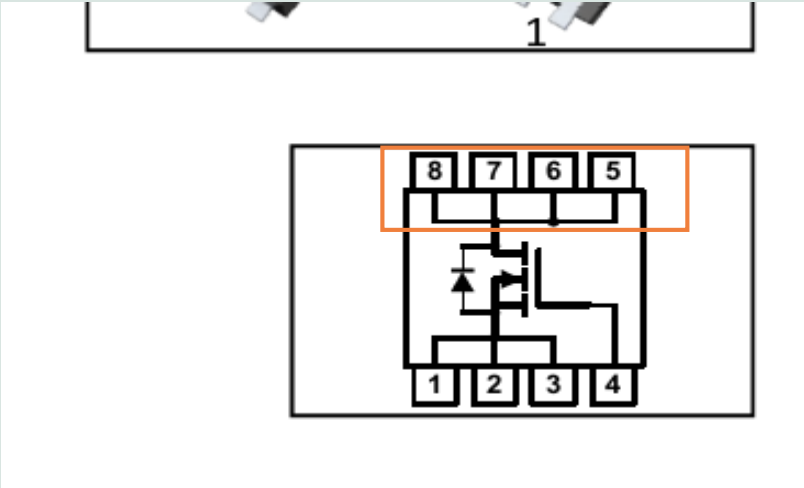
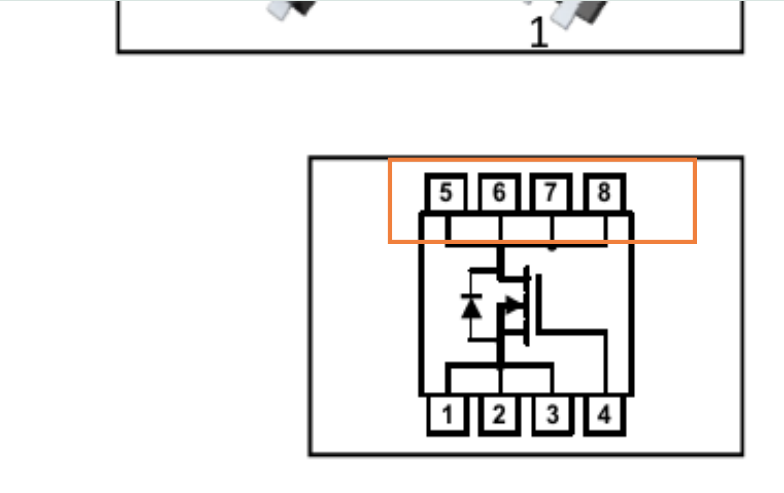
Change details for pin layout of the package – data sheet page1

Old

New

Pin layout number on the Top are mentioned Left to right (5 6 7 8) for SSO8 & S3O8

Pin layout number on the Top are mentioned right to left (8 7 6 5) for SSO8 & S3O8



Change details for Thermal resistance junction to ambient – data sheet page 4



| Description | Old | New | | | | | | | | | | |
|--|--|--|------------|------|---|------|--|--|------------|---|---|------|
| IAUZ40N06S5L050 | <table border="1"> <tr> <td>Thermal resistance, junction - ambient⁴⁾</td> <td>R_{thJA}</td> <td>-</td> <td>-</td> <td>61.8</td> </tr> </table> | Thermal resistance, junction - ambient ⁴⁾ | R_{thJA} | - | - | 61.8 | <table border="1"> <tr> <td>Thermal resistance, junction - ambient⁴⁾</td> <td>R_{thJA}</td> <td>-</td> <td>-</td> <td>38.5</td> </tr> </table> | Thermal resistance, junction - ambient ⁴⁾ | R_{thJA} | - | - | 38.5 |
| Thermal resistance, junction - ambient ⁴⁾ | R_{thJA} | - | - | 61.8 | | | | | | | | |
| Thermal resistance, junction - ambient ⁴⁾ | R_{thJA} | - | - | 38.5 | | | | | | | | |
| IAUZ40N06S5N105 | <table border="1"> <tr> <td>Thermal resistance, junction - ambient⁴⁾</td> <td>R_{thJA}</td> <td>-</td> <td>-</td> <td>63.4</td> </tr> </table> | Thermal resistance, junction - ambient ⁴⁾ | R_{thJA} | - | - | 63.4 | <table border="1"> <tr> <td>Thermal resistance, junction - ambient⁴⁾</td> <td>R_{thJA}</td> <td>-</td> <td>-</td> <td>40</td> </tr> </table> | Thermal resistance, junction - ambient ⁴⁾ | R_{thJA} | - | - | 40 |
| Thermal resistance, junction - ambient ⁴⁾ | R_{thJA} | - | - | 63.4 | | | | | | | | |
| Thermal resistance, junction - ambient ⁴⁾ | R_{thJA} | - | - | 40 | | | | | | | | |
| IPZ40N04S5-3R9 | <table border="1"> <tr> <td>Thermal resistance, junction - ambient⁴⁾</td> <td>R_{thJA}</td> <td>-</td> <td>-</td> <td>62.4</td> </tr> </table> | Thermal resistance, junction - ambient ⁴⁾ | R_{thJA} | - | - | 62.4 | <table border="1"> <tr> <td>Thermal resistance, junction - ambient⁴⁾</td> <td>R_{thJA}</td> <td>-</td> <td>-</td> <td>39.5</td> </tr> </table> | Thermal resistance, junction - ambient ⁴⁾ | R_{thJA} | - | - | 39.5 |
| Thermal resistance, junction - ambient ⁴⁾ | R_{thJA} | - | - | 62.4 | | | | | | | | |
| Thermal resistance, junction - ambient ⁴⁾ | R_{thJA} | - | - | 39.5 | | | | | | | | |
| IPZ40N04S5L-3R6 | | | | | | | | | | | | |

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Conclusion

- › Data sheet update regarding image of pin layout for package on page 1 due to typo and do not worsen the technical or electrical device behavior because the electrical potential at all pins 5 to 8 of the package is the same (MOSFET drain)
- › Data sheet update regarding parameter Thermal resistance junction to ambient on page 4 due to typo and do not worsen the technical or electrical device behavior because value for R_{thJA} is decreased



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