

RoHS

COMPLIANT

HALOGEN

Available

Vishay Siliconix

N-Channel 30-V (D-S) MOSFET

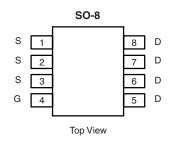
PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)			
30	0.00525 at V _{GS} = 10 V	20			
	0.007 at V_{GS} = 4.5 V	17			

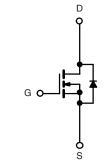
FEATURES

- Halogen-free According to IEC 61249-2-21
 Available
- TrenchFET[®] Power MOSFET
- Optimized for "Low Side" Synchronous Rectifier Operation
- 100 % Rg Tested

APPLICATIONS

- DC/DC Converters
- Synchronous Rectifiers





N-Channel MOSFET

Ordering Information: Si4858DY-T1-E3 (Lead (Pb)-free)					
Si4858DY-T1-GE3 (Lead (Pb)-free and Halogen-free)					

ABSOLUTE MAXIMUM RATINGS	$T_A = 25 \ ^{\circ}C$, unless	ss otherwise n	oted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^a$	T _A = 25 °C	– I _D	20	13	٨
	T _A = 70 °C		15	10	
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	60		A
Continuous Source Current (Diode Conduction) ^a		۱ _S	2.9	1.3	
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	3.5	1.6	W
	T _A = 70 °C		2.2	1	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	- R _{thJA} R _{thJF}	29	35	
Maximum Junction-to-Ambient ^a	Steady State		67	80	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		13	16	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			•				
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.0			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 24 V, V_{GS} = 0 V$			1	μA	
		V_{DS} = 24 V, V_{GS} = 0 V, T_{J} = 55 °C			5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	30			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		0.0040	0.00525	-	
		V _{GS} = 4.5 V, I _D = 19 A		0.0055	0.007	Ω	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		90		S	
Diode Forward Voltage ^a	V _{SD}	$I_{\rm S}$ = 2.9 A, $V_{\rm GS}$ = 0 V		0.75	1.1	V	
Dynamic ^b	L						
Total Gate Charge	Qg			30.5	40		
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 20 \text{ A}$		13.5		nC	
Gate-Drain Charge	Q _{gd}			9.5			
Gate Resistance	R _g		0.5	1.4	2.4	Ω	
Turn-On Delay Time	t _{d(on)}			21	35		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		10	20	ns	
Turn-Off Delay Time	t _{d(off)}	$\rm I_D \cong 1$ A, $\rm V_{GEN}$ = 10 V, $\rm R_g$ = 6 Ω		83	130		
Fall Time	t _f			27	45		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 2.9 A, dI/dt = 100 A/μs		50	80		

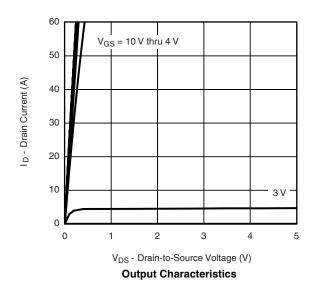
Notes:

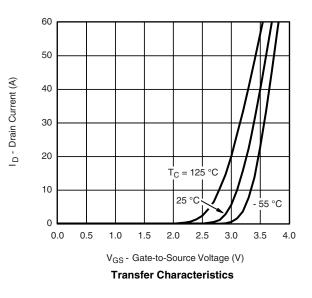
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



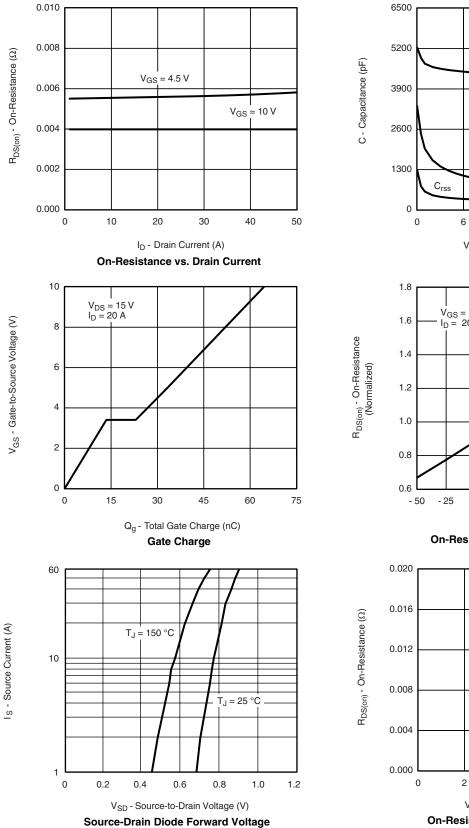


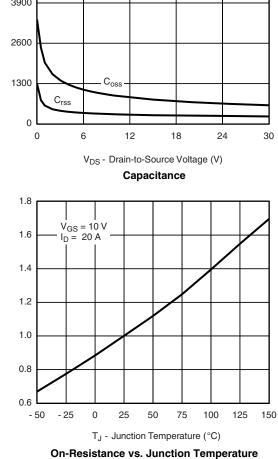


Si4858DY

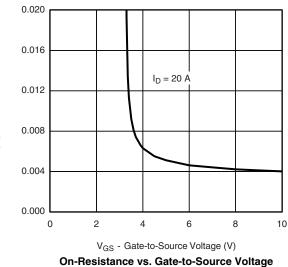
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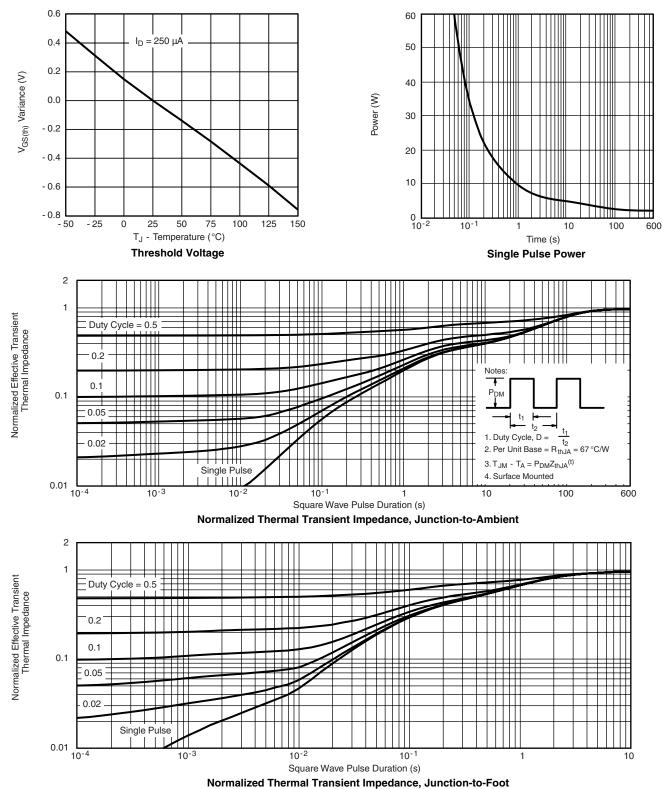


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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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