

Product Change Notification - SYST-27ZFOZ607

Date:

30 Nov 2018

Product Category:

Ethernet Switches

Affected CPNs:

Notification subject:

 Data Sheet - KSZ9897S 7-Port Gigabit Ethernet Switch with SGMII and RGMII/MII/RMII Interface
 Data Sheet Document Revision

Notification text:

SYST-27ZFOZ607

 Microchip has released a new DeviceDoc for the KSZ9897S 7-Port Gigabit Ethernet Switch with SGMII and RGMII/MII/RMII Interface of devices. If you are using one of these devices please read the document located at [KSZ9897S 7-Port Gigabit Ethernet Switch with SGMII and RGMII/MII/RMII Interface](#) .

Notification Status: Final

Description of Change: Below are the changes

Section/Figure/Entry	Correction
Section 8.1, "Package Mark- ing Information," on page 195	Updated top marking information.
Section 8.2, "Package Draw- ings," on page 196	Updated package drawings.
Table 4-16, "Matching Rule Options"	Table updated.
Section 4.4.9, "Tail Tagging Mode," on page 38	Section updated.
Section 4.1.9, "LinkMD® Cable Diagnostics," on page 22	LinkMD details added.
Section 4.4.2.4, "Learning," on page 29	Text correction.
Section 4.4.2.6, "Aging," on page 29	Corrected “time stamp” to “age count” in multiple locations.
Section 5.2.2.5, "PHY Auto- Negotiation Advertisement Register," on page 115	Changed default value of Pause (Flow Control) Capability bit to a note referencing the LED1_1 configuration strap.
Section 5.2.2.10, "PHY 1000BASE-T Control Regis- ter," on page 119	Corrected bit 10 default value. Added information on Test Mode Bits 15:13.
Section 5.2.8.4, "Port Authentication Control Reg- ister," on page 145	Corrected bits 1:0 description.
Section 5.2.2.16, "PHY LinkMD Register," on page 122	Updated register bit descriptions.
Section 5.1.1.4, "Global Chip ID 3 Register," on page 67	Corrected bit 0 description.
Section 5.4, "MDIO Manage- able Device (MMD) Regis- ters (Indirect)," on page 168	Corrected the MMD register read example.
Table 6-2, "RGMII Timing Values," on page 181	Revised minimum RGMII TSKEW parameter.
Table 3-3, "“Configuration	Corrected swapping of LED2_0 and LED4_0, added

Strap Descriptions, on page 16	notes in strapping. Corrected RXD6_0 and RXD7_0 in strapping table.
Table 1-3, "Register Nomenclature," on page 7	Added additional W0C "Write zero to clear" bit type.
Section 5.5.1, "SGMII Control Register," on page 170	Corrected defaults and bit types. Added details and updated bit names.
Section 5.5.2, "SGMII Status Register," on page 171	Corrected defaults and bit types. Updated Link Status description.
Section 5.5.5, "SGMII Auto-Negotiation Advertisement Register," on page 172	Added additional description.
Section 5.5.6, "SGMII Auto-Negotiation Link Partner Base Ability Register," on page 173, Section 5.5.7, "SGMII Auto-Negotiation Expansion Register," on page 174	Added new register definitions.
Section 5.5.8, "SGMII Digital Control Register," on page 174, Section 5.5.9, "SGMII Auto-Negotiation Control Register," on page 175, Section 5.5.10, "SGMII Auto-Negotiation Status Register," on page 176	Added additional description.
Section 5.1.3.1, "Power Down Control 0 Register," on page 75	Added SGMII-specific information to bits 4:3 description.
Section 5.2.1.5, "Port Interrupt Status Register," on page 109, Section 5.2.1.6, "Port Interrupt Mask Register," on page 109	Updated bit 3.
Section 2.1, "General Description," on page 8, Section 4.11.4, "Serial Gigabit Media Independent Interface (SGMII) (Port 7)," on page 62	Updated SGMII description.

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 30 Nov 2018

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):

[KSZ9897S 7-Port Gigabit Ethernet Switch with SGMII and RGMII/MII/RMII Interface](#)



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Affected Catalog Part Numbers (CPN)

KSZ9897STXC

KSZ9897STXC-TR

KSZ9897STXI

KSZ9897STXI-TR