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# PRODUCT INFORMATION NOTIFICATION

Subject: Datasheet Updates to the Enable Pins of PMIC S6BP501A/502A Products

To:

**Change Type:** Minor

#### **Product Information:**

This notification is to inform customers of updates to the Enable pins in the S6BP501A/502A datasheets.

The datasheet update recommends pins 26 (EN1V) and 27 (EN3V) to be tied together in the customer application so they are controlled by the same signal instead of being controlled separately.

If pins 26 and 27 are not connected in the customer application, the outputs of the PMIC may occasionally restart in the EN3V state transition.

### Changes to S6BP501A/502A datasheets:

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Page / Section	Parameters	From	То		
P5 / Table 3-1	Pins 26, 27	-	Connect together		
P14 / Figure 8-1	$V_{\text{EN1V}}, V_{\text{EN3V}}$	Separate line	$V_{\text{EN1V}} = V_{\text{EN3V}}$		
P17 / Table 8-3	EN1V, EN3V	Separate setting	EN1V=EN3V		
P18 / Table 8-4	EN1V, EN3V	Separate setting	EN1V=EN3V		
P19 / Figure 9-1	EN1V, EN3V	Separate line	EN1V=EN3V		
P24 / 11. Reference Data	-	-	Some data are measured under the condition of separated EN1V and EN3V. for reference. However, EN1V and EN3V are recommended to be connected together.		

### Part Numbers Affected: 5

See the attached 'Affected Parts List' file for a list of all part numbers affected by this change. Note that any new parts that are introduced after the publication of this PIN will include all changes outlined in this PCN.

#### **Approximate Implementation Date:**

Revised datasheets are now live on <a href="https://www.cypress.com">https://www.cypress.com</a>. Please visit the corresponding product page (S6BP50x Three-channel Instrument Cluster PMICs) to download the latest copy.

## **Anticipated Impact:**

There is no change to the product. Products manufactured with revised datasheet parameters are completely compatible with existing product from form, fit, functional, parametric, and quality performance perspectives.

Cypress also recommends that customers take this opportunity to review these changes against current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

#### Method of Identification:

Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

# **Response Required:**

This is an information only announcement. No response is required

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at <a href="mailto:pcn\_adm@cypress.com">pcn\_adm@cypress.com</a>.

Sincerely,

Cypress PCN Administration

Item	Marketing Part Number	Family
1	7GA420197	PMIC
2	S6BP501A00SN2B000	PMIC
3	S6BP501A00SN2B200	PMIC
4	S6BP502A00SN2B000	PMIC
5	S6BP502A00SN2B200	PMIC