	N Number: 20200129003	PCN Date:	Feb	<u>4, 2020</u>				
Tit	Title: Datasheet for ISOW7821							
Cu	stomer Contact: PCN Manager		De	ept:	Quality Services			
Ch	ange Type:							
	Assembly Site Design	ın		Wafer	Bump Site			
	Assembly Process	Sheet		Wafer	Bump Material			
	Assembly Materials Part	number change		Wafer	Bump Process			
	Mechanical Specification Test	Site		Wafer	Fab Site			
	Packing/Shipping/Labeling Test	Process		Wafer	Fab Materials			
				Wafer	Fab Process			
Notification Details								
Description of Change:								
	xas Instruments Incorporated is announcir	•	•	otificatio	on.			
The product datasheet(s) is being updated as summarized below.								
	e following change history provides furthe	r details.						
ŢEXAS ISOW7821								
INSTRUMENTS SLLSF40B – NOVEMBER 2017 – REVISED SEPTEMBER 2019								
Ch	anges from Revision A (March 2018) to Revision B	SEEDI	100-110	V LINDLIN 20	Page			
	The second secon							
•	Made editorial and cosmetic changes throughout the d							
•	Added "Robust Isolation Barrier" bullet in Features							
•	Added ">100-Year Projected Lifetime at 1 kV _{RMS} Working Voltage" bullet in Features							
•	Added "Up to 5000 V _{RMS} Isolation Rating" bullet in Features							
•	Added "Up to 10 kV _{PK} Surge Capability" bullet in Features							
•	Added "±8 kV IEC 61000-4-2 Contact Discharge Protection across Isolation Barrier" bullet in Features							
•	Changed VDE standard name From: DIN V VDE V 0884-11:2017-01 To: DIN VDE V 0884-11:2017-01 in Features and Insulation Specifications table							
•	Added "EN 62368-1" standard in TUV certification bullet in Features							
•	Deleted "All Agency Certifications are Planned" bullet in Features							
•	Updated Simplified Schematic to show two isolation capacitors in series instead of a single capacitor for signal isolation channels							
•	Added "Contact discharge per IEC 61000-4-2; Isolation barrier withstand test" specification of ±8000 in ESD Ratings table6							
•	Added table note "IEC ESD strike is applied across the barrier with all pins on each side tied together creating a two-terminal device" to ESD Ratings table							
•	Deleted "TJ or Junction temperature" parameter from Recommended Operating Conditions table as it is already specified in Absolute Maximum Ratings table							
•	Added "See Figure 34" to TEST CONDITIONS of V _{IOWM} specification							
	Updated Safety-Related Certifications table							
•	Added the following note to Figure 25: "Optional 100 µF capacitor can be added between V _{CC} and GND1; refer to Power Supply Recommendations"							
•	Added the following note to Figure 30: "Optional 100 µF capacitor can be added between V _{CC} and GND1; refer to Power Supply Recommendations"							
	Added the following text to Design Requirements: "Optional 100 µF decoupling capacitor can be added between V _{CC} and GND1 pins; refer to <i>Power Supply Recommendations</i> for more details"							
	Added text to Power Supply Recommendations section to emphasise that input decoupling capacitor should be larger than output capacitor by at least 100 µF							
	Added the following note to Figure 35: "Optional 100 µF capacitor can be added between V _{CC} and GND1; refer to Power Supply Recommendations"							
_								
	e datasheet number will be changing.	T						
	evice Family	Change From:		Change	e To:			
I	SOW7821	SLLSF40A		SLLSF4	10B			
These changes may be reviewed at the datasheet links provided.								
http://www.ti.com/product/ISOW7821								
Reason for Change:								
Tο	accurately reflect device characteristics.							

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):									
No anticipated impact. This is a specification change announcement only. There are no changes									
to the actual device.									
Changes to product identification resulting from this PCN:									
None.									
Product Affected:									
ISOW7821DWE	ISOW7821DWER	ISOW7821FDWE	ISOW7821FDWER						

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