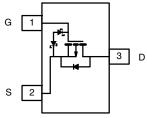




P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$ Max.	I _D (A) ^a	Q _g (Typ.)		
	0.045 at $V_{GS} = -10 \text{ V}$	- 4.8			
- 30	0.053 at V _{GS} = - 4.5 V	- 4.4	10.6 nC		
	0.080 at V _{GS} = - 2.5 V	- 3.6			





Top View Si2371EDS (E6)* * Marking Code

Ordering Information:

Si2371EDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

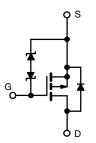
- TrenchFET® Power MOSFET
- 100 % R_g Tested
- Built-in ESD Protection
 - Typical ESD Performance 3000 V
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



HALOGEN FREE

APPLICATIONS

- Power Management for Portable and Consumer
 - Load Switches
 - OVP (Over Voltage Protection) Switch



P-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V_{DS}	- 30	V		
Gate-Source Voltage	V_{GS}	± 12	_ v		
	T _C = 25 °C		- 4.8		
Continuous Drain Current (T ₁ = 150 °C)	T _C = 70 °C	I_	- 3.8		
Continuous Diain Current (1) = 150 °C)	T _A = 25 °C	I _D	- 3.7 ^{b,c}		
	T _A = 70 °C		- 2.9 ^{b,c}	Α	
Pulsed Drain Current (t = 300 μs)	I _{DM}	- 20			
Continuous Source-Drain Diode Current	T _C = 25 °C	I.	- 1.4		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	- 1 ^{b,c}		
	T _C = 25 °C		1.7		
Maximum Power Dissipation	T _C = 70 °C	P _D	1.1	w	
Maximum Fower Dissipation	T _A = 25 °C	' D	1 ^{b,c}	7	
	T _A = 70 °C		0.6 ^{b,c}		
Operating Junction and Storage Temperature R	T _J , T _{stg}	- 55 to 150	°C		
Soldering Recommendations (Peak Temperatur		260	7 ~		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	100	130	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	60	75	O/ VV	

Notes:

- a. $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 175 °C/W.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	DS/T _J I _D = - 250 μA		- 24		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	1β = - 230 μΑ		2.2		IIIV/ C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 0.6		- 1.5	V	
Gate-Source Leakage	looo	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 10		
Jake-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	-		- 1	μΑ	
Zero date voltage Diam current		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			- 10	1	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le$ - 5 V, $V_{GS} =$ - 10 V	- 15			Α	
		V _{GS} = - 10 V, I _D = - 3.7 A		0.037	0.045		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 2 A		0.044	0.053	Ω	
		V _{GS} = - 2.5 V, I _D = - 2 A		0.066	0.080	1	
Dynamic ^b							
Total Gate Charge	Qg	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 3.7 A		22.8	35	nC	
Total Gate Charge	α _g			10.6	16		
Gate-Source Charge	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -3.7 \text{ A}$		1.7			
Gate-Drain Charge	Q_{gd}			2.6			
Gate Resistance	R_g	f = 1 MHz	2.2	11	22	Ω	
Turn-On Delay Time	t _{d(on)}			28	42		
Rise Time	t _r	$V_{DD} = -15 \text{ V}, R_L = 5.2 \Omega$		65	98		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 2.9 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		47	71		
Fall Time	t _f			62	93	ns	
Turn-On Delay Time	t _{d(on)}			7	14	113	
Rise Time	t _r	$V_{DD} = -15 \text{ V}, R_{L} = 5.2 \Omega$		8	16		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 2.9 A, V_{GEN} = - 10 V, R_g = 1 Ω		52	78		
Fall Time	t _f			52	78		
Drain-Source Body Diode Characterist	ics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 1.4	Α	
Pulse Diode Forward Current	I _{SM}				- 20	,,	
Body Diode Voltage	V_{SD}	I _S = - 2.9 A, V _{GS} = 0 V		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			13	20	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = - 2.9 A, dl/dt = 100 A/μs, T _{.I} = 25 °C		6	12	nC	
Reverse Recovery Fall Time	t _a	2.0 π, απαι – 100 π μο, 1 ₁ – 20 0		9		ns	
Reverse Recovery Rise Time	t _b			4			

Notes:

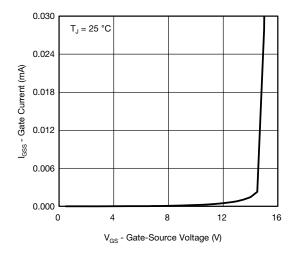
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.

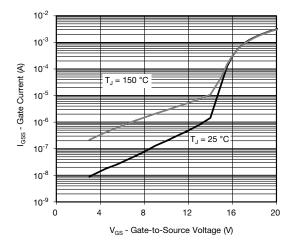
b. Guaranteed by design, not subject to production testing.



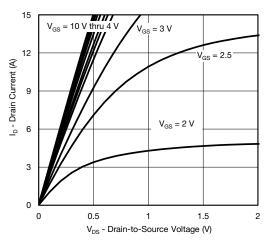
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



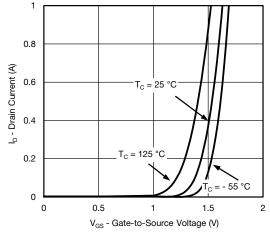
Gate Current vs. Gate-Source Voltage



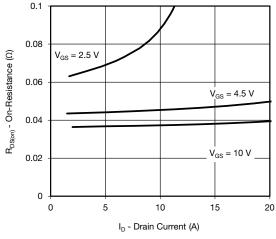
Gate Current vs. Gate-Source Voltage



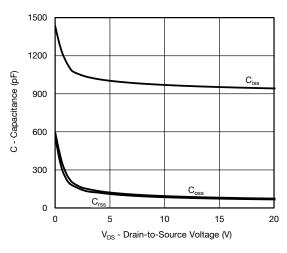
Output Characteristics



Transfer Characteristics

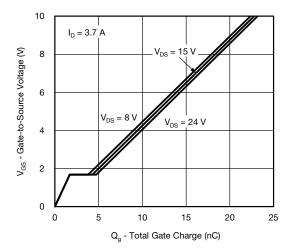


On-Resistance vs. Drain Current

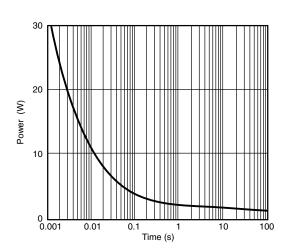


Capacitance

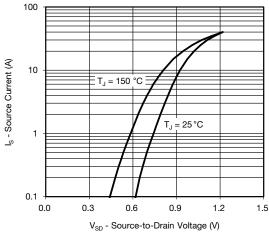
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



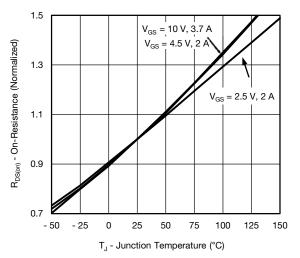
Gate Charge



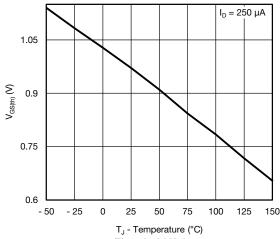
Single Pulse Power, Junction-to-Ambient



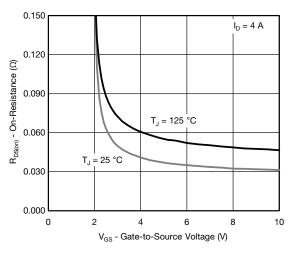
Soure-Drain Diode Forward Voltage



On-Resistance vs. Junction Temperature



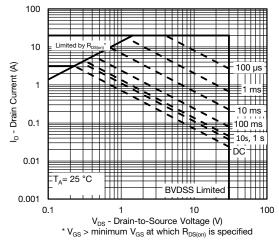
Threshold Voltage



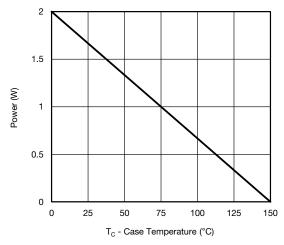
On-Resistance vs. Gate-to-Source Voltage



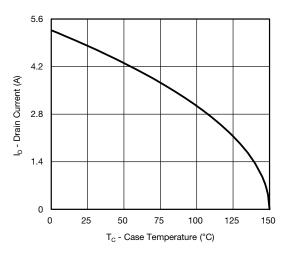
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



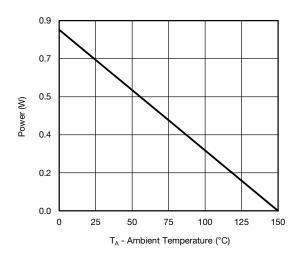
Safe Operating Area, Junction-to-Ambient



Power Junction-to-Case



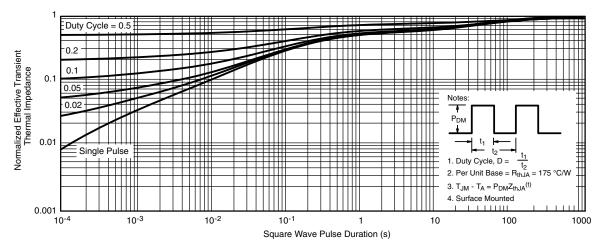
Current Derating*



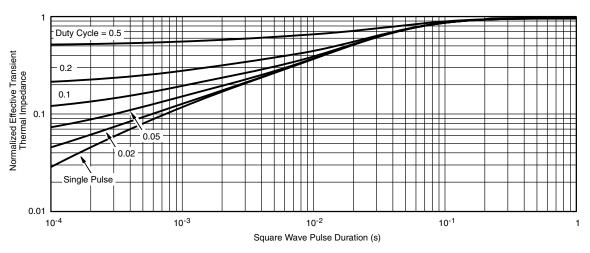
Power Junction-to-Ambient

 $^{^{\}star}$ The power dissipation P_D is based on $T_{J(max.)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppq?63924

SOT-23 (TO-236): 3-LEAD







Dim	MILLIMETERS		INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.9	5 BSC	0.037	4 Ref	
e ₁	1.9			'48 Ref	
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
FCN: S-03946-Rev K 09-	lul-01	•			

ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

APPLICATION NOTE



Legal Disclaimer Notice

Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.