

Vishay Siliconix

N-Channel 200-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	V _{DS} (V) R _{DS(on)} (Ω)			
200	0.130 at V _{GS} = 10 V	3		
	0.142 at V _{GS} = 6.0 V	2.8		

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested

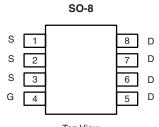
APPLICATIONS

• Primary Side Switch

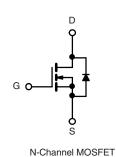


HALOGEN

Available



Top View



Ordering Information: Si4418DY-T1-E3 (Lead (Pb)-free) Si4418DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	200		V
Gate-Source Voltage		V _{GS}	± 20		
	T _A = 25 °C	– I _D	3	2.3	٨
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C		2.1	1.6	
Pulsed Drain Current		I _{DM}	12		A
Avalanche Current	L = 0.1 mH	I _{AS}	6		
Single Avalanche Energy (Duty Cycle \leq 1 %)		E _{AS} 1.8		1.8	mJ
Continuous Source Current (Diode Conduction) ^a		۱ _S	2.1	1.25	А
	T _A = 25 °C	P _D	2.5	1.5	W
Maximum Power Dissipation ^a	T _A = 85 °C		1.3	0.8	
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manimum lunchian ta Analiza 18	t ≤ 10 s	- R _{thJA}	36	50	°C/W
Maximum Junction-to-Ambient ^a	Steady State		71	85	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	15	20	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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SPECIFICATIONS $T_J = 25 \circ C$	C, unless c	otherwise noted					
Parameter	Symbol Test Conditions		Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	2		4	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
	I _{DSS}	$V_{DS} = 200 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 200 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 85 \text{ °C}$			1	μA	
Zero Gate Voltage Drain Current					20		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	12			А	
	Б	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}$ $V_{GS} = 6.0 \text{ V}, \text{ I}_{D} = 2.8 \text{ A}$		0.110	0.130	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}			0.120	0.142		
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 3 \text{ A}$		13		S	
Diode Forward Voltage ^a	V _{SD}	$I_{S} = 2.1 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V	
Dynamic ^b							
Total Gate Charge	Qg			20	30	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 3 \text{ A}$		4.5			
Gate-Drain Charge	Q _{gd}			6.5		1	
Gate Resistance	R _g	f = 1 MHz	1	2	3.4	Ω	
Turn-On Delay Time	t _{d(on)}			15	25		
Rise Time t _r		V_{DD} = 100 V, R_L = 100 Ω		15	25		
Turn-Off Delay Time	t _{d(off)}	$t_{d(off)}$ I _D \cong 1 A, V _{GEN} = 10 V, R _g = 6 Ω		40	60	ns	
Fall Time	t _f			20	30	1	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 2.1 A, dI/dt = 100 A/μs		70	110		

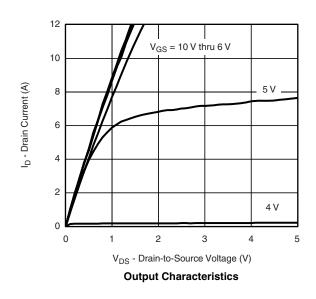
Notes:

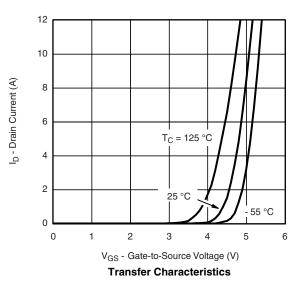
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





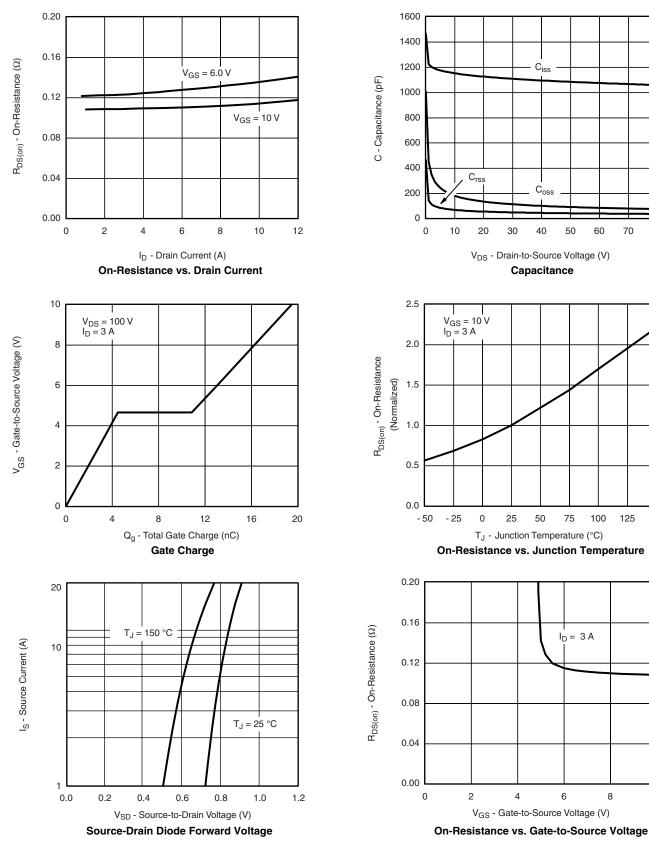


Si4418DY

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 $I_D = 3 A$

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

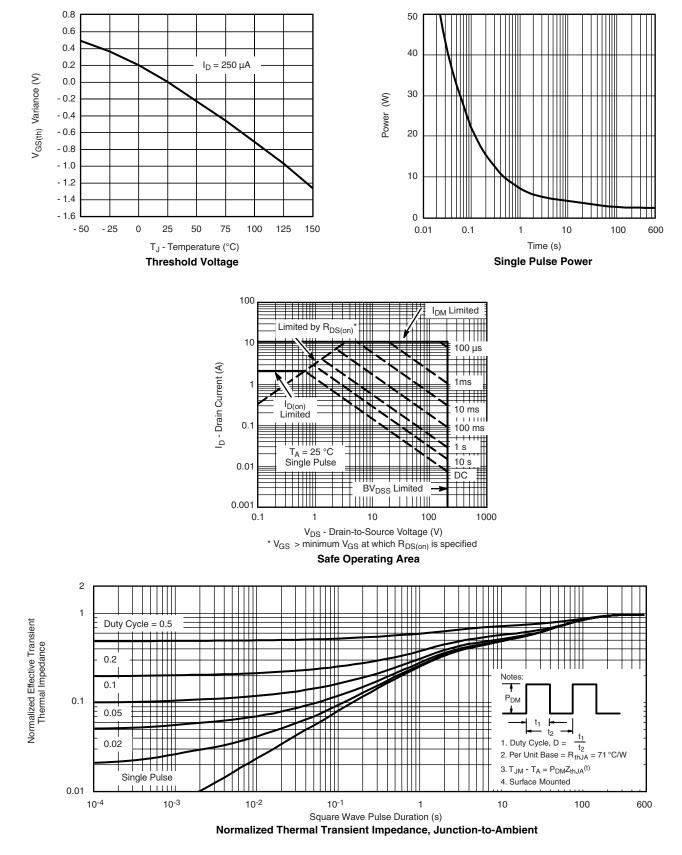


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Si4418DY

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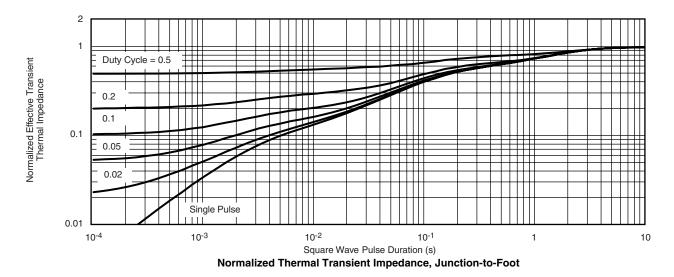






Si4418DY Vishay Siliconix

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg772513.



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