PCI	PCN Number:			20230128001.2				PCN Date:			February 01, 2023
Title: Qualify TI Malaysia as a					n a	dditional Asseml	oly and	Test	: sit	e for se	lect devices
Customer Contact: PCN M					ana	ger		Dep	pt:		Quality Services
Proposed 1 st Ship Date: Jul 31,					20	23	Samp accep				Mar 03, 2023*
*Sa	mple re	quests receive	r 03	3, 2023 will not	be supp	orte	d.				
Cha	ange Ty	vpe:									
\boxtimes	Assem	bly Site				Design				Wafer	· Bump Site
\boxtimes	Assem	bly Process				Data Sheet				Wafer	· Bump Material
\boxtimes	Assem	bly Materials				Part number change				Wafer	Bump Process
	Mechanical Specification					Test Site				Wafer	· Fab Site
Packing/Shipping/Labeling						Test Process				Wafer	· Fab Materials
									Wafer	· Fab Process	
						PCN Detail	S				
Des	scriptio	n of Change:									

Description of Change:

Texas Instruments Incorporated is announcing the qualification of TI Malaysia as an additional Assembly and Test site for devices listed below in the product affected section. Material differences between assembly sites are as follows.

Material Differences:

	TI Taiwan	TI Malaysia
Mold compound	4221499	4211880
Wire type*	0.96mil Au	1.0mil Cu (Die to Leadframe)

* - 0.96 mil Au wire for Die to Die wire bonding

Package Symbolization:

	Current	New		
TI Bug	Include	Replace with "TI" text		
Pin 1 ID	Stripe	Dot		
ECAT**	Include Value	Remove		
Example	212220	• 235558 • 525558		

** - Not all devices have ECAT information included in the symbolization, but for the ones that do, this information will be removed.

Test coverage, insertions, conditions will remain consistent with current testing.

Reason for Change: Continuity of Supply

- 1) To align with world technology trends and use wiring with enhanced mechanical and electrical properties
- 2) Maximize flexibility within our Assembly/Test production sites.
- 3) Cu is easier to obtain and stock

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative): None

Impact on Environmental Ratings

Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.

	REACH	Green Status	IEC 62474							
🛛 No Change	🛛 No Change	🛛 No Change	🛛 No Change							
Changes to product identification resulting from this PCN:										
Assembly Site										
TI Taiwan	Assembly Site Origin (22	2L) ASO: TAI								
TI Malaysia	Assembly Site Origin (22	2L) ASO: MLA								

Product Affected:

UCC21222QDQ1

UCC21222QDRQ1 SN21222QDRQ1

Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines)

Approved 25-Jan-2023

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре		Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: UCC21222QDRQ1	Qual Device: UCC21220ADR	Qual Device: UCC21222QDRQ1	Qual Device: UCC21220ADR	QBS Reference: ISO6721BQDRQ1	QBS Reference: UCC23513QDWYQ1	QBS Reference: ISO7741FEDWRQ1
Test Group	A - Acce	lerated Environ	ment St	ress Tes	sts					,		,	,	,
PC	A1	JEDEC J- STD-020 JESD22- A113	3	77	Preconditioning	MSL1 260C	1 Step	-	-	-	-	No Fails	-	-
PC	A1	JEDEC J- STD-020 JESD22- A113	3	77	Preconditioning	MSL2 260C	1 Step	No Fails	-	No Fails	-	-	-	-
HAST	A2	JEDEC JESD22- A110	3	77	Biased HAST	130C/85%RH	96 Hours	1/77/0	-	-	-	3/231/0	-	-
AC/UHAST	A3	JEDEC JESD22- A102/JEDEC JESD22- A118	3	77	Autoclave	121C/15psig	96 Hours	1/77/0	-	-	-	3/231/0	-	-
тс	A4	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	3/231/0	-	1/77/0	-	3/231/0	-	-
PTC	A5	JEDEC JESD22- A105	1	45	PTC	-40/125C	1000 Cycles	1/45/0	-	-	-	-	-	-
HTSL	A6	JEDEC JESD22- A103	1	45	High Temperature Storage Life	150C	1000 Hours	1/77/0	-	-	-	-	-	-
HTSL	A6	JEDEC JESD22- A103	1	45	High Temperature Storage Life	175C	500 Hours	-	-	-	-	3/135/0	-	-
Test Group	B - Acce	lerated Lifetime	Simula	tion Tes	ts									
HTOL	B1	JEDEC JESD22- A108	1	77	Life Test	125C	1000 Hours	-	-	-	-	3/231/0	3/231/0	-
HTOL	В1	JEDEC JESD22- A108	1	77	Life Test	150C	1000 Hours	-	-	-	-	-	-	3/231/0
ELFR	B2	AEC Q100- 008	1	77	Early Life Failure Rate	125C	48 Hours	-	-	-	-	-	3/2400/0	-

ELFR	B2	AEC Q100-	1	77	Early Life Failure Rate	150C	48 Hours	-	-	-	-	-	-	3/2400/0
Test Group	C - Pack	age Assembly I	Integrity	Tests										
WBS	C1	AEC Q100- 001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	-	1/30/0	-	3/228/0	-	-
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	-	1/30/0	-	3/228/0	-	-
SD	СЗ	JEDEC J- STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	-	-	-	1/15/0	-	-
SD	СЗ	JEDEC J- STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-	-	-	1/15/0	-	-
PD	C4	JEDEC JESD22- B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	3/30/0	-	1/10/0	-	3/30/0	-	-
Test Group	D - Die F	abrication Relia	Lbility Te	sts	·						·		·	·
ЕМ	D1	JESD61	-	-	Electromigration		-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements				
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements				
нсі	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements				
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements				
SM	D5	-	-		Stress Migration		-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements				
Test Group	E - Elect	rical Verification	n Tests											
ESD	E2	AEC Q100- 002	1	3	ESD HBM	-	2000 Volts	1/3/0	-	-	-	1/3/0	1/3/0	1/3/0
ESD	E3	AEC Q100- 011	1	3	ESD CDM	-	500 Volts	1/3/0	-	1/3/0	-	1/3/0	1/3/0	1/3/0
LU	E4	AEC Q100- 004	1	6	Latch-Up	Per AEC Q100-004	-	1/6/0	-	-	-	1/6/0	1/6/0	1/6/0
ED	E5	AEC Q100- 009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	1/30/0	-	1/30/0	-	3/90/0	3/90/0	3/90/0

QBS: Qual By Similarity

Qual Device UCC21222QDRQ1 is qualified at MSL3 260C

Qual Device UCC21220ADR is qualified at MSL2 260C

Qual Device UCC21222QDRQ1 is qualified at MSL3 260C

Qual Device UCC21220ADR is qualified at MSL2 260C

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40C to +150C

Grade 1 (or Q): -40C to +125C

Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level): Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

ZVEI ID reference: SEM-PA-18, SEM-PA-11, SEM-PA-08, SEM-PA-13, SEM-TF-01

Automotive New Product Qualification Summary (As per AEC-Q100, AEC-Q006 and JEDEC Guidelines)

Approved 25-Jan-2023

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Data Displayed as: Number of lots / Total sample size / Total failed								
Туре	#	Test Spec	Min Lot Qty	SS/Lot	Test Name / Condition	Duration	Qual Device: UCC21222QDRQ1	Qual Device: ISO6721BQDRQ1
Test G	roup /	A – Accelerat	ted Env	vironmen	t Stress Tests			
PC	A1	-	3	22	SAM Analysis, Pre Stress	Completed	-	-
PC	A1	JEDEC J- ST D-020 JESD22- A113	3	77	Preconditioning	Level 2-260C	-	No fails
PC	A1	JEDEC J- ST D-020 JESD22- A113	3	77	Preconditioning	Level 3-260C	No fails	
PC	A1	-	3	22	SAMAnalysis, Post Stress	Completed	3/66/0	2/44/0
HAST	A2	JEDEC JESD22- A110	3	77	Biased HAST, 130C/85%RH	96 Hours	1/77/0	3/231/0
HAST	A2	-	3	1	Cross Section, Post bHAST 96 Hours	Completed	-	-
HAST	A2	-	3	30	Wire Bond Shear, Post bHast, 96 Hours	Wires	-	-
HAST	A2	-	3	30	Bond Pull over Stitch, post bHA ST, 96 Hours	Wires	-	-
HAST	A2	-	3	30	Bond Pull over Ball, Post bHA ST, 96 Hours	Wires	-	-
HAST	A2	JEDEC JESD22- A110	3	77	Biased HAST, 130C/85%RH	192 Hours	1/70/0	3/210/0
HAST	A2	-	3	1	Cross Section, Post bHAST 192 Hours	Completed	1/1/0	3/3/0
HAST	A2	-	3	22	SAM Analysis, Post bHAST, 192 Hours	Completed	1/22/0	3/66/0
HAST	A2	-	3	30	Wire Bond Shear, Post bHast, 192 Hours	Wires	1/30/0	3/81/0
HAST	A2	-	3	30	Bond Pull over Stitch, post bHA ST, 192 Hours	Wires	1/30/0	3/81/0
HAST	A2		3	30	Bond Pull over Ball, Post bHA ST, 192 Hours	Wires	1/30/0	3/81/0
тс	A4	JEDEC JESD22-	3	77	Temperature	500 Cycles	3/231/0	3/231/0

Texas Instruments Incorporated

TI Information - Selective Disclosure

PCN# 20230128001.2

Туре	#	Test Spec	Min Lot Qty	SS/Lot	Test Name / Condition	Duration	Qual Device: UCC21222QDRQ1	Qual Device: ISO6721BQDRQ1
		A104 and Appendix 3			Cycle, -65/150C			
тс	A4	-	3	1	Cross Section, Post T/C 500 Cycles	Completed	-	-
тс	A4	-	3	22	SAM Analysis, Post T/C, 500 Cycles	Completed	-	-
тс	A4	-	3	30	Wire Bond Shear, Post T/C 500 Cycles	Wires	-	-
тс	A4	-	3	30	Bond Pull over Stitch Post T/C 500 Cycles	Wires	-	-
тс	A4	-	3	30	Bond Pull over Ball Post T/C 500 Cycles	Wires	-	-
тс	A4	JEDEC JESD22- A104 and Appendix 3	3	77	Tem perature Cycle , -65/1 50C	1000 Cycles	3/210/0	3/210/0
тс	A4	-	3	1	Cross Section, Post T/C 1000 Cycles	Completed	3/3/0	3/3/0
тс	A4	-	3	22	SAMAnalysis, Post T/C, 1000 Cycles	Completed	3/66/0	3/66/0
тс	A4	-	3	30	Wire Bond Shear, Post T/C 1000 Cycles	Wires	3/90/0	3/81/0
тс	A4	-	3	30	Bond Pull over Stitch, Post T/C, 1000 Cycles	Wires	3/90/0	3/81/0
тс	A4	-	3	30	Bond Pull over Ball, Post T/C, 1000 Cycles	Wires	3/90/0	3/81/0
PTC	A5	JEDEC JESD22- A105	1	45	Power Temperature Cycle -40/125C	1000 Cycles	1/45/0	NA
РТС	A5	JEDEC JESD22- A105	1	45	Power Temperature Cycle -40/125C High Temp	2000 Cycles	1/45/0	N/A
HTSL	A6	JEDEC JESD22- A103	3	45	Storage Bake 175C	500 Hours	1/77/0	3/135/0
HTSL	A6	-	3	1	Cross Section, Post HTSL 1000 Hours	Completed	-	-
HT SL	A6	JEDEC JESD22- A103	3	44	High Temp Storage Bake 150C	2000 Hours	1/76/0	3/132/0
HTSL	A6	-	3	1	Cross Section, Post HTSL 2000 Hours	Completed	1/1/0	3/3/0
Test G	roup	C – Package	Assem	nory integ	Wire Bond			
WBS	C1	AEC Q100-001	3	30	Shear, Cpk>1.67	Wires	3/90/0	3/90/0

Туре	#	Test Spec	Min Lot Qty	SS/Lot	Test Name / Condition	Duration	Qual Device: UCC21222QDRQ1	Qual Device: ISO6721BQDRQ1
WBP	C2	MIL- STD883 Method 2011	3	30	Bond Pull over Ball, Cpk >1.67	Wires	3/90/0	3/90/0

- QBS: Qual By Similarity

- Qual Device ISO6721BQDRQ1 is qualified at LEVEL2-260C

- Qual Device UCC21222QDRQ1 is qualified at LEVEL3-260C

- Devices ISO6721BQDRQ1 and UCC21222QDRQ1 contain multiple dies.

A1 (PC): Preconditioning:

Performed for THB, Biased HAST, AC, uHAST & TC samples, as applicable.

Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40C to +150C Grade 1 (or Q): -40C to +125C Grade 2 (or T): -40C to +105C Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold : HTOL, ED Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU Room : AC/uHAST

Green/Pb-free Status: Qualified Pb-Free(SMT) and Green

ZVEI ID reference: SEM-PA-18, SEM-PA-11, SEM-PA-08, SEM-PA-13, SEM-TF-01

For questions regarding this notice, e-mails can be sent to the contact below or your local Field Sales Representative.

Location	E-Mail
WW Change Management Team	PCN ww admin team@list.ti.com

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