

Product/Process Change Notification

N° 2022-034-A

Dear customer,

please find attached our Infineon Technologies AG PCN:

Several changes affecting product TLS850F0TA V50

Important information for your attention:

- Please respond to this PCN by indicating your decision on the approval form, sign it and return to your sales partner before 2022-12-14.
- Infineon aligns with the widely-recognized JEDEC STANDARD "JESD46", which stipulates: "Lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change."

Notwithstanding the aforesaid individual agreements shall prevail.

Your prompt reply will help Infineon to assure a smooth and well-executed transition. If Infineon does not hear from your side by the due date, we will assume your full acceptance to this proposed change and its implementation.

Your attention and response to this matter is greatly appreciated.





On 16 April 2020, Infineon acquired Cypress.

We are now in the process of merging and consolidating our tools and processes for PCN, Information Notes, Errata and Product Discontinuance.

For further details, please visit our website:

https://www.infineon.com/cms/en/about-infineon/company/cypress-acquisition/

Infineon Technologies AG

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Management Board Jochen Hanebeck (CEO), Constanze Hufenbecher, Dr. Sven Schneider, Andreas Urschitz, Dr. Rutger Wijburg Registered office Neubiberg Commercial register Amtsgericht München HRB 126492



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Products affected Please refer to attached affected product list PCN_2022-034-A_[customer-no].pdf

Detailed change information

Subject: Several changes affecting product TLS850F0TA V50

Reason/Motivation: Expansion of wafer production and wafer test to assure continuity of supply and

enable flexible manufacturing. Update of design to further centralize the FE

process and updating of the data sheet.

Description	Old	<u>New</u>	
DATA SHEET: Change of datasheet parameters/electrical specification (min./max./typ. values) and/or AC/DC specification	Rev. 1.0	Rev. 1.1	
DESIGN: Design changes in active elements.		Full mask redesign (Kulim only)	
PROCESS - WAFER PRODUCTION: Move all or parts of production to a different wafer fab site.	Infineon Technologies, Villach, Austria	Infineon Technologies Austria AG, Villach, Austria AND Infineon Technologies (Kulim) Sdn. Bhd., Kulim, Malaysia	
TEST FLOW: Move of all or part of electrical wafer test and/or final test to a different test site.	Infineon Technologies, Villach, Austria	Infineon Technologies Austria AG, Villach, Austria AND Infineon Technologies (Kulim) Sdn. Bhd., Kulim, Malaysia	

Product identification Wafer lot numbers from Villach start with VExxxxxx.

Wafer lot numbers from Kulim with 1Exxxxxx

Traceability assured via date code. No change in SP ordering number



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Anticipated impact of change

Based on the qualification performed, Infineon does not expect any negative impact on quality, function and reliability. No change in form, fit and function

expected.

DeQuMa-ID(s): SEM-DS-01 / SEM-DE-01 / SEM-PW-13 / SEM-TF-01

Attachments

PCN_2022-034-A_[customer-no].pdf affected product list

4_cip22034_A data sheet

Time schedule

Final qualification report	available
First samples available	on request
Intended start of delivery [1]	2023-05-02
Last order date (LOD) [2]	2023-05-02
Last delivery date (LDD) [3]	2024-05-02

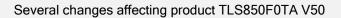
^[1] Provided date or earlier after customer approval.

If you have any questions, please do not hesitate to contact your local sales office.

^[2] Last date where orders for unchanged products will be accepted.

^[3] Last date for delivery of unchanged products. Delivery of changed products can be earlier (see Intended start of delivery) and depends on approval.

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Affected products sold to DIGI-KEY (4002348)

Sales name	SP number	OPN	Package	Customer part number
TLS850F0TA V50	SP001259116	TLS850F0TAV50ATMA	PG-TO263-7-1	TLS850F0TAV50ATMA1