e

RoHS

COMPLIANT

HALOGEN

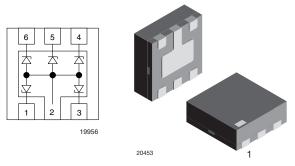
FREE

<u>GREEN</u>

(5-2008)

Vishay Semiconductors

5-Line ESD Protection Diode Array in LLP75-6L



www.vishay.com

MARKING (example only)



Dot = pin 1 marking YY = type code (see table below) XX = date code

DESIGN SUPPORT TOOLS

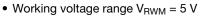


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FEATURES

- Ultra compact LLP75-6L package
- Low package profile < 0.6 mm
- 5-line ESD protection
- Surge immunity acc. IEC 61000-4-5 I_{PPM} > 12 A
- Low leakage current I_B < 1 μA
- ESD immunity acc. IEC 61000-4-2 ± 30 kV contact discharge ± 30 kV air discharge



- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

ORDERING INFORMATION					
DEVICE NAME ORDERING CODE		TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY		
GMF05C-HSF	GMF05C-HSF-GS08	3000	15 000		

PACKAGE DATA							
DEVICE NAME	AME PACKAGE TYPE CODE WEIGHT MOLDING COMPOUND MOISTURE SENSITIVITY LEVEL		SOLDERING CONDITIONS				
GMF05C-HSF	LLP75-6L	1A	4.2 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C	

ABSOLUTE MAXIMUM RATINGS GMF05C-HSF							
PARAMETER	TEST CONDITIONS		SYMBOL	VALUE	UNIT		
Peak pulse current	BiAs-mode: each input (pin 1; 3 - pin 6) to ground (pin 2); acc. IEC 61000-4-5; t _p = 8/20 μs; single shot		I _{PPM}	12	А		
Peak pulse power	BiAs-mode: each input (pin 1; 3 - pin 6) to ground (pin 2); acc. IEC 61000-4-5; t _p = 8/20 µs; single shot		P _{PP}	200	W		
ESD immunity	BiAs-mode: each input (pin 1; 3 - pin 6) to ground (pin 2);	Contact discharge	V _{ESD}	± 30	kV		
	acc. IEC 61000-4-2; 10 pulses	Air discharge	VESD	± 30	kV		
Operating temperature	Junction temperature		TJ	-55 to +125	°C		
Storage temperature			T _{STG}	-55 to +150	°C		

BiAs



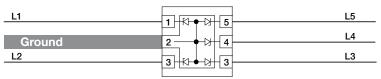


BIAs-MODE (5-line bidirectional asymmetrical protection mode)

With the GMF05C-HSF up to 5 signal- or data-lines (L1 to L5) can be protected against voltage transients. With pin 2 connected to ground and pin 1; 3 up to pin 6 connected to a signal- or data-line which has to be protected. As long as the voltage level on the data- or signal-line is between 0 V (ground level) and the specified maximum reverse working voltage (V_{RWM}) the protection diode between data line and ground offer a high isolation to the ground line. The protection device behaves like an open switch. As soon as any positive transient voltage signal exceeds the break through voltage level of the protection diode, the diode becomes conductive and shorts the transient current to ground. Now the protection device behaves like a closed switch. The clamping voltage (V_{C}) is defined by the breakthrough voltage (V_{BR}) level plus the voltage drop at the series impedance (resistance and inductance) of the protection device.

Any negative transient signal will be clamped accordingly. The negative transient current is flowing in the forward direction of the protection diode. The low forward voltage (V_F) clamps the negative transient close to the ground level.

Due to the different clamping levels in forward and reverse direction the GMF05C-HSF clamping behavior is bidirectional and asymmetrical (BiAs).



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ELECTRICAL CHARACTERISTICS GMF05C-HSF								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N _{channel}	-	-	5	lines		
Reverse stand-off voltage	Max. reverse working voltage	V _{RWM}	-	-	5	V		
Reverse voltage	at I _R = 1 μA	V _R	5	-	-	V		
Reverse current	at $V_R = V_{RWM} = 5 V$	I _R	-	< 0.1	1	μA		
Reverse breakdown voltage	at I _R = 1 mA	V _{BR}	6	-	8	V		
Reverse clamping voltage	at I _{PP} = 12 A acc. IEC 61000-4-5	V _C	-	-	12.5	V		
	at I _{PP} = 1 A acc. IEC 61000-4-5		-	7.8	9.5	V		
Forward clamping voltage	at I _F = 12 A acc. IEC 61000-4-5	V	-	-	5.5	V		
	at I _{PP} = 1 A acc. IEC 61000-4-5	V _F	-	1.5	-	V		
Capacitance	at $V_R = 0 V$; f = 1 MHz	0	-	126	150	pF		
	at V _R = 2.5 V; f = 1 MHz	C _D	-	76	-	pF		

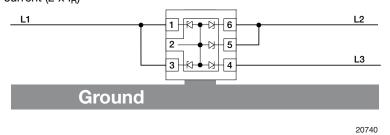
Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode: each input (pin 1; 3 - pin 6) to ground (pin 2)

If a higher surge current or peak pulse current (I_{PP}) is needed, some protection diodes in the GMF05C-HSF can also be used in parallel in order to "multiply" the performance.

If two diodes are switched in parallel you get

- double surge power = double peak pulse current (2 x I_{PPM})
- half of the line inductance = reduced clamping voltage
- half of the line resistance = reduced clamping voltage
- double line capacitance (2 x C_D)
- double reverse leakage current (2 x I_R)



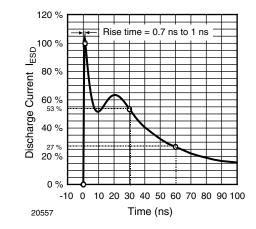
Rev. 1.8, 04-Jan-2019

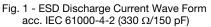
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TYPICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)





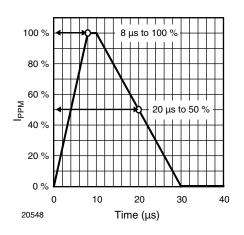


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

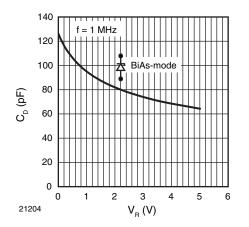


Fig. 3 - Typical Capacitance C_D vs. Reverse Voltage V_R

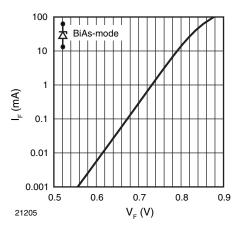


Fig. 4 - Typical Forward Current I_F vs. Forward Voltage V_F

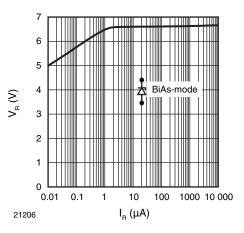


Fig. 5 - Typical Reverse Voltage V_R vs. Reverse Current I_R

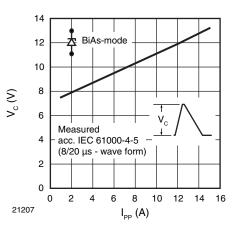


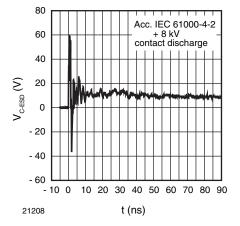
Fig. 6 - Typical Peak Clamping Voltage V_C vs. Peak Pulse Current I_{PP}

Rev. 1.8, 04-Jan-2019

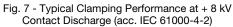
3 al questions, contact: ESDprotection@vis Document Number: 81731

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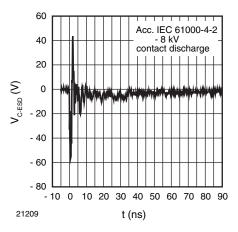


Fig. 8 - Typical Clamping Performance at - 8 kV Contact Discharge (acc. IEC 61000-4-2)

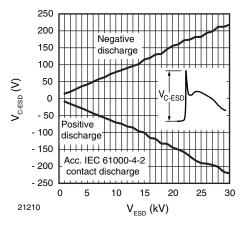
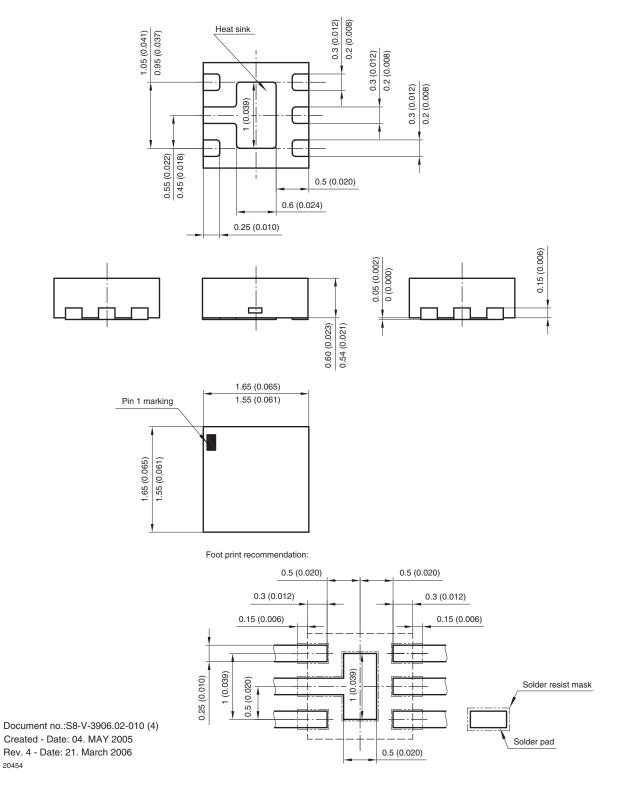


Fig. 9 - Typical Peak Clamping Voltage at ESD Contact Discharge (acc. IEC 61000-4-2)



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PACKAGE DIMENSIONS in millimeters (Inches): LLP75-6L

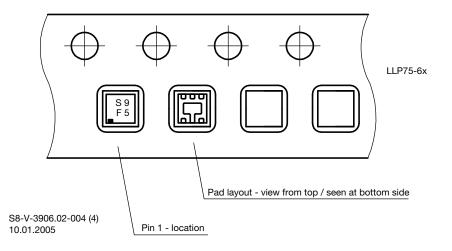


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