

Vishay Siliconix

P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
30	0.0057 at V_{GS} = - 10 V	- 24		
	0.0095 at V _{GS} = - 4.5 V	- 17		

PowerPAK SO-8

Bottom View Ordering Information: Si7483ADP-T1-E3 (Lead (Pb)-free)

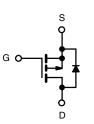
Si7483ADP-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- Halogen-free According to IEC 61249-2-21
 Available
- TrenchFET[®] Power MOSFETS
- New Low Thermal Resistance PowerPAK[®] Package with Low 1.07 mm Profile
- 100 % R_g tested

APPLICATIONS

- Battery and Load Switching
 - Notebook Computers
 - Notebook Battery Packs



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T	A = 25 °C, unles	ss otherwise n	oted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 30		V
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	- I _D	- 24	- 14	А
Continuous Drain Current $(1_j = 150^{\circ} C)$	T _A = 70 °C		- 19	- 11	
Pulsed Drain Current		I _{DM}	- 60		A
Continuous Source Current (Diode Conduction) ^a		ا _S	- 4.5	- 1.6	
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	5.4	1.9	W
Maximum Power Dissipation	T _A = 70 °C		3.4	1.2	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C
Soldering Recommendations (Peak Temperature) ^{b,c}			260		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	t ≤ 10 s	R _{thJA}	18	23	
Maximum Junction-to-Ambient*	Steady State		50	65	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	1.0	1.5	

Notes

a. Surface Mounted on 1" x 1" FR4 board.

b. See Solder Profile (<u>www.vishay.com/ppg?73257</u>). The PowerPAK SO-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

c. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.



COMPLIANT

HALOGEN

FREE

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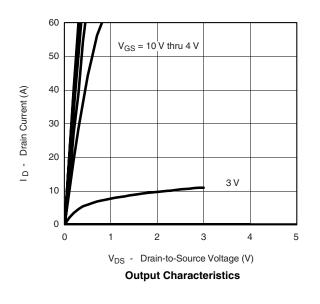
SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static				•				
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1.0		- 3.0	V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V			- 1			
		V_{DS} = - 30 V, V_{GS} = 0 V, T_{J} = 70 °C		- 10	μA			
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -10 V$	- 30			Α		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 24 A		0.0047	0.0057			
		V _{GS} = - 4.5 V, I _D = - 17 A		0.0075	0.0095	Ω		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 24 A		70		S		
Diode Forward Voltage ^a	V _{SD}	I _S = - 2.9 A, V _{GS} = 0 V		- 0.73	- 1.1	V		
Dynamic ^b								
Total Gate Charge	Qg			120	180	nC		
Gate-Source Charge	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 10 V, I_{D} = - 24 A		18				
Gate-Drain Charge	Q _{gd}	1		33				
Gate Resistance	Rg		1.6	3.2	4.8	Ω		
Turn-On Delay Time	t _{d(on)}			22	35			
Rise Time	t _r	V_{DD} = - 15 V, R_L = 15 Ω		33	50	ns		
Turn-Off Delay Time	t _{d(off)}	${\rm I}_{\rm D}\cong$ - 1.0 A, ${\rm V}_{\rm GEN}$ = - 10 V, ${\rm R}_{\rm g}$ = 6 Ω		210	320			
Fall Time	t _f			130	200			
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 2.9 A, dl/dt = 100 A/μs		70	130			

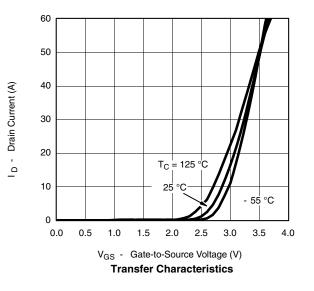
Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

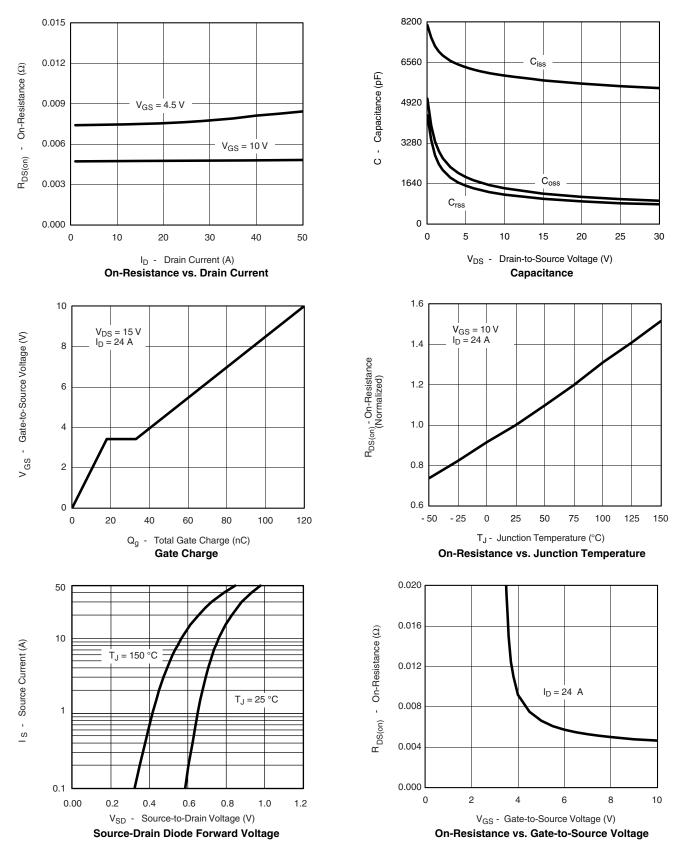






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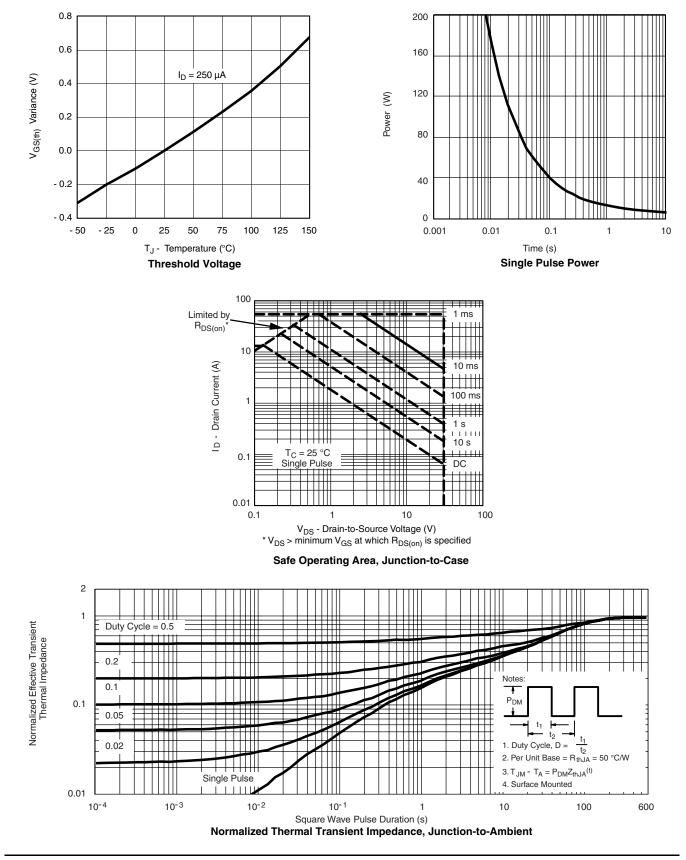
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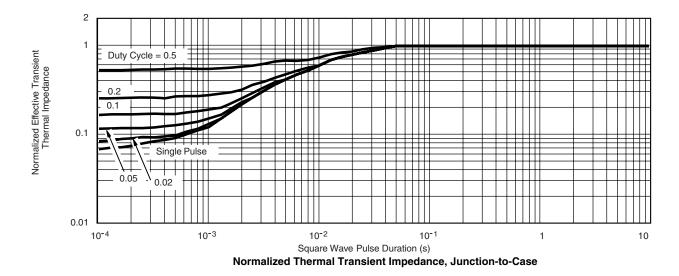
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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?73025.



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