Triple Noninverting Schmitt-Trigger Buffer

NL37WZ17

The NL37WZ17 is a high performance triple buffer with Schmitt-Trigger inputs operating from a 1.65 V to 5.5 V supply.

Features

- Designed for 1.65 V to 5.5 V V_{CC} Operation
- 3.2 ns t_{PD} at $V_{CC} = 5 \text{ V (Typ)}$
- Inputs/Outputs Overvoltage Tolerant up to 5.5 V
- I_{OFF} Supports Partial Power Down Protection
- Source/Sink 24 mA at 3.0 V
- Available in US8, UDFN8 and UQFN8 Packages
- Chip Complexity < 100 FETs
- NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

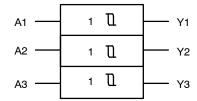


Figure 1. Logic Symbol



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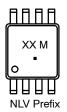
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US8 US SUFFIX CASE 493







UDFN8, 1.45x1.0 MU3 SUFFIX CASE 517BZ





UDFN8, 1.95x1.0 MU1 SUFFIX CASE 517CA





UQFN8, 1.4x1.2 MQ2 SUFFIX CASE 523AS





UQFN8, 1.6x1.6 MQ1 SUFFIX CASE 523AN



 $\begin{array}{lll} \text{X, XX, XXXX} &= \text{Specific Device Code} \\ \text{A} &= \text{Assembly Location} \\ \text{L} &= \text{Lot Code} \\ \text{Y} &= \text{Year Code} \end{array}$

Y = Year Code
W = Week Code
M = Date Code
Pb-Free Package

ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 6 of this data sheet.

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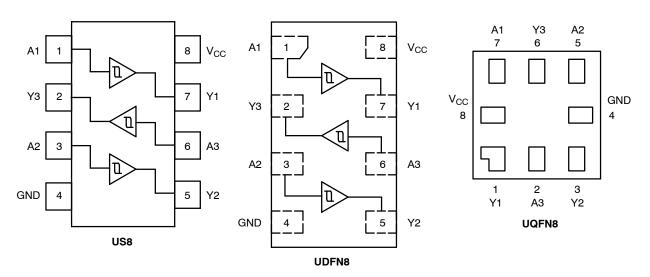


Figure 2. Pinout

PIN ASSIGNMENT

Pin	US8 / UDFN8	UQFN8
1	A1	Y1
2	Y3	A3
3	A2	Y2
4	GND	GND
5	Y2	A2
6	A3	Y3
7	Y1	A1
8	V _{CC}	V _{CC}

FUNCTION TABLE

A Input	Y Output	
L	L	
Н	Н	

MAXIMUM RATINGS

Symbol	Cha	racteristics	Value	Unit
V _{CC}	DC Supply Voltage	NLV	-0.5 to +7.0 -0.5 to +6.5	V
V _{IN}	DC Input Voltage	NLV	-0.5 to +7.0 -0.5 to +6.5	V
V _{OUT}	DC Output Voltage (NLV)	Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode (V _{CC} = 0 V)	-0.5 to V _{CC} + 0.5 -0.5 to +7.0 -0.5 to +7.0	V
	DC Output Voltage	Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode (V _{CC} = 0 V)	-0.5 to V _{CC} + 0.5 -0.5 to +6.5 -0.5 to +6.5	
I _{IK}	DC Input Diode Current	V _{IN} < GND	-50	mA
l _{ok}	DC Output Diode Current	V _{OUT} < GND	-50	mA
l _{out}	DC Output Source/Sink Current		±50	mA
I _{CC} or I _{GND}	DC Supply Current per Supply Pin of	r Ground Pin	±100	mA
T _{STG}	Storage Temperature Range		-65 to +150	°C
TL	Lead Temperature, 1 mm from Case	for 10 secs	260	°C
T _J	Junction Temperature Under Bias		+150	°C
θ_{JA}	Thermal Resistance (Note 2)	US8 UQFN8 UDFN	250 210 231	°C/W
P _D	Power Dissipation in Still Air	US8 UQFN8 UDFN8	500 595 541	mW
MSL	Moisture Sensitivity		Level 1	-
F _R	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	-
V _{ESD}	ESD Withstand Voltage (Note 3)	Human Body Model Charged Device Model	2000 1000	٧
I _{Latchup}	Latchup Performance (Note 4)		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Applicable to devices with outputs that may be tri-stated.
- Applicable to devices with outputs that may be the stated.
 Measured with minimum pad spacing on an FR4 board, using 10mm-by-1inch, 2 ounce copper trace no air flow per JESD51-7.
 HBM tested to ANSI/ESDA/JEDEC JS-001-2017. CDM tested to EIA/JESD22-C101-F. JEDEC recommends that ESD qualification to EIA/JESD22–A115–A (Machine Model) be discontinued per JEDEC/JEP172A.

 4. Tested to EIA/JESD78 Class II.

RECOMMENDED OPERATING CONDITIONS

Symbol	Characteristics			Max	Unit
V _{CC}	Positive DC Supply Voltage		1.65	5.5	V
V _{IN}	DC Input Voltage		0	5.5	V
V _{OUT}	DC Output Voltage	Active–Mode (High or Low State) Tri–State Mode (Note 1) Power–Down Mode ($V_{\rm CC}$ = 0 V)	0 0 0	V _{CC} 5.5 5.5	V
T _A	Operating Temperature Range		-55	+125	°C
t _r , t _f	Input Rise and Fall Time	$\begin{array}{c} V_{CC} = 1.65 \text{ V to } 1.95 \text{ V} \\ V_{CC} = 2.3 \text{ V to } 2.7 \text{ V} \\ V_{CC} = 3.0 \text{ V to } 3.6 \text{ V} \\ V_{CC} = 4.5 \text{ V to } 5.5 \text{ V} \end{array}$	0 0 0	No Limit No Limit No Limit No Limit	ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

			V _{CC}	T,	4 = 25°C	;	-55°C ≤ T	_∆ ≤ 125°C	
Symbol	Parameter	Condition	(V)	Min	Тур	Max	Min	Max	Units
V _T +	Positive Input Threshold Voltage		1.65 2.3 2.7 3.0 4.5 5.5	- - - - -	1.0 1.5 1.7 1.9 2.7 3.3	1.6 1.8 2.0 2.2 3.1 3.6	- - - -	1.6 1.8 2.0 2.2 3.1 3.6	V
V _T -	Negative Input		1.65 2.3 2.7 3.0 4.5 5.5	0.3 0.4 0.5 0.6 1.0	0.55 0.75 0.87 1.0 1.5		0.3 0.4 0.5 0.6 1.0	 - - -	٧
V _H	Input Hysteresis Voltage		1.65 2.3 2.7 3.0 4.5 5.5	0.15 0.25 0.3 0.4 0.6 0.7	0.52 0.75 0.83 0.93 1.2 1.4	1.0 1.1 1.15 1.2 1.5 1.7	0.15 0.25 0.3 0.4 0.6 0.7	1.0 1.1 1.15 1.2 1.5 1.7	V
V _{OH}	High-Level Output Voltage	$\begin{split} V_{IN} &= V_{IH} \text{ or } V_{IL} \\ I_{OH} &= -100 \mu\text{A} \\ I_{OH} &= -4 \text{ mA} \\ I_{OH} &= -8 \text{ mA} \\ I_{OH} &= -12 \text{ mA} \\ I_{OH} &= -16 \text{ mA} \\ I_{OH} &= -24 \text{ mA} \\ I_{OH} &= -32 \text{ mA} \end{split}$	1.65 to 5.5 1.65 2.3 2.7 3.0 3.0 4.5	V _{CC} - 0.1 1.29 1.9 2.2 2.4 2.3 3.8	V _{CC} 1.4 2.1 2.4 2.7 2.5 4.0		V _{CC} - 0.1 1.29 1.9 2.2 2.4 2.3 3.8	- - - -	٧
V _{OL}	Low-Level Output Voltage	$\begin{aligned} &V_{IN} = V_{IH} \text{ or } V_{IL} \\ &I_{OL} = 100 \mu\text{A} \\ &I_{OL} = 4 \text{ mA} \\ &I_{OL} = 8 \text{ mA} \\ &I_{OL} = 12 \text{ mA} \\ &I_{OL} = 16 \text{ mA} \\ &I_{OL} = 24 \text{ mA} \\ &I_{OL} = 32 \text{ mA} \end{aligned}$	1.65 to 5.5 1.65 2.3 2.7 3.0 3.0 4.5	- - - - -	0.08 0.2 0.22 0.28 0.38 0.42	0.1 0.24 0.3 0.4 0.4 0.55 0.55	- - - - -	0.1 0.24 0.3 0.4 0.4 0.55 0.55	V
I _{IN}	Input Leakage Current	V _{IN} = 5.5 V or GND	1.65 to 5.5	-	_	±0.1	-	±1.0	μΑ
l _{OFF}	Power Off Leakage Current	V _{IN} = 5.5 V or V _{OUT} = 5.5 V	0	-	-	1.0	-	10	μΑ
I _{CC}	Quiescent Supply Current	V _{IN} = V _{CC} or GND	5.5	-	-	1.0	-	10	μΑ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

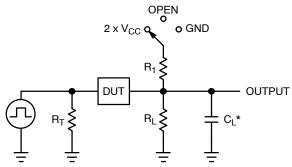
AC ELECTRICAL CHARACTERISTICS

				7	Γ _A = 25°(;	T _A = -55	to 125°C	
Symbol	Parameter	V _{CC} (V)	Test Conditions	Min	Тур	Max	Min	Max	Units
t _{PLH} ,	$\begin{array}{c c} t_{PLH}, \\ t_{PHL} \end{array} \begin{array}{c} Propagation \ Delay \\ \hline 2.5 \pm 0.2 \\ \hline 3.3 \pm 0.3 \end{array} \begin{array}{c} C_L = 15 \ pF \\ R_D = 1 \ M\Omega \\ R_1 = Open \end{array}$	-	6.8	9.2	_	9.2	ns		
^I PHL		2.5 ± 0.2		_	4.3	7.4	_	8.1	
		3.3 ± 0.3		_	3.3	5.0	_	7.0	
		5.0 ± 0.5		_	2.7	4.1	_	4.5	
		3.3 ± 0.3	C_L = 50 pF, R_D = 500 Ω , R_1 = Open	-	4.0	5.0	_	6.6	
		5.0 ± 0.5		_	3.2	4.9	_	5.4	

CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Condition	Typical	Units
C _{IN}	Input Capacitance	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0 \text{ V or } V_{CC}$	2.5	pF
C _{OUT}	Output Capacitance	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0 \text{ V or } V_{CC}$	2.5	pF
C _{PD}	Power Dissipation Capacitance (Note 5)	10 MHz, V_{CC} = 3.3 V, V_{IN} = 0 V or V_{CC} 10 MHz, V_{CC} = 5.5 V, V_{IN} = 0 V or V_{CC}	9 11	pF

^{5.} C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I_{CC(OPR)} = C_{PD} • V_{CC} • f_{in} + I_{CC}. C_{PD} is used to determine the no–load dynamic power consumption; P_D = C_{PD} • V_{CC}² • f_{in} + I_{CC} • V_{CC}.



Test	Switch Position	C _L , pF	R_L, Ω	R_1, Ω	
t _{PLH} / t _{PHL}	Open	See AC Characteristics Table			
t _{PLZ} / t _{PZL}	2 x V _{CC}	50	500	500	
t _{PHZ} / t _{PZH}	GND	50	500	500	

X = Don't Care

 C_L includes probe and jig capacitance R_T is Z_{OUT} of pulse generator (typically 50 $\Omega)$ f = 1 MHz

Figure 3. Test Circuit

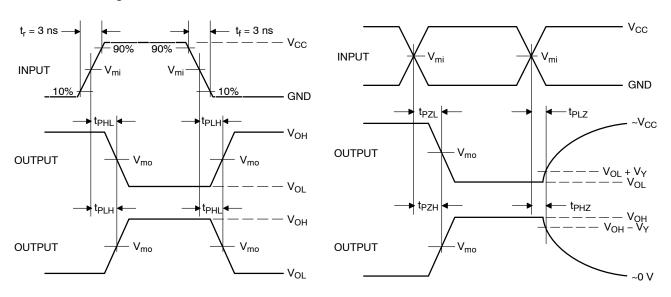


Figure 4. Switching Waveforms

		V _m		
V _{CC} , V	V _{mi} , V	t _{PLH} , t _{PHL}	t_{PZL} , t_{PLZ} , t_{PZH} , t_{PHZ}	V _Y , V
1.65 to 1.95	V _{CC} /2	V _{CC} /2	V _{CC} /2	0.15
2.3 to 2.7	V _{CC} /2	V _{CC} /2	V _{CC} /2	0.15
3.0 to 3.6	V _{CC} /2	V _{CC} /2	V _{CC} /2	0.3
4.5 to 5.5	V _{CC} /2	V _{CC} /2	V _{CC} /2	0.3

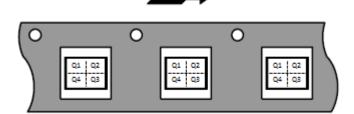
DEVICE ORDERING INFORMATION

Device	Packages	Specific Device Code	Pin 1 Orientation (See below)	Shipping [†]
NL37WZ17USG	US8	LX	Q4	3000 / Tape & Reel
NLV37WZ17USG*	US8	LX	Q4	3000 / Tape & Reel
NL37WZ17MQ1TCG (In Development)	UQFN8, 1.6 x 1.6, 0.5P	TBD	TBD	3000 / Tape & Reel
NL37WZ17MU1TCG (In Development)	UDFN8, 1.95 x 1.0, 0.5P	TBD	Q4	3000 / Tape & Reel
NL37WZ17MU3TCG (In Development)	UDFN8, 1.45 x 1.0, 0.35P	2	Q4	3000 / Tape & Reel
NL37WZ17MQ2TCG (In Development)	UQFN8, 1.4 x 1.2, 0.4P	TBD	TBD	3000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Pin 1 Orientation in Tape and Reel

Direction of Feed



^{*}NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.



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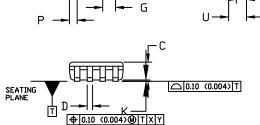
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DETAIL E

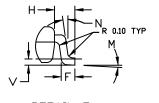
DATE 30 APR 2021

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSION, OR GATE BURR. MOLD FLASH, PROTRUSION, OR GATE BURR SHALL NOT EXCEED 0.14 (0.0055*) PER SIDE.
- DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH AND PROTRUSION SHALL NOT EXCEED 0.14 (0.0055*) PER SIDE.
- LEAD FINISH IS SOLDER PLATING WITH THICKNESS OF 0.0076-0.0203 MM (0.003-0.008°).
- 6. ALL TOLERANCE UNLESS OTHERWISE SPECIFIED ± 0.0508 MM (0.002").



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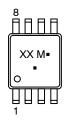


MOUNTING FOOTPRINT

DETAIL E

	MILLIMETERS		INCHES	
DIM	MIN.	MAX.	MIN.	MAX.
Α	1.90	2.10	0.075	0.083
В	2.20	2.40	0.087	0.094
C	0.60	0.90	0.024	0.035
D	0.17	0.25	0.007	0.010
Ŧ	0.20	0.35	0.008	0.014
G	0.50	BSC	0.020	BSC
I	0.40	REF	0.016	REF
۲	0.10	0.18	0.004	0.007
К	0.00	0.10	0.000	0.004
Г	3.00	3.25	0.118	0.128
М	0*	6*	0*	6*
N	0*	10*	0*	10*
Р	0.23	0.34	0.010	0.013
R	0.23	0.33	0.009	0.013
2	0.37	0.47	0.015	0.019
U	0.60	0.80	0.024	0.031
V	0.12	BSC	0.005	BSC
	0.12	BSC	0.005	BSC

GENERIC MARKING DIAGRAM*



XX = Specific Device Code

M = Date Code ■ = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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