| PCN Number: | 20200206 | 5006 | PCN Date | e: Feb | 18, 2 | 2020 | | | |
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| Title: Datasheet for | or MSP430FI | R2311, MS | P430FR2310 | | | | | | |
| Customer Contact: | PCN Manac | <u>jer</u> | | D | ept: | Quality Services | | | |
| Change Type: | | | | | _ | | | | |
| Assembly Site | | Desig | n | | Wa | fer Bump Site | | | |
| Assembly Process | | | Sheet | | | fer Bump Material | | | |
| Assembly Material | S | _= | number change | | _ | ifer Bump Process | | | |
| Mechanical Specification | | Test Site | | | _ | ifer Fab Site | | | |
| Packing/Shipping/ | | _= | Test Process | | Wafer Fab Materials | | | | |
| | | | | | Wa | ifer Fab Process | | | |
| | | Notific | ation Detail | s | | | | | |
| Description of Change: | | | | | | | | | |
| Texas Instruments Inc | | announcir | g an information | on only i | notific | ation. | | | |
| | | | | | i o cii i c | | | | |
| | he product datasheet(s) is being updated as summarized below. he following change history provides further details. | | | | | | | | |
| | 7 1 | | | | | | | | |
| TEXAS INSTRUMENTS | | | | | | 0FR2311, MSP430FR2310 | | | |
| | | | | SLASE58E - | FEBRUAF | RY 2016-REVISED DECEMBER 2019 | | | |
| Changes from August 29, 20 | 18 to Decemb | er 9, 2019 | | | | Page | | | |
| Updated Section 3.1, Related Products Changed the note that begins "Supply voltage changes faster than 0.2 V/μs can trigger a BOR reset" in Section 5.3, Recommended Operating Conditions Added the note that begins "TI recommends that power to the DVCC pin must not exceed the limits" in Section 5.3, Recommended Operating Conditions Changed the note that begins "A capacitor tolerance of ±20% or better is required" in Section 5.3, Recommended Operating Conditions Combined former sections 5.8 and 5.10 to Section 5.9, Production Distribution of LPM Supply Currents Corrected "SvS Enabled" test condition on Figure 5-2, LPM3.5 Supply Current vs Temperature Added the note "See MSP430 32-kHz Crystal Oscillators for details on crystal section, layout, and testing" to Table 5-3, XT1 Crystal Oscillator (Low Frequency) Changed the note that begins "Requires external capacitors at both terminals" in Table 5-3, XT1 Crystal Oscillator (Low Frequency) Added the T_{TB,cap} parameter in Table 5-13, Timer_B Changed the parameter symbol from R₁ to R₁,Mux in Table 5-20, ADC, Power Supply and Input Range Conditions Corrected the test conditions for the R₁,Mux parameter in Table 5-20, ADC, Power Supply and Input Range Conditions Added R₁,Misc TYP value of 34 kΩ in Table 5-21, ADC, 10-Bit Timing Parameters Added formula for R₁ in Table 5-21, ADC, 10-Bit Timing Parameters Added from RTCCLK to RTCCKSEL in Table 6-81, Port P1, P2 Registers (Base Address: 0200h) Added P2SELC information in Table 6-31, Port P1, P2 Registers (Base Address: 0200h) Added note to "ADC calibration" in Table 6-46, Device Descriptors Added note to "ADC calibration" in Table 6-46, Device Descriptors | | | | | | | | | |
| The datasheet number Device Family | will be char | nging. | Change From: | • | Cha | ngo To: | | | |
| Device Faililly | | | Change From: SLASE58D | • | | nge To: SE58E | | | |
| MSD/30FD3311 MSD | 130ED3310 | | SLASESOD | | SLA | SLJOE | | | |

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/MSP430FR2310

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

| | MSP430FR2310IPW16 | MSP430FR2310IPW16R | MSP430FR2310IPW20 | MSP430FR2310IPW20R |
|---|-------------------|--------------------|-------------------|--------------------|
| | MSP430FR2310IRGYR | MSP430FR2310IRGYT | MSP430FR2311IPW16 | MSP430FR2311IPW16R |
| ĺ | MSP430FR2311IPW20 | MSP430FR2311IPW20R | MSP430FR2311IRGYR | MSP430FR2311IRGYT |

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