PCN	Number:	201603090	01	1		F	PCN Date:	3/10/2016		
Title	: Datasheet for	r ADS41B29/	Α	NDS	41B49					
Cust	omer Contact:	PCN Manager				De	pt: Qua	ality Services		
Char	nge Type:						, ,	,		
	Assembly Site			Пг	Design	П	Wafer Bun	nn Site		
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The r	nroduct datasheeti	(s) is heing u	nd	date	ed as summarized below.					
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The f	following change h	nistory provid	es	s fui	rther details.					
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4	T EXAS						ADS41B29, A	DS/18/0		
,	Instruments				SBAS486F-NOV	EMBEE	R 2009-REVISED FE			
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•	Changed title and change	ed <i>ADS41B49/</i> 291	to A	ADS4	41Bx9 and QFN to VQFN throughou	ıt doc	ument	1		
					ble, front-page figure, ESD Ratings t			tion		
					mming section, Register Maps section			0.0		
	Implementation section, Power Supply Recommendations section, Layout section, Device and Documentation Support section, and Mechanical, Packaging, and Orderable Information section									
	Deleted Ordering Information table									
	Changed Pin Functions table: changed title and format									
	Added Added last row to Voltage applied to input pins section in Absolute Maximum Ratings table									
	Changed <i>Temperature</i> parameters in <i>Absolute Maximum Ratings</i> table: changed format of <i>Temperature</i> section and changed maximum specifications for T _A and T _J							7		
•	Changed TYP column header to NOM in Recommended Operating Conditions table									
•	Changed Digital Outputs, T_J parameter in Recommended Operating Conditions table							8		
•	Deleted High-Performance Modes section from Recommended Operating Conditions table									
•	Changed conditions of Electrical Characteristics: General table from temperature to ambient temperature									
Changed conditions of Electrical Characteristics: ADS41B29, ADS41B49 table from temperature to ambient										
	temperature10									
	Added footnote 1 to Electrical Characteristics: ADS41B29, ADS41B49 table									
	Changed conditions of Timing Requirements: LVDS and CMOS Modes table from temperature to ambient temperature 12									
	Added footnotes 6 and 7 to Timing Requirements: LVDS and CMOS Modes table									
	Added footnote 1 to Timing Requirements: Reset table									
	Changed title of Figure 13 and Figure 14									
•	Changed title of Figure 31 and Figure 32									
• 0	Changed conditions of Table 6									
Added Summary of High-Performance Modes section										
Changed bit registers to satisfy new standard requirements										
The datasheet number will be changing.										
					nge To:					
	ADS41B29/ADS41B49 SBAS486E SBAS486F									
				 ;						

These changes may be reviewed at the datasheet links provided. http://www.ti.com/product/ADS41B49

Reason for Change:

To more accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

ADS41B29IRGZ25	ADS41B29IRGZR	ADS41B29IRGZT
ADS41B49IRGZR	ADS41B49IRGZT	

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

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