PCN	Numb	er:	2	February 23, 2023											
Title: Qualification of RFAB as an additional Fab site option for select LBC8 devices															
Cust	tomer	Cont	act:	<u>PCI</u>	N Ma	<u>anager</u>			De	pt:		Quality Services			
Prop	osed 1	1st S	hip Date:	Aug	g 22	, 2023		Sample requests accepted until:				Mar 22, 2023*			
*Sai	*Sample requests received after March 22, 2023 will not be supported.														
Cha	nge Ty	•			_										
<u> </u>	Assen		Site	Į Į	4		/ Process			<u> </u>		mbly Materials			
	Design Test S			J I	+		l Specification Shipping/Labeling				Mechanical Specificat Test Process				
\vdash	Wafer		n Site	[= 1		ımp Mater		ig	\exists	Wafer Bump Process				
	Wafer		<u> </u>		$\overline{\mathbb{X}}$			b Materials				r Fab Process			
				[Part number change							0. 1 40 1 10 000			
Notification Details															
Description of Change:															
Texas Instruments is pleased to announce the qualification of its RFAB fabrication facility as an additional Wafer Fab source for the selected devices listed in the "Product Affected" section.															
		Cı	urrent Fab S	Site				A	dditi	ona	Fab S	Site			
Cu	Current Fab Process Site					lafer meter	New F		Process			Wafer Diameter			
	MIHO8		LBC8		20	0 mm	RFAE				8	300 mm			
Qual details are provided in the Qual Data Section.															
Reas	son for	⁻ Cha	nge:												
Cont	inuity o	of sup	oply.												
Anti	cipate	d imį	pact on Fit,	For	m, F	unction,	Quality of	or Reli	iabilit	ty (p	ositiv	e / negative):			
None	e.														
Cha	nges to	o pro	duct identif	ficat	ion	resulting	from thi	s PCN	:						
Fab	Site In	nform	nation:												
	Chip Sit		Chip Site	Orig	in C	ode (20L)	Chip 9	Site Co	<u>oun</u> tr	de (21	1L) Chip Site City				
	MIHO8	3		MI	H8			JPN USA				Ibaraki			
	RFAB			RF	В				Richardson						
Sample product shipping label (not actual product label) TEXAS INSTRUMENTS MADE: IN: Malaysia 20: (1P) SN74LS07NSR (Q) 2000 (D) 0336															
MSL 2 /260C/1 YEAR SEAL DT MSL 1 /235C/UNLIM 03/29/04 (4W) TKY (1T) 7523483SI2 OPT:															
Droc	Product Affected:														
FIUC	auct Al	iecte	su.												

ISO1640BQDRQ1

ISO1540QDRQ1

ISO1640QDWRQ1

Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines)

Approve Date 02-February-2023

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: ISO1640BQDRQ1	Qual Device: ISO1640QDWRQ1	QBS Reference: UCC23513QDWYQ1	QBS Reference: ISO1640BQDRQ1	QBS Reference: ISO6760QDWRQ1	QBS Reference: ISO6763QDWRQ1	QBS Reference: ISO6762QDWRQ1
Test Group	A - Acce	elerated Enviro	nment S	tress Te	sts									
PC	A1	JEDEC J- STD-020 JESD22- A113	3	77	Preconditioning	MSL2 260C	1 Step	No Fails	-	No Fails	No Fails	No Fails	No Fails	No Fails
HAST	A2	JEDEC JESD22- A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	-	3/231/0	3/231/0	1/77/0	1/77/0	1/77/0
AC/UHAST	А3	JEDEC JESD22- A102/JEDEC JESD22- A118	3	77	Autoclave	121C/15psig	96 Hours	-	-	3/231/0	3/231/0	1/77/0	1/77/0	1/77/0
тс	A4	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	1/77/0	-	3/231/0	3/231/0	1/77/0	1/77/0	-
TC-BP	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull	-	-	1/5/0	-	-	1/5/0	-	1/5/0	-
HTSL	A6	JEDEC JESD22- A103	1	45	High Temperature Storage Life	150C	1000 Hours	-	-	-	1/45/0	-	-	-
HTSL	A6	JEDEC JESD22- A103	1	45	High Temperature Storage Life	175C	500 Hours	-	-	3/135/0	-	1/45/0	1/45/0	-
Test Group B	3 - Accel	lerated Lifetime	Simula	tion Test	ts									
HTOL	B1	JEDEC JESD22- A108	1	77	Life Test	125C	1000 Hours	-	-	3/231/0	-	1/77/0	-	-
ELFR	B2	AEC Q100- 008	1	77	Early Life Failure Rate	125C	48 Hours	-	-	3/2400/0	-	-	-	-
Test Group C	C - Packa	age Assembly I	integrity	Tests										
WBS	C1	AEC Q100- 001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	1/30/0	3/90/0	3/90/0	1/30/0	1/30/0	-
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	1/30/0	3/90/0	3/90/0	1/30/0	1/30/0	-
SD	C3	JEDEC J- STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	-	1/15/0	-	-	1/15/0	-
SD	C3	JEDEC J- STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-	1/15/0	-	-	1/15/0	-
PD	C4	JEDEC JESD22- B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	-	-	3/30/0	-	-	1/10/0	-
Test Group D) - Die Fa	abrication Relia	bility Te	sts										
ЕМ	D1	JESD61		-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35		-	Time Dependent Dielectric Breakdown	-		Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
нсі	D3	JESD60 & 28			Hot Carrier Injection			Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-		Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-		-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Test Group I	E - Elect	rical Verificatio	n Tests											
ESD	E2	AEC Q100- 002	1	3	ESD HBM	-	2000 Volts	-	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0
ESD	E3	AEC Q100- 011	1	3	ESD CDM	-	500 Volts	-	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0
LU	E4	AEC Q100- 004	1	6	Latch-Up	Per AEC Q100-004	-	-	1/6/0	1/6/0	1/6/0	1/6/0	1/6/0	1/6/0
ED	E5	AEC Q100- 009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	1/30/0	1/30/0	3/90/0	3/90/0			-

- QBS: Qual By Similarity
 Qual Device ISO1640BQDRQ1 is qualified at MSL2 260C
 Qual Device ISO1640QDWRQ1 is qualified at MSL2 260C

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

Ambient Operating Temperature by Automotive Grade Level:

- Grade 0 (or E): -40C to +150C
- Grade 1 (or Q): -40C to +125C
- Grade 2 (or T): -40C to +105C
- Grade 3 (or I): -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold: HTOL, ED
- . Room/Hot: THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Affected ZVEI IDs: SEM-PW-13, SEM-PW-02

For questions regarding this notice, e-mails can be sent to the contact below or your local Field Sales Representative.

Location	E-Mail						
WW Change Management Team	PCN www admin_team@list.ti.com						

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