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Bulletin Date: 10/18/2016	Bulletin Effective Date: 10/18/2016
Title: Si3402-B and Si3402-C Human Body Model ESD Notification	
Bulletin Details	
Description: This Bulletin provides details on an exception to Human Body Model ESD performance on the Si3402-B and Si3402-C products.	
Issue: Human Body Model Electrostatic Discharge (HBM ESD) resiliency is a standard qualification test for Silicon Labs for its products. Silicon Labs qualification standard is +/-2000V for all pins and test conditions. The SWO pin (Switching transistor output, pin #18) on the Si3402-B-GM and Si3402-C-GM has been found to fall short of this standard. The SWO positive-to-VDD negative HBM ESD strike case is resilient to only 800V. All other test cases on all other pins meet the +/- 2000V standard.	
Impact: The Industry Council on ESD Target Levels and JEDEC publication JEP155A.01 state that Basic ESD Control methods are sufficient to allow safe manufacturing with proven margin for HBM ESD resiliency down to +/- 500V. Basic ESD Control is defined as programs which include wrist straps, grounded work surfaces, and safe packaging materials in manufacturing environments. SWO pin #18 exceeds the 500V floor for all test cases.	
Resolution: Silicon Labs sets high standards for all of its integrated circuit products. ESD protection structures on the Si3402-C-GM for the SWO pin #18 are being improved to allow HBM ESD to conform to the Silicon Labs corporate standard of +/- 2000V. The Si3402-C-GM product can provide an upgrade option for those customers who are concerned about this issue. Production of the revised silicon is anticipated in 1Q17 and production in 2Q17.	
Silicon Labs will continue to produce the current Si3402-B-GM for those customers who wish to continue to use it.	
Reason: See Description section above.	
Product Identification: Si3402-B-GM Si3402-B-GMR Si3402-C-GM Si3402-C-GMR	
This change is considered a minor change which does not affect form, fit, function, guality, or reliability.	

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Customer Actions Needed: No customer action required.

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