PCN Number:			201	20170928004			PCN Date:	te: January 11, 20			. 2018	
Titl			r TPS548B22				1 011 20001	1				
								Dept:		nt:	Quality Services	
Customer Contact: PCN Manager Dept: Quality Services Change Type:												
		embly Site			Design				П	r Bump Site		
Ħ		Assembly Process			X	Data Sheet			Ħ		r Bump Material	
Ħ	Assembly Materials				Part number change				П		r Bump Process	
	Mechanical Specification)	Ħ	Test Site					r Fab Site	
	Packing/Shipping/Labeling			ng		Test Process				Wafe	r Fab Materials	
									Wafer Fab Process			
	Notification Details											
Des	Description of Change:											
Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details. Texas Texas Texas												
	• '	NSTRUMENTS						SL	USCE	4A – JANU	ARY 2017-REVISED JULY 2017	
CI	Changes from Original (January 2017) to Revision A Page											
_	Corre	cted nackage name	in title o	of nin con	necti	on diagram from	"DOP" to "RVF"				3	
	Corrected package name in title of pin connection diagram from "DQP" to "RVF"											
	Added MIN and MAX values for VDD OVEO rising threshold Added MIN and MAX for all SS settings and table notes 3 and 4 in <i>Timing Requirements</i>											
											13	
											s SS Settings 19	
	Adde	d Figure 17 and Fig	jure 18								19	
The	data	sheet number	will h	e chan	ain	٦.						
Device Family			5	C CHAIL	9		Change From:			Chang	e To:	
TPS548B22						SLUSCE	SLUSCE4			SLUSCE4A		
These changes may be reviewed at the datasheet links provided.												
							iiiks provide	u.				
http://www.ti.com/product/TPS548B22												
Rea	son	for Change:										
	To accurately reflect device characteristics.											
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):												
In order to ensure either 4-ms or 8-ms soft start setting would work properly in the customer												
system, additional application consideration is needed. The recommended application												
workaround to support the 4-ms and 8-ms soft-start settings is to ensure sufficient time delay												
between the VDD and EN_UVLO signals. There are no changes to the actual device.												
Changes to product identification resulting from this PCN: None.												
		Affortod										
		Affected:	Т-	TDCE40D22DVET								
TPS548B22RVFR				TPS548B22RVFT								

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com

Europe	PCNEuropeContact@list.ti.com					
Asia Pacific	PCNAsiaContact@list.ti.com					
Japan	PCNJapanContact@list.ti.com					