1

Schottky Rectifier, 10 A

Base cathode

ό1

Anode

Q 4, 2

20 mJ

dз

Anode

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-10WQ045FNHM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

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MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES						
I _{F(AV)}	Rectangular waveform	10	А					
V _{RRM}		45	V					
I _{FSM}	t _p = 5 μs sine	400	A					
V _F	10 A _{pk} , T _J = 125 °C	0.53	V					
TJ	Range	- 40 to 175	°C					

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-10WQ045FNHM3	UNITS			
Maximum DC reverse voltage	V _R	45	V			
Maximum working peak reverse voltage	V _{RWM}	40	V			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average forward current See fig. 5		50 % duty cycle at T_{C} = 157 °C, rectangular waveform		10	A		
Maximum peak one cycle non-repetitive surge current	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	400	A		
See fig. 7		10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	75			
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 3 \text{ A}, L = 4.4 \text{ mH}$		20	mJ		
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3.0	А		

D-PAK (TO-252AA)

E_{AS}

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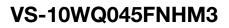
PRODUCT SUMMARY							
Package	D-PAK (TO-252AA)						
I _{F(AV)}	10 A						
V _R	45 V						
V_F at I_F	0.53 V						
I _{RM}	15 mA at 125 °C						
T _J max.	175 °C						
Diode variation	Single die						





VS-10WQ045FNHM3

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ELECTRICAL SPECIFICATIONS							
PARAMETER	ARAMETER SYMBOL TEST CONDITIONS		VALUES	UNITS			
		10 A	T _{.1} = 25 °C	0.63	V		
Maximum forward voltage drop	V _{FM} ⁽¹⁾	20 A	1j = 25 0	0.80			
See fig. 1	VFM (*)	10 A	T, = 125 °C	0.53			
		20 A	1j = 125 0	0.71			
Maximum reverse leakage current	imum reverse leakage current		$V_{\rm B} = Rated V_{\rm B}$	1	mA		
See fig. 2	IRM \''	T _J = 125 °C	VR - Haleu VR	15	IIIA		
Threshold voltage V _{F(TO}		$T_J = T_J$ maximum		0.255	V		
orward slope resistance r _t				22	mΩ		
Typical junction capacitance	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 $^{\circ}\mathrm{C}$		760	pF		
Typical series inductance	L _S	Measured lead to lead 5 m	m from package body	5.0	nH		

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

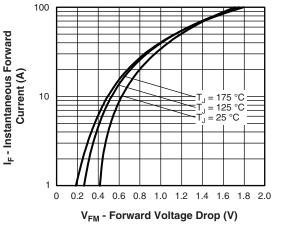
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 40 to 175	°C		
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	2.0	°C/W		
Maximum thermal resistance, junction to ambient	R _{thJA}		50	C/W		
Approximate weight			0.3	g		
			0.01	oz.		
Marking device		Case style D-PAK	10WQ0	45FNH		

Note

 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

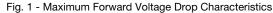
VS-10WQ045FNHM3

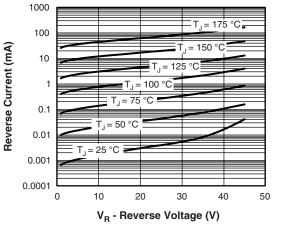
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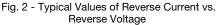


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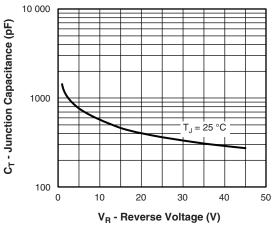


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

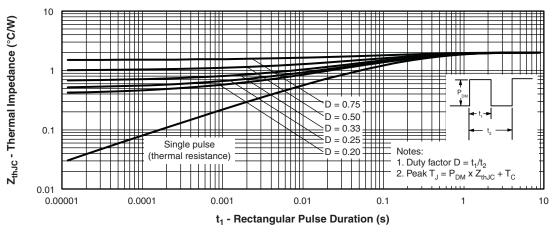


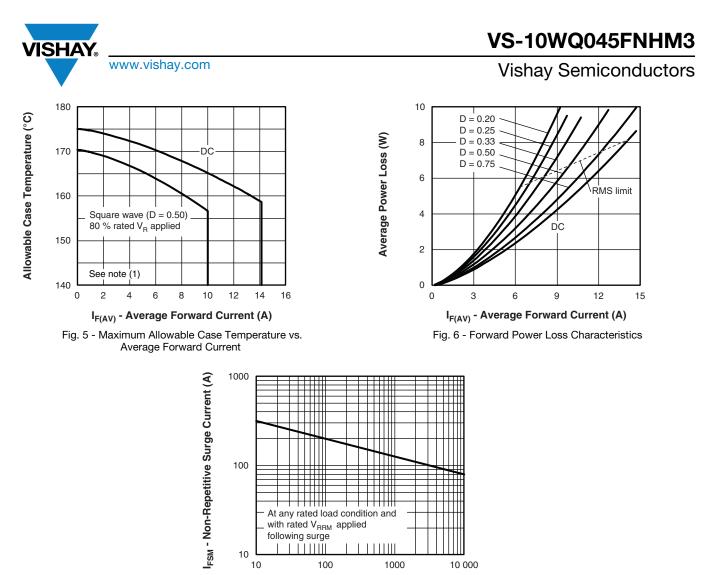
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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t_p - Square Wave Pulse Duration (µs)

Fig. 7 - Maximum Non-Repetitive Surge Current

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, x \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, x \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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ORDERING INFORMATION TABLE

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Device code	VS-	10	W	Q	045	FN	TRL	Н	М3
	1	2	3	4	5	6	(7)	8	9
	1 -	- Visl	nay Sen	nicondu	ctors pro	oduct			
	2 -	Cur	rent rati	ng (10 A	A)				
	3 -	- Pac	kage id	entifier:					
	_	W =	D-PAK						
	4 -	Sch	ottky "G)" series					
	5 -	Vol	age rati	ng (045	= 45 V)				
	6	- FN	= TO-2	52AA (D	-PAK)				
	7 -	• N	one = T	ube					
		• TI	R = Tap	e and re	el				
		• TF	RL = Ta	pe and ı	eel (left	oriente	d)		
		• TF	RR = Ta	pe and	reel (rigl	ht orien	ted)		
	8	• Н=	AEC-Q	101 qua	alified				
	9 -			ntal digit en-free,		complia	int, and	termina	tions le

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-10WQ045FNHM3	75	3000	Antistatic plastic tube				
VS-10WQ045FNTRHM3	2000	2000	13" diameter reel				
VS-10WQ045FNTRRHM3	3000	3000	13" diameter reel				
VS-10WQ045FNTRLHM3	3000	3000	13" diameter reel				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?95033				

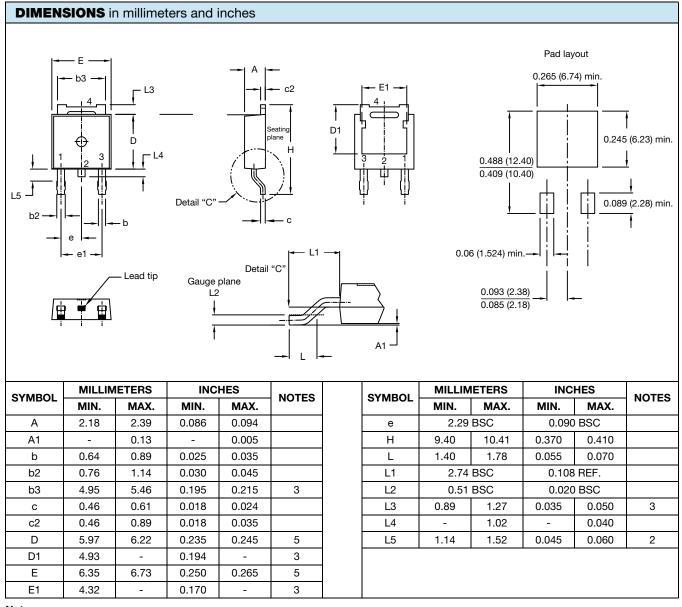
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Outline Dimensions



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DPAK (TO-252AA)



Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁵⁾ Outline conforms to JEDEC[®] outline TO-252AA, except for D1 dimension



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