PCN Number:			201	20170616006		<b>PCN Date:</b>	Ju	June 20, 2017		17	
Title: Datasheet for			r TPS5	4620							
<b>Customer Contact:</b>			PCN A	PCN Manager				Dept:		Quality Services	
Change Type:											
	Asse	embly Site			Design				Wafer Bump Site		
Assembly Process			□ Data Sheet □			Wafer	Bump Material				
Assembly Materials			Part number change				Wafer	Bump Process			
Mechanical Specification				Test Site				Wafer	Fab Site		
Packing/Shipping/Labeling				Test Proce	ess			Wafer	Fab Materials		
										Wafer	Fab Process
	Notification Details										

# Notification Details

## Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



TPS54620

SLVS949F -MAY 2009-REVISED MAY 2017

#### Changes from Revision E (June 2016) to Revision F

Page

•	Updated data sheet text to our latest documentation and translations standards	. 1
•	Removed all references to the SwitcherPro™ Software Tool because it is no longer available for this part	. 1
•	Moved storage temperature ratings to the Absolute Maximum Ratings table	5
•	Changed Handling Ratings table to ESD Ratings	5
•	Changed RHY package to RHL in the Thermal Information table	6
•	Changed RGY values in the Thermal Information table	6
•	Updated packages in the last bullet point of Layout Guidelines	34
	Added information to the last list item in Layout Guidelines	34

#### From:

### 7.4 Thermal Information

THERMAL METRIC (1)		RGY	RHY	LINET
	THERMAL METRIC**	14 PINS	14 PINS	UNIT
R <sub>BJA</sub>	Junction-to-ambient thermal resistance	36.9	43.4	°C/W
R <sub>6JCtop</sub>	Junction-to-case (top) thermal resistance	48.7	45.7	°C/W
R <sub>6JB</sub>	Junction-to-board thermal resistance	13.9	20.0	°C/W
γлт	Junction-to-top characterization parameter	0.6	0.7	°C/W
<b>У</b> ЈВ	Junction-to-board characterization parameter	14.2	20.0	°C/W
Reschot	Junction-to-case (bottom) thermal resistance	3.1	2.6	°C/W

#### To:

#### 6.4 Thermal Information

		TPS5		
	THERMAL METRIC <sup>(1)</sup>	RGY (VQFN)	RHL (VQFN)	UNIT
		14 PINS	14 PINS	
R <sub>0JA</sub>	Junction-to-ambient thermal resistance	40.1	40.1	°C/W
ReJCtop	Junction-to-case (top) thermal resistance	34.4	34.4	°C/W
$R_{\theta JB}$	Junction-to-board thermal resistance	11.4	11.4	°C/W
ΨJT	Junction-to-top characterization parameter	0.5	0.5	°C/W
ΨЈВ	Junction-to-board characterization parameter	11.4	11.4	°C/W
ReJCbot	Junction-to-case (bottom) thermal resistance	1.8	1.8	°C/W

The datasheet number will be changing.

Device Family	Change From:	Change To:
TPS54620	SLVS949E	SLVS949F

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/TPS54620

#### **Reason for Change:**

To accurately reflect device characteristics.

## Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

## Changes to product identification resulting from this PCN:

None.

	Prod	luct	Affect	ted:

905-5462001	TPS54620RGYR	TPS54620RGYT	TPS54620RHLR
TPS54620RHLT			

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com