## Product Bulletin

Document \# : PB22549X
Issue Date: 12 December 2018

| Title of Change: | Datasheet update for the NB3H5150/NB3H5150-01 Series. CLK4 100MHz, 125MHz and 156.25MHz <br> frequency selections have potential issue of starting up at incorrect frequency. |
| :--- | :--- | :--- |
| Effective date: | 12 December 2018 |
| Contact information: | Contact your local ON Semiconductor Sales Office or [e.rupnow@onsemi.com](mailto:e.rupnow@onsemi.com) |
| Type of notification: | This Product Bulletin is for notification purposes only. ON Semiconductor will proceed with <br> implementation of this change upon publication of this Product Bulletin. |
| Change Category: | $\square$ Wafer Fab $\quad \square$ Assembly Change $\quad \square$ Test Change $\quad \nabla$ Other $\quad$ Datasheet |


| Change Sub-Category(s): Manufacturing Site Addition Manufacturing Site Transfer Manufacturing Process Change | Material Ch Product spe | Datasheet/Product Doc change <br> Shipping/Packaging/Marking <br> Other: |
| :---: | :---: | :---: |
| Sites Affected: | ON Semiconductor Sites: None | External Foundry/Subcon Sites: None |

## Description and Purpose:

The purpose of this notification is to inform customers about a product / data sheet update. A design issue was discovered on these products, where at power up the CLK 4 frequencies of $100 \mathrm{MHz}, 125 \mathrm{MHz}$ and 156.26 MHz could potentially have an incorrect frequency.

- CLK4 100 MHz frequency has the potential to start up at 250 MHz .
- CLK4 125 MHz frequency has the potential to start up at 312.5 MHz .
- CLK4 156.25 MHz frequency has the potential to start up at 312.5 MHz .

The CLK1, 2 or 3 frequencies are not impacted by this issue nor are other frequencies that can be generated on CLK4.
Power cycling the part will correct the CLK4 output if an incorrect frequency is observed at power up, multiple power cycles could be required. Once the part is powered up with correct frequency, the frequency will remain stable and will not change.

This issue can only occur at power up of the part and does not affect the reliability of the product.

> Current NB3H5150-01 Datasheet

Table 4. NB3H5150-01MNTXG - CLK4A \& CLK4B OUTPUT FREQUENCY SELECT TRUTH TABLE (MHz) WITH 25 MHz CRYSTAL*

| FS4A | FS4B | CLK4 (MHz) | Divider Type |
| :---: | :---: | :---: | :---: |
| Low | Low | 33.33 (LVCMOS) | Integer |
| Low | Mid / Float | 66.66 (LVCMOS) | Fractional |
| Low | High | 133.33 (LVCMOS) | Fractional |
| Mid / Float | Low | 133.33 (LVPECL) | Fractional |
| Mid / Float* | Mid / Float* | 156.25 (LVPECL) | Integer |
| Mid / Float | High | 125.00 (LVPECL) | Integer |
| High | Low | 25.00 (LVPECL) | Integer |
| High | Mid / Float | 100.00 (LVPECL) | Integer |
| High | High | 161.1328 (LVPECL) | Fractional |

## New NB3H5150-01 Datasheet

Table 4. NB3H5150-01MNTXG - CLK4A \& CLK4B OUTPUT FREQUENCY SELECT TRUTH TABLE (MHz) WITH 25 MHz CRYSTAL*

| FS4A | FS4B | CLK4 (MHz) | Divider Type |
| :---: | :---: | :---: | :---: |
| Low | Low | 33.33 (LVCMOS) | Integer |
| Low | Mid/Float | 66.66 (LVCMOS) | Fractional |
| Low | High | 133.33 (LVCMOS) | Fractional |
| Mid/Float | Low | 133.33 (LVPECL) | Fractional |
| Mid / Float* | Mid / Float* | $\mathbf{1 5 6 . 2 5}$ (LVPECL) | Integer ** |
| Mid/Float | High | 125.00 (LVPECL) | Integer ** |
| High | Low | 25.00 (LVPECL) | Integer |
| High | Mid/Float | 100.00 (LVPECL) | Integer ** |
| High | High | 161.1328 (LVPECL) | Fractional |

*(Default)
** These frequencies selections are not recommended for use.
They have potential to start up at incorrect frequency ( 2 or $2.5 x$ desired frequency). Multiple power cycles maybe required to achieve the correct frequency.

| Current NB3H5150 Datasheet |  |  |  | New NB3H5150 Datasheet |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Table 4. NB3H5150 - CLK4A \& CLK4B OUTPUT FREQUENCY SELECT TRUTH TABLE (MHz) WITH 25 MHz CRYSTAL* |  |  |  | Table 4. NB3H5150 - CLK4A \& CLK4B OUTPUT FREQUENCY SELECT TRUTH TABLE (MHz) WITH 25 MHz CRYSTAL* |  |  |  |
| FS4A | FS4B | CLK4 (MHz) | Divider Type | FS4A | FS4B | CLK4 (MHz) | Divider Type |
| Low | Low | 33.33 (LVCMOS) | Integer | Low | Low | 33.33 (LVCMOS) | Integer |
| Low | Mid / Float | 66.66 (LVCMOS) | Fractional | Low | Mid/ Float | 66.66 (LVCMOS) | Fractional |
| Low | High | 133.33 (LVCMOS) | Fractional | Low | High | 133.33 (LVCMOS) | Fractional |
| Mid/Float | Low | 155.52 (LVPECL) | Fractional | Mid / Float | Low | 155.52 (LVPECL) | Fractional |
| Mid/ Float ${ }^{\text {+ }}$ | Mid/ Float* | 156.25 (LVPECL) | Integer | Mid/Float* | Mid/ Float ${ }^{\text {P }}$ | 156.25 (LVPECL) | Integer ** |
| Mid / Float | High | 125.00 (LVPECL) | Integer | Mid / Float | High | 125.00 (LVPECL) | Integer ** |
| High | Low | 106.25 (LVPECL) | Fractional | High | Low | 106.25 (LVPECL) | Fractional |
| High | Mid/Float | 100.00 (LVCMOS) | Integer | High | Mid/ Float | 100.00 (LVCMOS) | Integer ** |
| High | High | 161.1328 (LVPECL) | Fractional | High | High | 161.1328 (LVPECL) | Fractional |
| ${ }^{\text {'(Default) }}$ |  |  |  | *(Default) <br> *\# These <br> They hav desired f achieve | uencies tential to ency). rrect fre | ions are not rec up at incorrect e power cycles cy. | nded for use. ncy (2 or 2.5x required to |
| List of Affected Parts: |  |  |  |  |  |  |  |
| NB3H5150-01MNTXG |  |  |  |  |  |  |  |

## Appendix A: Changed Products

NB3H5150-01MNTXG
NB3H5150MNTXG

