PCN Number: 202		200727000.1		PCN I	N Date:		Aug. 3, 2020	
Title: Qualification of RF.		AB as an additional Fab site option for select LBC7 devices						
Customer Contact:			PCN Manager		Dept:			Quality Services
Proposed 1 st Ship Date:		Nov. 3, 2020 Estimated Availability		ted Sample pility:		le	Date provided at sample request.	
Change Type:								
Assembly Site		Assembly Process		Assembly Materials				
Design		☐ Electrical Specification			Mechanical Specification			
Test Site		Packing/Shipping/Labeling Test Process		st Process				
☐ Wafer Bump Site		Wafer Bump Material				Wa	Wafer Bump Process	
		\boxtimes					Wa	fer Fab Process
			Part number change					

PCN Details

Description of Change:

Texas Instruments is pleased to announce the qualification of its RFAB fabrication facility as an additional Wafer Fab source for the selected devices listed in the "Product Affected" section.

	Current Site		Additional Site		
Current Fab Process Site		Wafer Diameter	Additional Fab Site	Process	Wafer Diameter
FFAB	LBC7	200 mm	RFAB	LBC7	300 mm

Qual details are provided in the Qual Data Section.

Reason for Change:

Continuity of Supply

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

Changes to product identification resulting from this PCN:

Current:

Current Chip Site	Chip Site Origin Code (20L)	Chip Site Country Code (21L)	Chip Site City
FR-BIP-1	TID	DEU	Freising

New Fab Site:

New Chip Site	RFB	Crip Site Country Code (21L)	Richardson
New Chin Site	Chin Site Origin Code (201)	Chip Site Country Code (21L)	Chin Site City

Sample product shipping label (not actual product label)





(1P) \$N74L\$07N\$R

(Q) 2000 (D) 0336

(31T)LOT: 3959047MLA

(4W) TKY(1T) 7523483\$12

(P)

(2P) REV: (V) 0033347

(20L) CSO: SHE (21L) CCO:USA

(22L) ASO: MICA (23L) ACO: MYS

Product Affected:

TLV7031DCKR	TLV7041DPWR	TPS22929DDBVT	TPS54519RTET
TLV7031DPWR	TPS22929DDBVR	TPS54519RTER	TS3A225ERTER

Qualification Report

Approve Date 6-October-2010

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	Test Name / Condition	Duration	Qual Device: TPS51217DSC
ED	Electrical Characterization	Per Datasheet Parameters	Pass
HAST	Biased HAST, 130C/85%RH	96 Hours	3/231/0
AC	Autoclave, 121C	96 Hours	3/231/0
HBM	ESD - HBM	2000 V	3/9/0
CDM	ESD - CDM	500 V	3/9/0
HTOL	Life Test, 135C	635 Hours	3/231/0
HTSL	High Temp. Storage Bake, 170C	420 Hours	3/231/0
LU	Latch-up	(per JESD78)	3/18/0
TC	Temperature Cycle, -65/150C	500 Cycles	3/18/0

- Qual Device TPS51217DSC is qualified at LEVEL2-260C
- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on activation energy of 0.7 eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on activation energy of 0.7 eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Green/Pb-free Status:

Qualified Pb-Free (SMT) and Green

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