PCN Number: 201				191104001.2				N Da	ite:	Nov 13, 2019	
Title: Conversion to TSMC 0.6/0.5um Hybrid Process											
Customer	Contact:		PCN	l Manager			Dept:			Quality Services	
Droposed	1 st Ship Date:		Max	, 12 2020	E	Estima	ted	San	nple	Date provided at	
Proposed	1 Silip Date.		May 13, 2020 Availab			bility:			sample request.		
Change Type:											
	nbly Site			Assembly Proc			Asse			mbly Materials	
Desig			Щ	Electrical Specification				Mechanical Specificat			
Test S			Щ	Packing/Shippi				Щ	Process		
	Bump Site			Wafer Bump M						r Bump Process	
Wafer	Fab Site		Щ	Wafer Fab Mat				\boxtimes	Wafe	r Fab Process	
				Part number cl							
				Notification	n De	<u>tails</u>					
Descriptio	n of Change:										
metallizatio	n/SOG Etch Ba	ck pr	oces		.5um					0.6um back end end process for the	
	Change	e Fro	m					Ch	ange '	То	
C	.6um TSMC Ba	cken	d Pro	cess		0.5	um T	SMO	C Back	end Process	
IME) layer: PEOX +				IMI	IMD layer: PEOX+SACVD-OX+PEOX+S					
	Metal: Ti / A	AlSiCu	ı / Ti	iN	dep. & Etch bad						
						Metal:	Via l	Plug	TiN/W	CVD/AlCu /TiN	
Reason fo	r Change:										
Quality Imp	provement.										
Anticipate	d impact on F	it, Fo	orm,	Function, Qua	lity c	r Reli	abili	ty (positi	ve / negative):	
None.											
Changes t	o product ide	ntific	atio	n resulting fro	m thi	is noti	ficat	ion	:		
None.											
Product At	ffected:										

Automotive New Product Qualification Summary

V62/07602-01XE

V62/07602-02XE

V62/07602-03XE

(As per AEC-Q100 and JEDEC Guidelines)

Q100H Grade-1 qual for REF31XXAQDBZRQ1 (TSMC-WF2 / 0.5/0.6-DPDM) in HNT using 3-pin SOT pkg Approved 28-Mar-2017

Product Attributes

Attributes	Qual Device: REF3133AQDBZRQ1	Qual Device: REF3112AQDBZRQ1	Qual Device: REF3120AQDBZRQ1	Qual Device: REF3125AQDBZRQ1	Qual Device: REF3130AQDBZRQ1	Qual Device: REF3140AQDBZRQ1	QBS Process Reference: OPA356AQDBVRQ1
Operating Temp Range	-40 to +125 C						
Automotive Grade Level	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1 Grade 1		Grade 1
Product Function	Power Management	Signal Chain					
Wafer Fab Supplier	TSMC-WF2						
Die Revision	E	E	E	E	E	E	-
Assembly Site	HNT	HNT	HNT	HNT	HNT	HNT	NFME
Package Type	SOT						
Package Designator	DBZ	DBZ	DBZ	DBZ	DBZ	DBZ	DBV
Ball/Lead Count	3	3	3	3	3	3	5

⁻ QBS: Qual By Similarity

REF3212AMDBVREP

REF3220AMDBVREP

REF3225AMDBVREP

REF3230AMDBVREP

REF3233AMDBVREP

REF3240AMDBVREP

V62/07602-04XE

V62/07602-05XE

V62/07602-06XE

⁻ Qual Device REF3112AQDBZRQ1 is qualified at LEVEL2-260C
- Qual Devices qualified at LEVEL3-260C: REF3120AQDBZRQ1, REF3130AQDBZRQ1, REF3140AQDBZRQ1, REF3125AQDBZRQ1, REF3133AQDBZRQ1

Qualification Results Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Spec	Min Lot Qty	SS/ Lot	Test Name / Condition	Duration	Qual Device: REF3133AQDBZRQ1	Qual Device: REF3112AQDBZRQ1	Qual Device: REF3120AQDBZRQ1	Qual Device: REF3125AQDBZRQ1	Qual Device: REF3130AQDBZRQ1	Qual Device: REF3140AQDBZRQ1	QB \$ Process Reference: OPA356AQDBVRQ1
Test Group	A-Ac	celerated Environme	ent Stre	ss Test	8								
PC	A1	JEDEC J-STD- 020 JESD22- A113	3	77	Automotive Preconditioning	Level 2-260C peak	3/all/0	-	-	-	-	-	3/aII/0
HAST	A2	JEDEC JESD22- A110	3	77	Biased HAST, 130C/85%RH	96 Hours	3/231/0	-	-	-	-	-	3/231/0
AC	A3	JEDEC JESD22- A102	3	77	Autoclave 121C	96 Hours	3/231/0		-		-	-	3/230/0
TC	A4	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle, -65/150C	500 Cycles	3/231/0	-	-	-	-	-	3/230/0
TC-BP	A4	MIL-STD883 Method 2011	1	30	Post Temp. Cycle Bond Pull	500 Cycles	1/30/0	-	-	-	-	-	1/30/0
PTC	A5	JEDEC JESD22- A105	1	45	Power Temperature Cycle	1000 Cycles	N/A	N/A	N/A	N/A	N/A	N/A	-
HTSL	A6	JEDEC JESD22- A103	1	45	High Temp Storage Bake 175C	500 Hours	1/45/0	-		-	-	-	1/45/0
Test Group	B-Ac	celerated Lifetime S	imulatio	on Tests	8								
HTOL	B1	JEDEC JESD22- A108	3	77	Life Test, 125C	1000 Hours	3/231/0	-	•	1	-	-	3/231/0
ELFR	B2	AEC Q100-008	3	800	Early Life Failure Rate, 125C	48 Hours	1	-	-	-	-	-	3/2400/0
EDR	В3	AEC Q100-005	3	77	NVM Endurance, Data Retention, and Operational Life	-	N/A	N/A	N/A	N/A	N/A	N/A	-
est Group	C – Pac	kage Assembly Inte	arity Te	sts									
	_	AEC 0400 004	, ,		2 and Share (Cally 4 S7)	\A6	4/20/0						

Test G	Test Group C – Package Assembly Integrity Tests												
WE	ss c	1 AEC Q100-001	1	30	Bond Shear (Cpk>1.67)	Wires	1/30/0	-	-	-	-	-	-
WE	BP C	2 MIL-STD883 Method 2011	1	30	Bond Pull (Cpk>1.67)	Wires	1/30/0	-	-	-	-	-	-
SE) C	B102	1	15	Solderability (>95% Coverage)	Steam aging 8 hrs	-	-	-	-	-	-	1/15/0*
PE	o c	4 JEDEC JESD22- B100 and B108	3	10	Physical Dimensions (Cpk>1.67)	-	3/30/0	-	-	-	-	-	-
SB	S C		3	50	Solder Ball Shear (Cpk>1.67)	Post HTSL/Bump	NA	-	-	-	-	ı	-
LI	. -	B105	1	50	Lead Integrity	Leads	NA	-	-	-	-	•	-
Test Gro	oup D – I	Die Fabrication Reliability	/ Tests										
EN	M D	1 JESD61	-	-	Electromigration		Completed Per Process Technology Requirements	Completed Per Process Technology Requirements					
TDI	DB D	2 JESD35	-		Time Dependent Dielectric Breakdown		Completed Per Process Technology Requirements	Completed Per Process Technology Requirements					
но	OI D	3 JESD60 & 28	-	-	Hot Injection Carrier		Completed Per Process Technology Requirements	Completed Per Process Technology Requirements					
NB	ті Б	4 -	-	-	Negative Bias Temperature Instability		Completed Per Process Technology Requirements	Completed Per Process Technology Requirements					
SN			-	-	Stress Migration		Completed Per Process Technology Requirements	Completed Per Process Technology Requirements					
Test G	roup E	 Electrical Verification 	n Tests										
HB	M E	2 AEC Q100-002	1	3	ESD - HBM	2000 V	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0
CD	МЕ	3 AEC Q100-011	1	3	ESD - CDM	500 V (all pins) 750V (corner pins)	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0
LU	J E	4 AEC Q100-004	1	6	Latch-up	(Per AEC Q100- 004)	1/6/0	1/6/0	1/6/0	1/6/0	1/6/0	1/6/0	1/6/0
E	D E	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold test	1/30/0	1/30/0	1/30/0	1/30/0	1/30/0	1/30/0	3/90/0
Made: Co	nte: Soldershift is nerformed on a senarate davice SN74LVC2GRODCLIRO1 which has same lead frame plating material on same hardsone type by same assembly site												

Note: Solderability is performed on a separate device. SN74LVC2G66QDCURQ1, which has same lead frame plating material on same package type by same assembly site.

A1 (PC): Preconditioning:

Performed for THB, Biased HAST, AC, uHAST &TC samples, as applicable.

Ambient Operating Temperature by Automotive Grade Level: Grade 0 (or E): -40°C to +150°C

Grade 1 (or Q): -40°C to +125°C

Grade 2 (or T): -40°C to +105°C Grade 3 (or I): -40°C to +85°C

El (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):
Room/Hot/Cold: HTOL, ED

Room/Hot: THB/HAST, TC/PTC, HTSL, ELFR, ESD & LU

Room: AC/uHAST

Green/Pb-free Status:

Qualified Pb-Free (SMT) and Green

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