Notification Number:20200820002Notification Date:Aug. 24, 2020Title:Datasheet for F28M36P63C2, F28M36P53C2, F28M36H53B2, F28M36H33B2Customer Contact:PCN ManagerDept:Quality Services

Change Type: Electrical Specification

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification.

The product datasheet(s) is being updated as summarized below.

The following change history provides further details.



F28M36P63C2, F28M36P53C2 F28M36H53B2, F28M36H33B2

SPRS825F - OCTOBER 2012-REVISED JUNE 2020

Chang	s from December 15, 2017 to June 23, 2020 (from E Revision (December 2017) to F Revision)	age
	Global: Removed F28M36H53C2 and F28M36H33C2. Global: Replaced "emulator" with "JTAG debug probe". Section 1.1 (Features): Added "Three External Interrupts" feature. Section 1.2 (Applications): Updated section. Gection 3.1 (Related Products): Updated section. Gable 4-1 (Signal Descriptions): Updated DESCRIPTION of XRS, EMU0, and EMU1. Gection 5.2 (ESD Ratings – Commercial): Changed title from "ESD Ratings" to "ESD Ratings – Commercial". Gection 5.2: Added ANSI/ESDA/JEDEC JS-002 to charged-device model (CDM). Gection 5.9.1.1 (Power Management and Supervisory Circuit Solutions): Updated section. Gection 5.10.1.5 (ADC Electrical Data and Timing): Removed "Typical ADC Total Error" figure (was Figure 5-12 in SPRS825E). Gable 5-38 (Electrical Characteristics of the Comparator/DAC): Changed "Comparator response time to PWM Trip Zone (Async)" parameter to "Comparator response time to GPIO". Gable 5-75 (SPI Master Mode External Timing (Clock Phase = 0)): Updated MIN value (for both BRR EVEN and BRR ODD) for Parameter 23, t _{d(SPC)M} . Gable 5-76 (SPI Master Mode External Timing (Clock Phase = 1)): Updated MIN value (for both BRR EVEN and BRR ODD) for Parameter 23, t _{d(SPC)M} . Gable 6-7 (Control Subsystem Flash, ECC, OTP, Boot ROM): Removed references to 256KB Flash from 2010 0000 to 0011 FFFF. Changed SIZE (BYTES) of EPI0 from 2G to 2M. Added footnote about Control Subsystem having less address reach to EPI memory than the Master Subsystem.	1 1 9 15 42 52 73 76 147
:	Fable 6-8 (Master Subsystem Flash, ECC, OTP, Boot ROM): Updated M ADDRESS range 0068 0480 to 0070 01FF. Fable 6-8: Removed references to 256KB Flash from 0022 0000 to 002D FFFF. Fable 6-11 (Master Subsystem Analog and EPI): Added footnote about Control Subsystem having less address reach to EPI memory than the Master Subsystem. Section 6.18 (Code Security Module): Updated section. Section 7.1 (TI Reference Design): Changed section title from "TI Design or Reference Design" to "TI Reference Design". Updated section. Section 8 (Device and Documentation Support): Changed "Community Resources" section to "Support Resources" section. Updated section.	171 240 244
:	Section 8.2 (Tools and Software): Updated section. Section 8.3 (Documentation Support): Updated section. Section 8.4 (Related Links): Updated section.	246 247

The datasheet number will be changing.

Device Family	Change From:	Change To:
F28M36P63C2, F28M36P53C2 F28M36H53B2, F28M36H33B2	SPRS825E	SPRS825F

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/F28M36P63C2

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):							
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.							
Changes to product identification resulting from this notification:							
None.							
Product Affected:							
F28M36H33B2ZWTT	F28M36H53B2ZWTT	F28M36P53C2ZWTS	F28M36P53C2ZWTT				
F28M36P63C2ZWTS	F28M36P63C2ZWTT						

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