PCN Number:	20160812001	**PCN Date:**	Sept. 26, 2016
Title:	Datasheet for OPA4188		
Customer Contact:	PCN Manager	**Dept:**	Quality Services
Proposed 1st Ship Date:	Dec. 26, 2016		
Change Type:			

☐	Assembly Site	☐	Design	☐	Wafer Bump Site
☐	Assembly Process	☒	Data Sheet	☐	Wafer Bump Material
☐	Assembly Materials	☐	Part number change	☐	Wafer Bump Process
☐	Mechanical Specification	☐	Test Site	☐	Wafer Fab Site
☐	Packing/Shipping/Labeling	☐	Test Process	☐	Wafer Fab Materials
				☐	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification.

The product datasheet(s) is being updated as summarized below.

The following change history provides further details.

TEXAS INSTRUMENTS

OPA4188

SBOS641D – JUNE 2012 – REVISED SEPTEMBER 2016

Changes from Revision C (April 2015) to Revision D **Page**

- Changed high supply over-temperature input bias current limit in *High-Voltage Operation Electrical Characteristics* table ... 6
- Changed high supply noise units in *High-Operating Voltage Electrical Characteristics* table ... 6
- Changed high supply room-temperature quiescent current limit in *High-Voltage Operation Electrical Characteristics* table ... 7
- Changed high supply over-temperature quiescent current limit in *High-Voltage Operation Electrical Characteristics table* ... 7
- Changed low supply over-temperature input bias current limit in *Low-Voltage Operation Electrical Characteristics table* ... 7
- Changed low supply noise units for input voltage noise density parameter in *Low-Voltage Operation Electrical Characteristics* table ... 7
- Changed low supply room-temperature quiescent current limit in *Low-Voltage Operation Electrical Characteristics* table ... 8
- Changed low supply over-temperature quiescent current limit in *Low-Voltage Operation Electrical Characteristics* table ... 8

	PARAMETER		CONDITIONS	PREVIOUS REVISION				NEW REVISION			
				MIN	TYP	MAX	UNIT	MIN	TYP	MAX	UNIT
V_S = ±4 V to ±18 V	**INPUT BIAS CURRENT**										
	I_B	Input bias current	V_{CM} = V_S / 2		±160	±1400	pA		±160	±1400	pA
			T_A = -40°C to +125°C			±8	nA			±18	nA
	I_{OS}	Input offset current			±320	±2800	pA		±320	±2800	pA
			T_A = -40°C to +125°C			±6	nA			±6	nA
	POWER SUPPLY										
	I_Q	Quiescent Current (per amplifier)	V_S = ±4 V to VS = ±18 V		415	475	µA		415	500	µA
			I_O = 0 mA, T_A = -40°C to +125°C			525	µA			570	µA
V_S = ±2 V to ±4 V	**INPUT BIAS CURRENT**										
	I_B	Input bias current	V_{CM} = V_S / 2		±160	±1400	pA		±160	±1400	pA
			T_A = -40°C to +125°C			±8	nA			±18	nA
	I_{OS}	Input offset current			±320	±2800	pA		±320	±2800	pA
			T_A = -40°C to +125°C			±6	nA			±6	nA
	POWER SUPPLY										
	I_Q	Quiescent Current (per amplifier)	V_S = ±2 V to VS = ±4 V		385	440	µA		385	465	µA
			I_O = 0 mA, T_A = -40°C to +125°C			525	µA			540	µA

The datasheet number will be changing.

Device Family	Change From:	Change To:
OPA4188	SBOS641C	**SBOS641D**

These changes may be reviewed at the datasheet links provided.
http://www.ti.com/product/OPA4188

Reason for Change:

To more accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

OPA4188AID	OPA4188AIDR	OPA4188AIPW	OPA4188AIPWR

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com