

Customer Information Notification

Issue Date: 17-Nov-2020 Effective Date: 18-Nov-2020

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Change Category							
[] Wafer Fab Process	[] Assembly	[] Product Marking	[] Test Location	[] Design			
	Process						
[] Wafer Fab Materials	[]	[] Mechanical	[]Test	[] Errata			
	Assembly	Specification	Process				
	Materials	·					
[] Wafer Fab Location	[]	[]	[]Test	[] Electrical			
	Assembly	Packing/Shipping/Labeling	Equipment	spec./Test			
	Location			coverage			
[] Firmware	[X] Other - Data Sheet Update (Improvements and Clarification						

PF8101_PF8201 Data Sheet Update Rev 3.0 (Improvements and Clarifications)

Description

NXP Semiconductors announces the data sheet update to revision 3.0 for the PF8101, PF8201 and derived part numbers associated with this notification.

The new data sheet revision provides updated specification information and clarifications. This includes tightening the output accuracy specification on all switching regulators, and additional information to complement the device functional description and overall system level implementation of the PF8101 and PF8201 devices.

The revision history included in the updated document provides a detailed description of the changes.

PF8101_PF8201 Revision 3.0 Data Sheet Change Summary:

- 1. Clarify the minimum specification on the SWxLX pins as the minimum DC voltage allowed in the pin. Additional note is added to clarify that the SWxLX pins are tolerant to negative transient spike during the dead band time with expectable fast transients as low as -3.0V.
- 2. Added new transitions to the State machine transition definition table in order to clarify missing conditions related to the XFAILB during power up and power down events.

- 3. Add a section to clarify how the peak current limit specification on all switching regulators should be used by the customer to calculate the DC current limitation.
- 4. Output accuracy of the Type1 Buck regulators (SW1 SW6) is split into four operating ranges to allow tighter accuracy at higher output voltage configuration. Output voltages from 1.0V to 1.8V provide a spec improvement from +/-2.0% to +/-1.5% output accuracy.
- 5. Update VSWxACC values and conditions.
- 6. Add a note to clarify that the Switching regulators can be operated above the nominal currents as long as they do not reach the current limitation threshold.
- 7. Add a section on the Clock management to clarify the system level implementation to handle an external Clock out of range failure.

The PF8101_PF8201 revision 3.0 data sheet is attached to this notice and is available at: https://www.nxp.com/docs/en/data-sheet/PF8101 PF8201.pdf

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02, SEM-DS-03

Reason

Switching regulator accuracy specification adds one more operating range to provide spec improvement on output voltages from 1.0 to 1.8V. Clarifications are provided to complement missing or unclear functional specifications.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Data Sheet Revision

A new datasheet will be issued

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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Changed Orderable Part#	Changed Part 12NC	Changed Part Number	Changed Part Description	Package Outline	Package Name	Status	Product Line
MC33PF8101A0ES	935368343557	MC33PF8101A0ES	PF8100	SOT684-21(DD/SC)	HVQFN56	RFS	Safety & Power Mgmt
MC33PF8201A0ES	935368347557	MC33PF8201A0ES	PF8200	SOT684-21(DD/SC)	HVQFN56	RFS	Safety & Power Mgmt
MC34PF8101A0EP	935383915557	MC34PF8101A0EP	PF8100	SOT684-21	HVQFN56	RFS	Safety & Power Mgmt