September **PCN Number:** 20210901000.2 **PCN Date:** 01, 2021 Title: Qualification of MLA as an alternate Assembly & Test site for Select Devices **Customer Contact:** PCN Manager Dept: **Quality Services Estimated Sample** Date provided at **Proposed 1<sup>st</sup> Ship Date:** Feb 28, 2022 **Availability:** sample request **Change Type:** Assembly Site Wafer Bump Site Design Assembly Process **Data Sheet** Wafer Bump Material Part number change **Assembly Materials** Wafer Bump Process **Mechanical Specification** Test Site Wafer Fab Site Packing/Shipping/Labeling Test Process Wafer Fab Materials Wafer Fab Process

### **PCN Details**

### **Description of Change:**

Texas Instruments Incorporated is announcing the qualification of TI Malaysia as an additional Assembly & Test site for the list of devices shown below. There are no construction differences between the two sites.

Test coverage, insertions, conditions will remain consistent with current testing and verified with test MQ

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### **Reason for Change:**

Supply continuity

### Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

### **Impact on Environmental Ratings**

Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.

RoHS	REACH	Green Status	IEC 62474
No Change	🛮 No Change	No Change	No Change     ■     No Change     No Change     ■     No Change     No Chan

### Changes to product identification resulting from this PCN:

Assembly Site	Assembly Site Origin (22L)	Assembly Country Code (23L)	Assembly City		
TI Taiwan	TAI	TWN	Chung Ho, New Taipei City		
TI Malaysia	MLA	MYS	Kuala Lumpur		

Sample product shipping label (not actual product label)





(1P) \$N74L\$07N\$R (Q) 2000 (D) 0336 (31T)LOT: 3959047MLA (4W) TKY(1T) 7523483\$I2 (P) (2P) REV: (V) 0033317 (20L) C\$0: SHE (21L) CCO:U\$A (22L) ASO: MLA (23L) ACO: MY\$

Product Affected:								
LP8861QPWPRQ1	LP8867QPWPRQ1	LP8869QPWPRQ1	TPS61194PWPRQ1					
LP8862QPWPRQ1	LP8869CQPWPRQ1	TPS61193PWPRQ1	TPS71133PWPRQ1					
LP8867CQPWPRQ1								



TI Information Selective Disclosures

Automotive New Product Qualification Summary (As per AEC-Q100, Q006 and JEDEC Guidelines)

# TPS71133PWPRQ1, LP8861QPWPRQ1, LP8862QPWPRQ1, TPS61193PWPRQ1, TPS61194PWPRQ1, LP8867CQPWPRQ1, LP8867QPWPRQ1, LP8869QPWPRQ1, LP8869QPWPRQ1 Qualification

### Approved 10-Aug-2021 Updated 8/10/2021-Added QBS Data

### **Product Attributes**

Attributes	Qual Device: TPS71133PWPRQ1	QBS Product Reference: PLP8861QPWPRQ1	QBS Process Reference: S0704038C0PLPR
Operating Temp Range	-40 to +125 C	-40 to +125 C	-40 to +125 C
Automotive Grade Level	Grade 1	Grade 1	Grade 1
Wafer Fab Supplier	RFAB	RFAB	RFAB
Die Revision	A	A	C1
Assembly Site	MLA	TAI	TAI
Package Type	HTSSOP	HTSSOP	TQFP
Package Designator	PWP	PWP	PLP
Ball/Lead Count	20	20	128

<sup>-</sup> QBS: Qual By Similarity

<sup>-</sup> Qual Device TPS61194PWPRQ1, LP8867CQPWPRQ1, LP8861QPWPRQ1, LP8862QPWPRQ1, LP8869CQPWPRQ1, TPS61193PWPRQ1, LP8867QPWPRQ1, LP8869QPWPRQ1 and TPS71133PWPRQ1 are qualified at LEVEL2-260C. TPS71133PWPRQ1 represents the device family.

Туре	#	Test Spec	Min Lot Qty	SS/Lot	Test Name / Condition	Duration	Qual Device: TP\$71133PWP RQ1	QBS Product Reference: PLP8861QPWP RQ1	QBS Process Reference: <u>\$0704038C0PLPR</u>
			3	1	Cross Section, Post T/C 500 Cycles	Completed	3/3/0	-	-
			3	22	SAM Analysis, Post T/C 500 Cycles	Completed	3/66/0	-	-
			3	30	Wire Bond Shear, Post T/C 500 Cycles	Wires	3/90/0	-	-
			3	30	Bond Pull over Stitch, Post T/C 500 Cycles	Wires	3/90/0	-	-
			3	30	Bond Pull over Ball Post T/C 500 Cycles	Wires	3/90/0		-
тс	A4	JEDEC JESD22- A104	3	70	Temperature Cycle, -85/150C	1000 Cycles	3/231/0	1	-
			3	1	Cross Section, Post T/C 1000 Cycles	Completed	3/3/0	-	-
			3	22	SAM Analysis, Post T/C 1000 Cycles	Completed	3/66/0	-	-
			3	30	Wire Bond Shear, Post T/C 1000 Cycles	Wires	3/90/0	-	-
			3	30	Bond Pull over Stitch, Post T/C 1000 Cycles	Wires	3/90/0	-	-
			3	30	Bond Pull over Ball, Post T/C 1000 Cycles	Wires	3/90/0	-	-
PTC	A5	JEDEC JESD22- A105	1	45	Power Temperature Cycle, -40/125C	1000 Cycles	-	-	3/45/0
HTSL	A6	JEDEC JESD22- A103	3	45	High Temp Storage Bake 150C	1000 Hours	3/231/0	-	-
			3	1	Cross Section, Post HTSL 1000 Hours	Completed	3/3/0	-	-
HTSL	A6	JEDEC JESD22- A103	3	45	High Temp Storage Bake 175C	500 Hours	-	1/77/0	3/45/0
HTSL	A6	JEDEC JESD22- A103	3	45	High Temp Storage Bake 150C	2000 Hours	3/231/0	-	-
			3	1	Cross Section, Post HTSL 2000 Hours	Completed	3/3/0	-	-
HTSL	A6	JEDEC JESD22- A103	3	45	High Temp Storage Bake 175C	1000 Hours	-	-	-

## Qualification Results Data Displayed as: Number of lots / Total sample size / Total failed

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	Туре	#	Test Spec	Min Lot Qty	SS/Lot	Test Name / Condition	Duration	Qual Device: TPS71133PWP RQ1	QBS Product Reference: <u>PLP8861QPWP</u> <u>RQ1</u>	QBS Process Reference: <u>\$0704038C0PLPR</u>
		Test	Group A – A	Acceler	ated Envi	ronment Stress Tests				
				3	22	SAM Analysis, T0	Completed	3/66/0	-	-
	PC	A1	JEDEC J- STD-020; JESD22- A113	3	77	Preconditioning	Level 2- 260C	3/AII/0	1/AII/0	3/AII/0
				3	22	SAM Analysis, Post Preconditioning	Completed	3/66/0	-	-
	HAST	A2	JEDEC JESD22- A110	з	77	Biased HAST, 130C/85%RH	98 Hours	3/231/0	1/77/0	3/231/0
				3	1	Cross Section, Post bHAST 96 Hours	Completed	3/3/0	-	-
				3	30	Wire Bond Shear, Post bHast, 98 Hours	Wires	3/90/0	-	-
				3	30	Bond Pull over Stitch, post bHAST, 98 Hours	Wires	3/90/0	-	-
				3	30	Bond Pull over Ball, Post bHAST, 98 Hours	Wires	3/90/0	-	-
	HAST	A2	JEDEC JESD22- A110	3	70	Biased HAST, 130C/85%RH	192 Hours	3/231/0		3/231/0
				3	h	Cross Section, Post bHAST 192 Hours	Completed	3/3/0	-	-
				3	22	SAM Analysis, Post bHAST, 192 Hours	Completed	3/66/0	-	-
				3	30	Wire Bond Shear, Post bHast 192 Hours	Wires	3/90/0	-	-
				3	30	Bond Pull over Stitch, post bHAST 192 Hours	Wires	3/90/0	-	-
				3	30	Bond Pull over Ball, Post bHAST 192 Hours	Wires	3/90/0	-	-
	UHAST	А3	JEDEC JESD22- A118	3	77	Unbiased HAST, 130C/85%RH	98 Hours	3/231/0	-	-
	AC	А3	JEDEC JESD22- A102	3	77	Autoclave 121C	98 Hours	-	1/77/0	3/231/0
	тс	A4	JEDEC JESD22- A104	3	77	Temperature Cycle, -85/150C	500 Cycles	3/231/0	1/77/0	3/231/0

	Tos	Group P	Accolor	rated Life	time Simulation Tests				
	Tes	JEDEC	Accelei	ated Life	ume simulation rests				
HTOL	B1	JESD22- A108	3	77	Life Test, 125C	1000 Hours	1/77/0	-	3/231/0
HTOL	B1	JEDEC JESD22- A108	3	77	Life Test, 150C	408 Hours	-	1/77/0	-
ELFR	B2	AEC Q100- 008	3	800	Early Life Failure Rate, 125C	48 Hours	-	-	3/2400/0
EDR	В3	AEC Q100- 005	3	77	NVM Endurance, Data Retention, and Operational Life	-	NA	NA	NA
	Τe		– Packa	age Assei	mbly Integrity Tests				
WBS	C1	AEC Q100- 001	3	30	Wire Bond Shear, Cpk>1.67	Wires	3/90/0	-	-
WBP	C2	MIL- STD883 Method 2011	3	30	Wire Bond Pull, Cpk>1.87	Wires	3/90/0	-	-
SD	С3	JEDEC JESD22- B102	1	15	Surface Mount Solderability >95% Lead Coverage	Pb Free	1/15/0	-	-
SD	С3	JEDEC JESD22- B102	1	15	Surface Mount Solderability >95% Lead Coverage	Pb	1/15/0	-	-
PD	C4	JEDEC JESD22- B100 and B108	3	10	Auto Physical Dimensions Cpk>1.87	-	3/30/0	-	-
LI	C6	JEDEC JESD22- B105	1	50	Lead Integrity	Leads	N/A	N/A	N/A
		est Group D	– Die I	Fabricatio	n Reliability Tests				
EM	D1	JESD61	-	-	Electromigration	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Injection Carrier	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements

		Test Group	E – Ele	ectrical V					
НВМ	E2	AEC Q100- 002	1	3	ESD - HBM - Q100	3000 V	1/3/0	1/3/0	-
CDM	E3	AEC Q100- 011	1	3	ESD - CDM - Q100	1000 V	1/3/0	1/3/0	-
LU	E4	AEC Q100- 004	1	6	Latch-up	(Per AEC- Q100-004)	1/6/0	1/6/0	-
ED	E5	AEC Q100- 009	3	30	Electrical Distributions	Cpk>1.67 Room, Hot, and Cold	3/90/0	3/90/0	3/90/0

<sup>-</sup> Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Green/Pb-free Status:

Qualified Pb-Free(SMT) and Green

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<sup>-</sup> The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

<sup>-</sup> The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours

<sup>-</sup> The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

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