



DT1240E-04LP

Product Summary

V _{BR} (Min)	IPP (Max)	Сио (Тур)
5V	5A	0.55pF

Description

The DIODES[™] DT1240E-04LP is a high-performance device suitable for protecting four high-speed I/Os. These devices are assembled in U-DFN2510-10 and U-DFN2510-10 (Type CJ) packages and have high ESD surge capability and low capacitance.

Applications

Typically used at high-speed ports such as USB2.0, USB3.0, USB3.1, IEEE1394 (Firewire[®], iLink), Serial ATA, DVITM, HDMI1.4TM, HDMI2.0TM and PCITM.

4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

Features

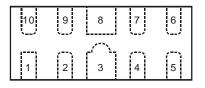
- Clamping Voltage: 7.5V at 10A 100ns, TLP 8.2V at 5A (8µs/20µs)
- IEC 61000-4-2 (ESD): Air ±14kV, Contact ±12kV
- IEC 61000-4-5 (Lighting): 5A (8µs/20µs)
- 4 Channels of ESD Protection
- Low Channel Input Capacitance of 0.55pF Typical
- TLP Dynamic Resistance: 0.2Ω
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

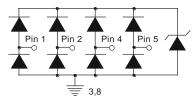
Mechanical Data

- Package: U-DFN2510-10
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Schematic
- Terminals: Finish NiPdAu, Solderable per MIL-STD-202, Method 208 @
- Weight: 0.038 grams (Approximate)

Pin Number	Description
1, 2, 4, 5	I/O
6, 7, 9, 10	No Connection
3, 8	Vss



Pin Description (Top View)



Device Schematic

Ordering Information (Note 4)

Part Number	Package	Marking	Reel Size (inches)	Tape Width (mm)	Packing		
Fait Nulliber	Fackage	Marking	Reel Size (Inches)	Tape Width (min)	Qty.	Carrier	
DT1240E-04LP-7	U-DFN2510-10	MW5	7	8	3,000	Tape & Reel	
D11240E-04LF-7	U-DFN2510-10 (Type CJ)	MW5	7	8	3,000	Tape & Reel	

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

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^{2.} See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.



Marking Information

	MW5	YM	
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MW5 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: J = 2022)
M = Month (ex: 9 = September)



MW5 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 2 = 2022)

W = Week

(ex: a = Week 27; z Represents Week 52 and 53)

X = Internal Code (ex: U = Monday)

Date Code Key for YM

Year	2017		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	E			J	К	L	М	Ν	0	Р	R	S
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Date Code Key for YWX

Year	2017		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	7		2	3	4	5	6	7	8	9	0	1
14 / I-					1							
Week	1-26				27-52				53			
Code	A-Z				a-z			z				
Internal Code	Su	ın	Mor	n	Tue		Wed	Thu	I	Fri		Sat
Code	Т	•	U		V		W	Х		Y		Ζ

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current, per IEC 61000-4-5	IPP	5	A	I/O to V _{SS} , 8/20µs
Peak Pulse Power, per IEC 61000-4-5	Ppp	47	W	I/O to Vss, 8/20µs
ESD Protection – Contact Discharge, per IEC 61000-4-2	Vesd_contact	±12	kV	I/O to V _{SS}
ESD Protection – Air Discharge, per IEC 61000-4-2	V _{ESD_AIR}	±14	kV	I/O to V _{SS}
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C	—

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	PD	350	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	Reja	360	°C/W

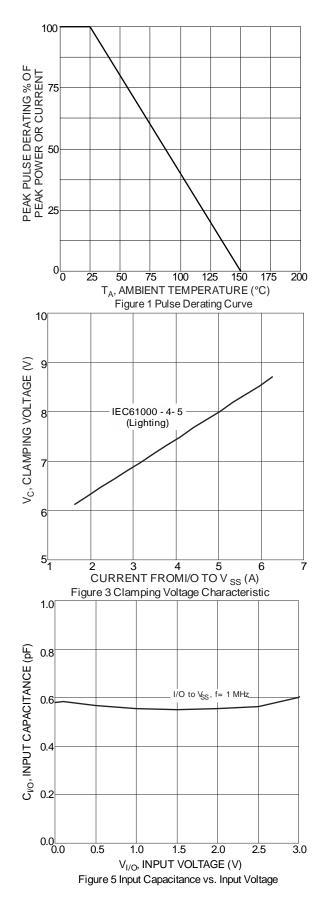
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

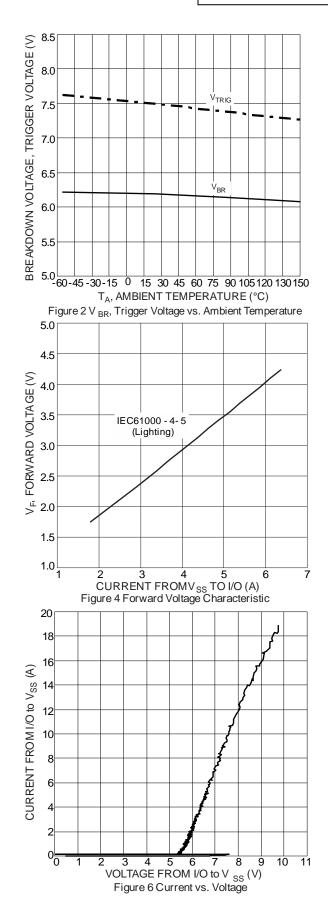
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	VRWM			3.3	V	
Reverse Current	IR	_	_	1.0	μA	V_R = 3.3V, I/O to V _{SS}
Reverse Breakdown Voltage	V _{BR}	5	_	_	V	$I_R = 1 \text{mA}$, I/O to V _{SS}
Forward Clamping Voltage	VF	-1.0	-0.85	_	V	IF = -15mA, I/O to Vss
Reverse Clamping Voltage (Note 6)	Vc	_	8.2	9.5	V	IPP = 5A, I/O to Vss, 8/20µs
ESD Clamping Voltage	Vesd	_	7.5	_	V	TLP, 10A, t _P = 100ns, I/O to Vss
Dynamic Reverse Resistance	Rdif-r	_	0.2	_	Ω	TLP, 10A, t _P = 100ns, I/O to Vss
Dynamic Forward Resistance	RDIF-F	_	0.2	_	Ω	TLP, 10A, tP = 100ns, Vss to I/O
Channel Input Capacitance	Ci/O	_	0.55	0.65	pF	VI/O = 2.5V, Vss = 0V, f = 1MHz
Delta Ci/o	CI/OMAX-CI/OMIN	_	0.04	_	pF	CI/OMAX-CI/OMIN

Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's website at http://www.diodes.com/package-outlines.html. 6. Clamping voltage value is based on an 8x20µs peak pulse current (I_{PP}) waveform.

2 of 6 www.diodes.com





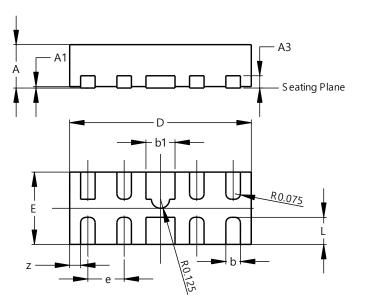




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

Package Type 1:

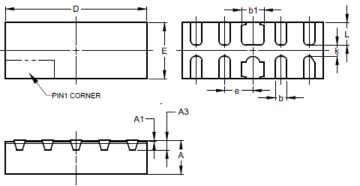


	U-DFN2510-10								
Dim	Dim Min Max Typ								
Α	0.545	0.605	0.575						
A1	0.00	0.05	0.03						
A3	-	-	0.13						
b	0.15	0.25	0.20						
b1	0.35	0.45	0.40						
D	2.450	2.575	2.500						
е	-	-	0.50						
Е	0.950	1.075	1.000						
L	0.325	0.425	0.375						
z	-	-	0.150						
AI	I Dimens	sions in	mm						

Package Type 2:

U-DFN2510-10 (Type CJ)

U-DFN2510-10



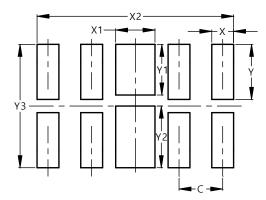
U-DFN2510-10									
	(Type CJ)								
Dim	Min	Max	Тур						
Α	0.545	0.605							
A1	0.00	0.05							
A3	0.	152RE	F						
b	0.150	0.250							
b1	0.350	0.450							
D	2.450	2.575							
E	0.950	1.075							
е			0.500						
E	0.950	1.075	1.000						
L	0.350	0.450							
k	0.	200RE	F						
All D	imensi	ons in	mm						



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2510-10 & U-DFN2510-10 (Type CJ)



Dimensions	Value (in mm)
С	0.500
Х	0.250
X1	0.450
X2	2.250
Y	0.625
Y1	0.575
Y2	0.700
Y3	1.400



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