



P-Channel 20 V (D-S) MOSFET



Marking code: H7

PRODUCT SUMMARY						
V _{DS} (V)	-20					
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -4.5$ V	0.066					
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -2.5$ V	0.086					
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -1.8$ V	0.130					
Q _g typ. (nC)	9					
I _D (A) ^d	-3.8					
Configuration	Single					

FEATURES

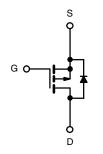
- TrenchFET® power MOSFET
- 100 % R_g tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



HALOGEN FREE

APPLICATIONS

- Load switch for portable devices
- DC/DC converter



P-Channel MOSFET

ORDERING INFORMATION			
Package	SOT-23		
Lead (Pb)-free and halogen-free	Si2367DS-T1-GE3		

PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-source voltage		V _{DS}	-20	V	
Gate-source voltage		V _{GS}	± 8	¬	
Continuous drain current (T _J = 150 °C)	T _C = 25 °C		-3.8		
	T _C = 70 °C	l . —	-3		
	T _A = 25 °C	I _D	-2.8 ^{a, b}		
	T _A = 70 °C		-2.2 a, b	Α	
Pulsed drain current (10 µs width)		I _{DM}	-15		
Continuous source-drain diode current	T _C = 25 °C		-1.4		
	T _A = 25 °C	l _S	-0.8 ^{a, b}		
Maximum power dissipation	T _C = 25 °C		1.7		
	T _C = 70 °C	_	1.1	14/	
	T _A = 25 °C	P _D	0.96 ^{a, b}	— W	
	T _A = 70 °C		0.62 ^{a, b}		
Operating junction and storage temperature rai	T _J , T _{stg}	-55 to +150	°C		

THERMAL RESISTANCE RATINGS							
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT		
Maximum junction-to-ambient a, c	t ≤ 5 s	R _{thJA}	100	130	°C/W		
Maximum junction-to-foot (drain)	Steady state	R_{thJF}	60	75] C/W		

Notes

- a. Surface mounted on 1" x 1" FR4 board
- b. t = 5 s
- c. Maximum under steady state conditions is 175 °C/W
- d. $T_C = 25$ °C



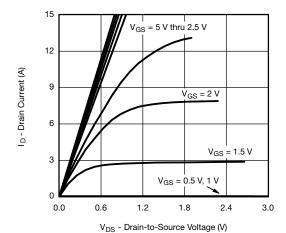
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static		<u> </u>			•	· ·	
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-20	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	J 050 A	-	-20	-	mV/°C	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = -250 μA	-	-2.5	-		
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	-0.4	-	-1	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	-	-	± 100	nA	
		$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	-1	μΑ	
Zero gate voltage drain current	I _{DSS}	V _{DS} = -20 V, V _{GS} = 0 V, T _J = 55 °C	-	-	-10		
On-state drain current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-5	-	-	Α	
	(* /	$V_{GS} = -4.5 \text{ V}, I_D = -2.5 \text{ A}$	-	0.055	0.066	066 086 Ω	
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} = -2.5 V, I _D = -2 A	-	0.071	0.086		
	==(=:.)	V _{GS} = -1.8 V, I _D = -1.5 A	-	0.100	0.130		
Forward transconductance a	9 _{fs}	V _{DS} = -10 V, I _D = -2.5 A	-	7.5	-	S	
Dynamic ^b	0.0	50 . 5			l	I	
Input capacitance	C _{iss}		_	561	_	pF	
Output capacitance	C _{oss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	112	_		
Reverse transfer capacitance	C _{rss}	be , de ,	-	89	_		
The version of the ve	0155	$V_{DS} = -10 \text{ V}, V_{GS} = -8 \text{ V}, I_D = -2.5 \text{ A}$	_	15	23	1	
Total gate charge	Q_g	VDS = 10 V, VGS = 0 V, ID = 2.07	_	9	13.5	nC	
Gate-source charge	Q _{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -2.5 \text{ A}$	_	1	-		
Gate-drain charge	Q _{ad}	ge i , de i , b	-	2.5	-		
Gate resistance	R _g	f = 1 MHz	2	10	20	Ω	
Turn-on delay time	t _{d(on)}			20	40		
Rise time	t _r	$V_{DD} = -10 \text{ V}, R_L = 5 \Omega,$	-	20	40	1	
Turn-off delay time	t _{d(off)}	$I_D \cong -2$ A, $V_{GEN} = -4.5$ V, $R_g = 1$ Ω	_	40	70	1	
Fall time	t _f	3	_	10	20	1	
Turn-on delay time	t _{d(on)}		_	8	16	ns	
Rise time	t _r	$V_{DD} = -10 \text{ V}, R_{L} = 5 \Omega,$	_	9	18	-	
Turn-off delay time	t _{d(off)}	$I_D \cong -2 \text{ A}, V_{GEN} = -8 \text{ V}, R_a = 1 \Omega$	-	35	65		
Fall time	t _f	5 . 52.1	_	9	18		
Drain-Source Body Diode Characterist					1		
Continuous source-drain diode current	Is	T _C = 25 °C	_	_	-1.4		
Pulse diode forward current	I _{SM}	<u> </u>	_	_	-15	A	
Body diode voltage	V _{SD}	I _S = -2 A, V _{GS} = 0 V	_	-0.79	-1.2	V	
Body diode reverse recovery time	t _{rr}	.5 -19.05 -1		21	35	ns	
Body diode reverse recovery time	_	1 0 A di/d+ 100 A/··-	<u>-</u>	15	25	nC	
Reverse recovery fall time	Q _{rr}	I _F = -2 A, di/dt = 100 A/μs, T _{.I} = 25 °C		9	-	110	
neverse recovery rail time	t _a	1 1 - 20 0	-	9	_	ns	

Notes

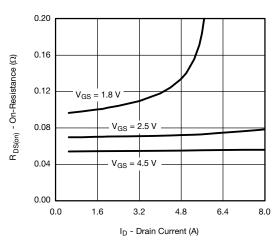
- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

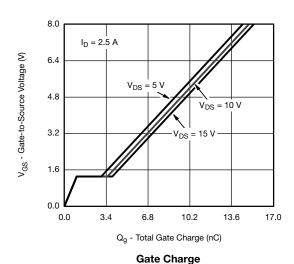


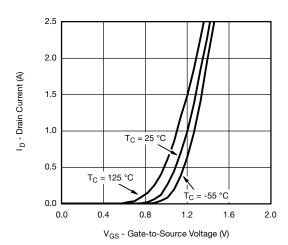


Output Characteristics

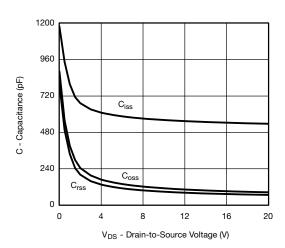


On-Resistance vs. Drain Current and Gate Voltage

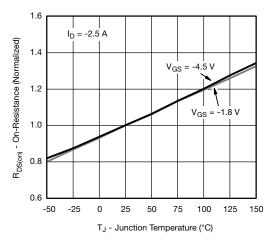




Transfer Characteristics

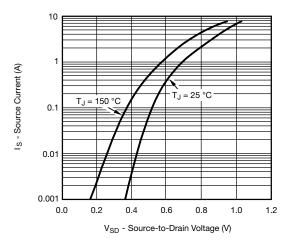


Capacitance

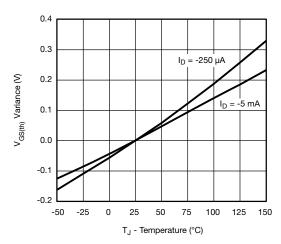


On-Resistance vs. Junction Temperature

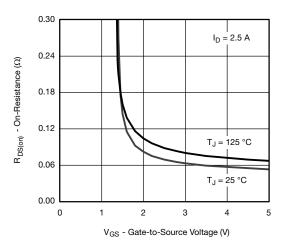




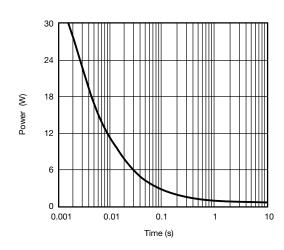
Source-Drain Diode Forward Voltage



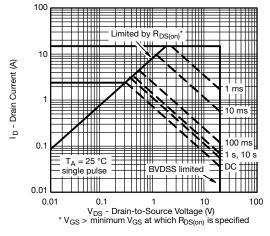
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage

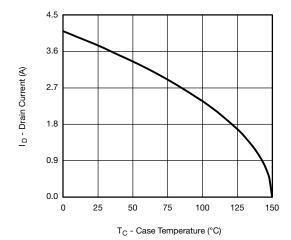


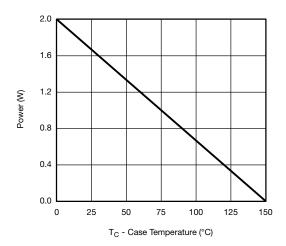
Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Ambient

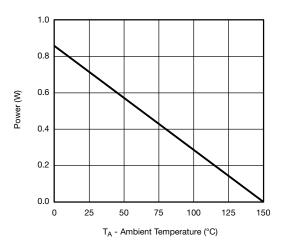






Current Derating a

Power Derating, Junction-to-Foot

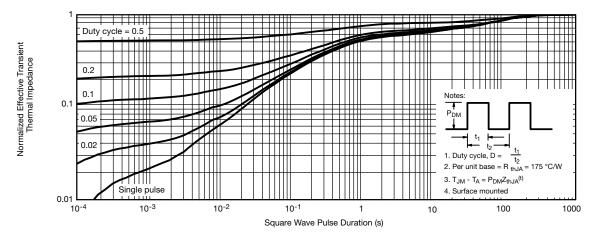


Power, Junction-to-Ambient

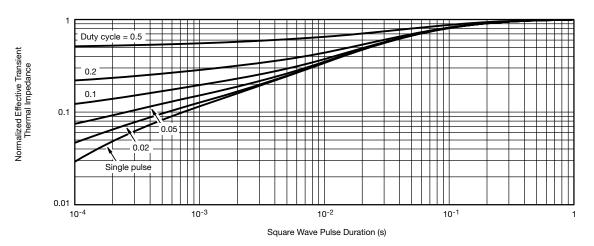
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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Vishay Siliconix

SOT-23 (TO-236): 3-LEAD







Dim	MILLI	METERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
FCN: S-03946-Rev K 09-	lul-01	•			

ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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