

# **Final Product Change Notification**

202201008F01: i.MX RT1024 Fab Site Transfer from SMIC8 to SMICB2 with New Silicon Revision

Note: This notice is NXP Company Proprietary.

Issue Date: Mar 21, 2022 Effective date: Jun 19, 2022

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#### **Management summary**

Wafer Fabrication site transfer for the i.MX RT1024 to the SMICB2, Beijing, China Wafer Fabrication site.

#### **Change Category**

[]Wafer Fab Process	[]Assembly Process	[]Product Marking	[]Test Process	[X]Design
[]Wafer Fab Materials	[]Assembly Materials	[]Mechanical Specification	[]Test Equipment	[]Errata
[X]Wafer Fab Location	[]Assembly Location	[]Packing/Shipping/Labeling	[]Test Location	[]Electrica spec./Test coverage
		_		

[]Firmware [X]Other: data sheet

## **PCN Overview**

# **Description**

NXP Semiconductors announces the Wafer Fabrication site transfer for the i.MX RT1024 associated with this notification, from the current SMIC8, Shanghai, China Wafer Fabrication site to the SMICB2, Beijing, China Wafer Fabrication site.

Wafer Fabrication site transfer was successfully qualified adhering to NXP specifications.

i.MX RT1024 maskset revision 1N87V (from 0N87V) which has been qualified with new part numbers MIMXRT1024xxxxB. Datasheet update to Revision 2 with new part number added. The Datasheet can be found from below NXP website:

https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/i-mx-rt-crossover-mcus/i-mx-rt1024-crossover-mcu-with-arm-cortex-m7-core:i.MX-RT1024?tab=Documentation Tab

The new mask revision has the below improvement:

Fixed ERR050101 USB: Endpoint conflict issue in device mode. An endpoint conflict occurs when the USB is working in device mode during an isochronous communication.

Due to the unprecedented supply constraints within the Semiconductor Industry and existing backlog profile, new orders must be placed on the new replacement parts immediately. There are cases that existing backlog will need to be migrated to the replacement parts. NXP will work with each customer to manage those transitions.

Please contact your NXP sales or marketing representative for additional information.

#### Reason

SMIC8, Shanghai, China wafer Fabrication site will cease production for 40nm technology production line and need transfer the products to internal SMICB2 Wafer Fabrication site. The Qualification of SMICB2, Beijing, China Wafer fabrication site is required for manufacturing flexibility and customer supply assurance.

The release of i.MX RT1024 new maskset 1N87V fixed ERR050101 USB: Endpoint conflict issue in device mode.

#### **Identification of Affected Products**

Replacement part type created, see Parts Affected list

### **Product Availability**

#### Sample Information

Samples are available upon request

#### **Production**

Planned first shipment Sep 01, 2022

### Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

#### **Data Sheet Revision**

A new datasheet will be issued

#### **Disposition of Old Products**

Existing inventory will be shipped until depleted

Self qualification: view online Additional documents: view online

## **Timing and Logistics**

In compliance with JEDEC J-STD-046, your acknowledgement of this change is expected by Apr 20, 2022.

## **Contact and Support**

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards. Customer Focus, Passion to Win.

NXP Quality Management Team.

#### **About NXP Semiconductors**

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