DC	I Niss	m h o u	201	710250	<u> </u>		DCN Date:	1		m, 11	2010	
PCN Number: Title: Datasheet for				710250			PCN Date:	January 11, 2018				
		er Contact:							-	4.	Quality Convisos	
			PCN	Manage	<u> </u>			ט	ер	t:	Quality Services	
Clia		Type:				Docian			7	Mafo	r Bump Cito	
H	Assembly Site Assembly Process				☐ Design ☐ Data Sheet					Wafer Bump Site Wafer Bump Material		
Ħ	Assembly Materials			Part number change				_	Wafer Bump Process			
	Mechanical Specification			Test Site					Wafer Fab Site			
	Packing/Shipping/Labeling			Test Process					Wafer Fab Materials			
										Wafe	r Fab Process	
Notification Details												
Description of Change: Texas Instruments Incorporated is announcing an information only notification.												
The product datasheet(s) is being updated as summarized below. The following change history provides further details. TEXAS INSTRUMENTS TPS548D22												
								SL	USC	70D – M	ARCH 2016-REVISED JULY 2017	
Cha	nges 1	from Revision C (Septem	ber 2016)) to	Revision D					Page	
Added MIN and MAX values for VDD UVLO rising threshold												
Added MIN and MAX for all SS settings and table notes 3 and 4 in <i>Electrical Characteristics</i>												
Changed V _{OUT} = 5 V to V _{OUT} = 5.5 V for Figure 13												
Added notes for 8 ms and 4 ms in Table 4; added Application Workaround to Support 4-ms and 8-ms SS Settings 18												
Added Figure 16 and Figure 17												
• Changed "1.6 µs" to "1.538 µs"; "150 ns" to "300 ns" and "963 µF" to "969 µF"												
	3	,	, ,				-					
<u>The</u>	data	sheet number	will b	e chang	ging							
De	vice	Family				Change	From:		(Chang	je To:	
TF	PS548	3D22				SLUSC7	'0C		5	SLUS	C70D	
		anges may be	revie	wed at	the	datasheet	links provide	d.				
<u>http</u>)://w	ww.ti.com/pro	duct/	ΓPS548Ι	<u>D22</u>	<u>2</u>						
Rea	son	for Change:										
To a	accur	ately reflect d	evice (characte	eris	tics.						
Ant	icipa	ted impact o	n Fit	Form,	Fι	inction, O	uality or Rel	iabili	itv	(pos	sitive / negative):	
		-					-					
In order to ensure either 4-ms or 8-ms soft start setting would work properly in the customer system, additional application consideration is needed. The recommended application												
workaround to support the 4-ms and 8-ms soft start settings is to ensure sufficient time delay												
between the VDD and EN_UVLO signals. There are no changes to the actual device. Changes to product identification resulting from this PCN:												
		s to product	ıaent	iricatio	n r	esulting f	rom this PCI	N:				
Non												
Pro	duct	Affected:										
TPS548D22RVFR TPS54					8D	22RVFT						

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com