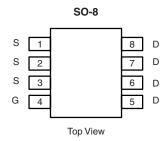




P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
- 30	0.010 at V _{GS} = - 10 V	- 13		
	0.0155 at V _{GS} = - 4.5 V	- 10		



Ordering Information: Si4411DY-T1-E3 (Lead (Pb)-free)

Si4411DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

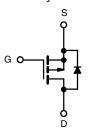
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

- Notebook
 - Load Switch
 - Battery Switch



P-Channel MOSFET

Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 30		V
Gate-Source Voltage		V _{GS}	± 20		V
Outlines During Outline (T. 450.00)	T _A = 25 °C	- I _D	- 13	- 9	^
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 10.5	- 7.5	
Pulsed Drain Current		I _{DM}	- 50		Α
Continuous Source Current (Diode Conduction) ^a		I _S	- 2.7	- 1.36	
Maximum Power Dissipation ^a	T _A = 25 °C	В	3.0	1.5	W
	T _A = 70 °C	P_{D}	1.9	0.95	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	t ≤ 10 s	R _{thJA}	33	42	°C/W	
Maximum Junction-to-Ambient*	Steady State		70	85		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	16	21		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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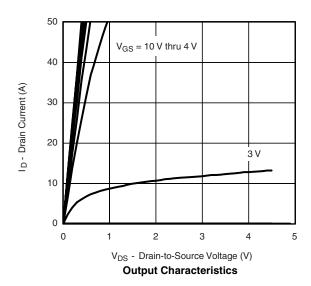
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit	
Static			•		<u> </u>		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$			- 3.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zava Cata Valtaga Drain Current	1	V _{DS} = - 30 V, V _{GS} = 0 V			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$	= - 30 V, V _{GS} = 0 V, T _J = 70 °C		- 10	μΑ	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 30			Α	
	В	V _{GS} = - 10 V, I _D = - 13 A		0.008			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 10 A		0.0125	0.0155	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 13 A		40		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 2.7 A, V _{GS} = 0 V		- 0.74	- 1.1	V	
Dynamic ^b			•				
Total Gate Charge	Q_g			43	65	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -5 \text{ V}, I_{D} = -13 \text{ A}$		8.5			
Gate-Drain Charge	Q_{gd}			18.5			
Gate Resistance	R_{g}			3.4		Ω	
Turn-On Delay Time	t _{d(on)}			18	30		
Rise Time	t _r	V_{DD} = - 15 V , R_L = 15 Ω		15	25	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong -1 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 6 \Omega$		140	250		
Fall Time	t _f			75	120		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 2.1 A, dI/dt = 100 A/μs		60	100		

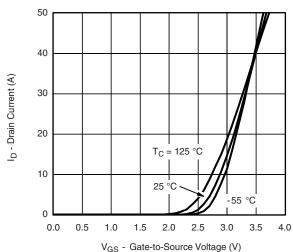
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





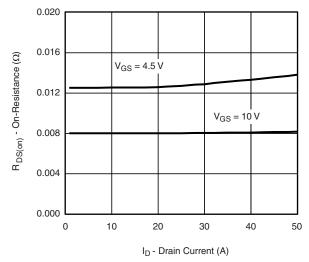
Transfer Characteristics



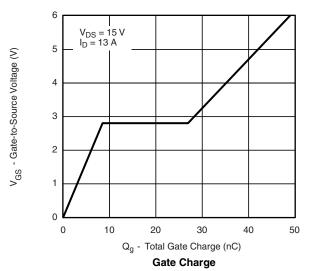


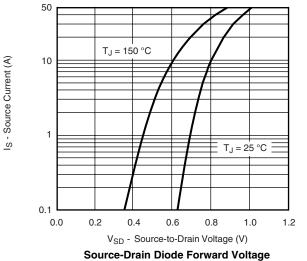


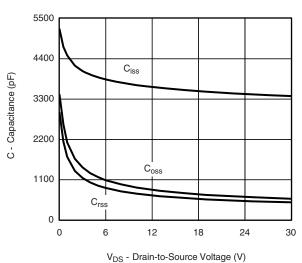
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

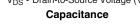


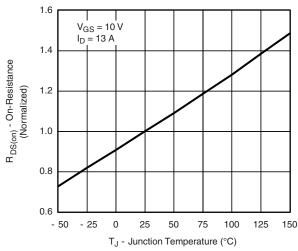
On-Resistance vs. Drain Current



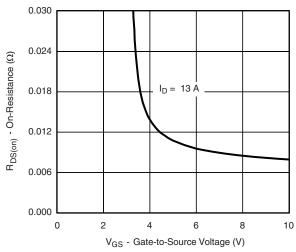








On-Resistance vs. Junction Temperature

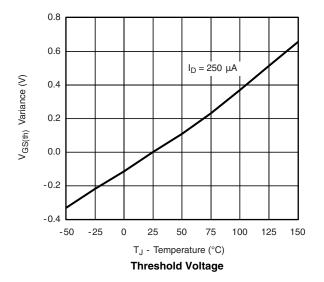


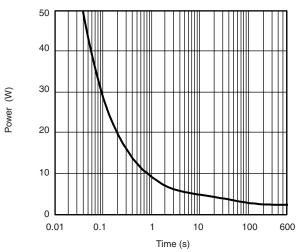
On-Resistance vs. Gate-to-Source Voltage

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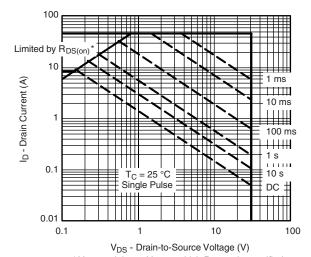
VISHAY

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

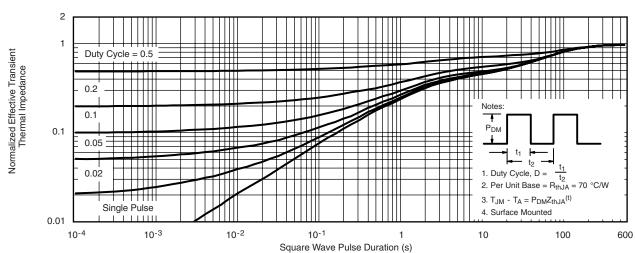




Single Pulse Power

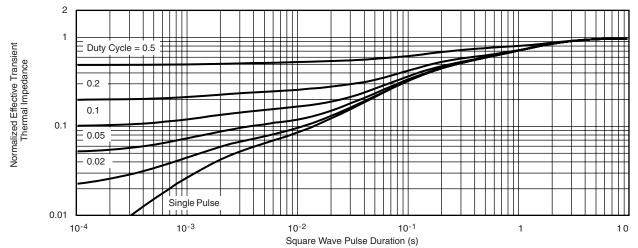


 * V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified **Safe Operating Area, Junction-to-Case**





TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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