PCN Number:	202204	04000.1		PCN Date: A		pril 04, 2022			
Title: Qualifica	ation of l	JTL3 as ar	additiona	ices					
Customer Conta									
		July 3,		Estimat	Estimated Samp				
Change Type:					vanabi	iicy i	Sample request		
Assembly Sit	<u> </u>		Design			Wafe	r Bump Site		
Assembly Pro			Data S				r Bump Material		
Assembly Ma				ımber change			Vafer Bump Process		
Mechanical S		ion	Test Site				afer Fab Site		
Packing/Ship	ping/Lab	eling	Test Process			Wafe	Wafer Fab Materials		
							Vafer Fab Process		
			PCN	Details					
Description of C	hange:								
Texas Instruments Incorporated is announcing the qualification of UTL3 as an alternate Assembly site for devices listed below in the product affected section. There are no construction differences between the two sites									
Reason for Char	nge:								
Supply continuity									
Anticipated imp	act on F	orm, Fit,	Function	, Quality or Rel	iability	(pos	itive / negative)):	
None									
Impact on Envir	onment	al Rating	s						
Checked boxes in change. If below larges.									
RoHS F		RE	ACH	Green S	Green Status		IEC 62474		
		🛛 No Cha	ange 🗵 No Change						
Changes to prod	luct ide	ntificatio	n resultin	g from this PCI	N :				
Assembly Site	Assemb	ly Site Orig	in (22L) A	ssembly Country C	ode (23L	.)	Assembly City		
UTL1	NSE			THA	THA		Bangkok		
UTL3	UT3			THA			Bangpakong		
	G4 R SEAL DT 03/29/04 03:1750		KMCKM0	(1P) \$N74L\$07N (Q) 2000 ((31T) LOT: 3959 (4W) TKY(1T) 75 (P) (2P) REV: (V) (20L) CSO: SHE (21	D) 0336	312 17 84			
Product Affecte						1		1	
DRV5032AJDMRR		RV5032DU	DMDD	DRV5032FADMF	0.0	I DRV	アリろンEDDMDD	1	
DRV5032AJDMRT		RV5032DU		DRV5032FADMF			5032FDDMRR 5032FDDMRT	-	



Qualification Report Approve Date 30-MARCH -2022

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Name	Condition	Duration	Qual Device: DRV5032DUDMRR	Qual Device: DRV5032FDDMRR	Qual Device: DRV5032FADMRR	Qual Device: DRV5032AJDMRR
UHAST	A3	Unbiased HAST	130C/85%RH	96 Hours	1/77/0	1/77/0	1/77/0	-
TC	A4	Temperature Cycle	-65C/150C	500 Cycles	1/77/0	1/77/0	1/77/0	-
HTSL	A6	High Temperature Storage Life	150C	1000 Hours	1/77/0	1/77/0	1/77/0	-
CHAR	E5	Electrical Characterization	Min, Typ, Max Temp	-	1/30/0	-	-	-

- · QBS: Qual By Similarity
- Qual Device DRV5032DUDMRR is qualified at MSL1 260C
- Qual Device DRV5032FDDMRR is qualified at MSL1 260C
- · Qual Device DRV5032FADMRR is qualified at MSL1 260C
- Qual Device DRV5032AJDMRR is qualified at MSL1 260C
- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47:-55C/125C/700 Cycles and -65C/150C/500 Cycles

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Green/Pb-free Status:

Qualified Pb-Free(SMT) and Green

TI Qualification ID: R-CHG-2108-069

For questions regarding this notice, e-mails can be sent to the contacts shown below or your local Field Sales Representative.

Location	E-Mail				
WW Change Management Team	PCN ww admin_team@list.ti.com				

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