CHANGE NOTIFICATION



Analog Devices, Inc. 1630 McCarthy Blvd., Milpitas CA (408) 432-1900

March 08, 2018 PCN_030818

Dear Sir/Madam:

Subject: Notification of Change to LTC3670 Datasheet

Please be advised that Analog Devices, Inc. Milpitas, California has made a minor change to the LTC3670 product datasheet to facilitate improvement in our manufacturing capability. The change is shown on the attached page of the marked up datasheet. There was no change in form, fit, function, quality or reliability of the product. The product shipped after May 08, 2018 will be tested to the new limits.

Should you have any questions or concerns please contact your local Analog Devices sales representatives or you may contact me at 408-432-1900 ext. 2077, or by e-mail at JASON.HU@ANALOG.COM. If I do not hear from you by May 08, 2018, we will consider this change to be approved by your company.

Sincerely,

Jason Hu Quality Assurance Engineer

For questions on this PCN, please contact Jason Hu or you may send an email to your regional contacts below or contact your local ADI sales representatives.

Rest of Asia: PCN_ROA@analog.com

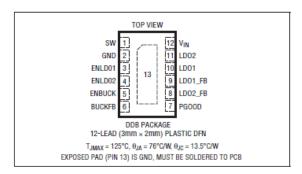
LTC3670

ABSOLUTE MAXIMUM RATINGS

(Notes 1, 2, 3)

VIN, ENBUCK, ENLDO1, ENLDO2, PG00D-0.3V to 6V SW, BUCKFB, LD01_FB, LD02_FB, LD01, LD02.....-0.3V to (V_{IN} + 0.3V) I_{SW}......600mA I_{LD01}, I_{LD02}......250mA I_{PGOOD}......40mA Junction Temperature 125°C Operating Temperature Range.....-40°C to 85°C Storage Temperature Range.....-65°C to 125°C

PIN CONFIGURATION



ORDER INFORMATION

LEAD FREE FINISH	TAPE AND REEL	PART MARKING	PACKAGE DESCRIPTION	TEMPERATURE RANGE
LTC3670EDDB#PBF	LTC3670EDDB#TRPBF	LDBY	12-Lead (3mm × 2mm) Plastic DFN	-40°C to 85°C

Consult LTC Marketing for parts specified with wider operating temperature ranges.

Consult LTC Marketing for information on non-standard lead based finish parts.

For more information on lead free part marking, go to: http://www.linear.com/leadfree/

For more information on tape and reel specifications, go to: http://www.linear.com/tapeandreel/

ELECTRICAL CHARACTERISTICS The • denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25$ °C. $V_{IN} = 3.6$ V, unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
V _{IN}	Input Voltage Range		•	2.5		5.5	V
V _{UVLO}	Undervoltage Lockout Threshold	V _{IN} Rising			2.2	2.3	2.45 V
	Undervoltage Lockout Hysteresis				18	100	m۷
IΩ	V _{IN} Quiescent Current, No Load All Outputs Enabled Buck Enabled Only Buck Enabled Only, in Dropout One LDO Enabled Only Shutdown	(Note 4) VBUCKFB = 0.9V VBUCKFB = 0.9V VBUCKFB = 0.9V VENBUCKFB = 0V VENBUCK = VENLDO1 = VENLDO2 = 0V			70 38 700 22	110 60 1100 35 1	Ац Ац Ац Ац Ац
V _{IL} V _{IH}	ENBUCK, ENLDO1, ENLDO2 Pin Thresholds Logic Low Voltage Logic High Voltage		•	1.2		0.4	V V
	ENBUCK, ENLDO1, ENLDO2 Pin Pull-Down Resistance				4		MΩ
R _{PGOOD}	PGOOD Pin Logic Low Output Resistance				30		Ω
	PGOOD Pin Hi-Z Leakage	V _{PGOOD} = 6V				1	μА
	PGOOD Threshold on Feedback Voltages of Enabled Regulators	(Note 5)			92		%

