



### DMN2005UFGQ

### 20V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C (Note 10)
20V	$4.6 m\Omega$ @ $V_{GS} = 4.5 V$	50A
201	$8.7 \text{m}\Omega @ V_{GS} = 2.5 \text{V}$	36A

### **Features and Benefits**

- Low R<sub>DS(ON)</sub>—Ensures On-State Losses are Minimized
- Small-Form Factor Thermally Efficient Package Enables Higher **Density End Products**
- Occupies 33% of the Board Area Occupied by SO-8 Enabling Smaller End Product
- 100% Unclamped Inductive Switching, Test in Production— Ensures More Reliable And Robust End Application
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

## **Description and Applications**

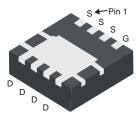
This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

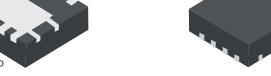
- Motor Control
- Load Switch
- DC-DC Converters

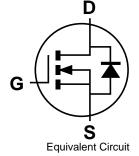
#### **Mechanical Data**

- Case: PowerDI®3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208@3
- Weight: 0.072 grams (Approximate)

#### PowerDI3333-8







**Bottom View** 

Top View

### Ordering Information (Note 5)

Part Number	Case	Packaging		
DMN2005UFGQ-7	PowerDI3333-8	2000/Tape & Reel		
DMN2005UFGQ-13	PowerDI3333-8	3000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

### Marking Information



N05= Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 19 = 2019) WW = Week Code (01 to 53)



## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V <sub>DSS</sub>	20	V		
Gate-Source Voltage			V <sub>GSS</sub>	±12	V
Continuous Drain Current (Notes 7&10) V <sub>GS</sub> = 4.5V	Steady State	$T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$	I <sub>D</sub>	50 40	А
		$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	18 14	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	130	Α		
Maximum Continuous Body Diode Forward Current (Note 7)			I <sub>S</sub>	2.6	Α
Avalanche Current , L = 0.2mH			I <sub>AS</sub>	23.9	Α
Repetitive Avalanche Energy, L = 0.2mH	E <sub>AS</sub>	58.4	mJ		

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	P <sub>D</sub>	1.05	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>ÐJA</sub>	120	°C/W
Total Power Dissipation (Note 7)	T <sub>A</sub> = +25°C	P <sub>D</sub>	2.27	W
Thermal Resistance, Junction to Ambient (Note 7) Steady State		R <sub>ÐJA</sub>	55	°C/W
Thermal Resistance, Junction to Case (Note 7)		R <sub>eJC</sub>	6.1	*C/VV
Operating and Storage Temperature Range		$T_{J,}T_{STG}$	-55 to +150	°C

Notes:

- 6. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.7. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

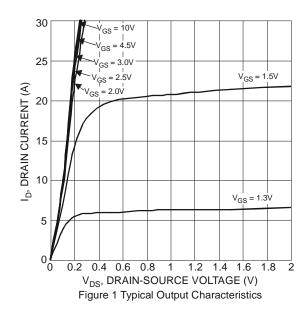


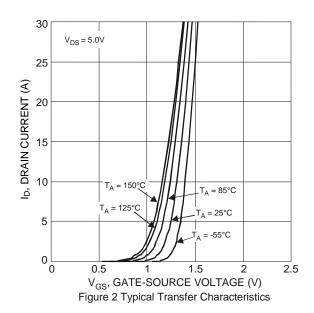
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	_	10	μΑ	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.4	0.7	1.2	٧	$V_{DS} = V_{GS}, I_D = 250\mu A$	
Static Drain-Source On-Resistance	D		4	4.6	mΩ	$V_{GS} = 4.5V, I_D = 13.5A$	
Static Dialii-Source Oil-Resistance	R <sub>DS(ON)</sub>	ı	4.9	8.7	11122	$V_{GS} = 2.5V, I_D = 13.5A$	
Diode Forward Voltage	$V_{SD}$	_	0.8	1.1	V	$V_{GS} = 0V, I_{S} = 27A$	
DYNAMIC CHARACTERISTICS (Note 9)	DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C <sub>iss</sub>		6,495	_	pF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V, f = 1MHz	
Output Capacitance	Coss	_	546	_	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	477	_	pF		
Gate Resistance	$R_g$	_	0.7	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	$Q_g$	_	68.8	_	nC		
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_g$	_	164	_	nC	V 40V L 27A	
Gate-Source Charge	Q <sub>gs</sub>	_	10.4	_	nC	$V_{DS} = 16V, I_D = 27A$	
Gate-Drain Charge	$Q_{gd}$	_	17.4	_	nC	1	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	12.4	_	ns	$V_{GS} = 5V, V_{DS} = 10V,$ $R_{G} = 4.7\Omega, I_{D} = 13.5A$	
Turn-On Rise Time	t <sub>R</sub>	_	25.7	_	ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	114	_	ns		
Turn-Off Fall Time	t <sub>F</sub>	_	38	_	ns		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	16.1	_	ns	I <sub>F</sub> = 13.5A, di/dt = 100A/μs	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	-	8.5		nC	I <sub>F</sub> = 13.5A, di/dt = 100A/μs	

Notes:

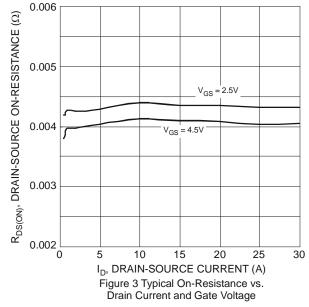
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.
- 10. Limited by package.

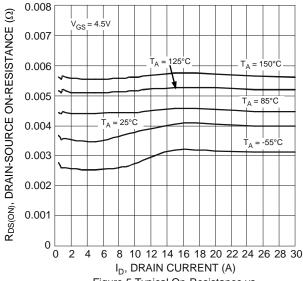


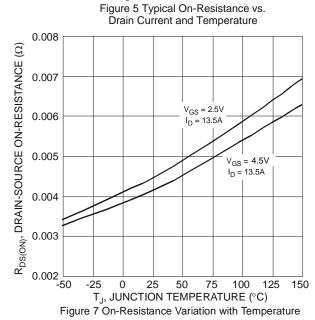


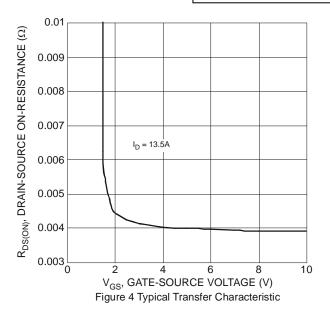












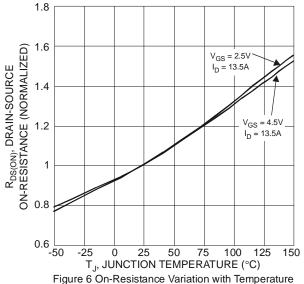
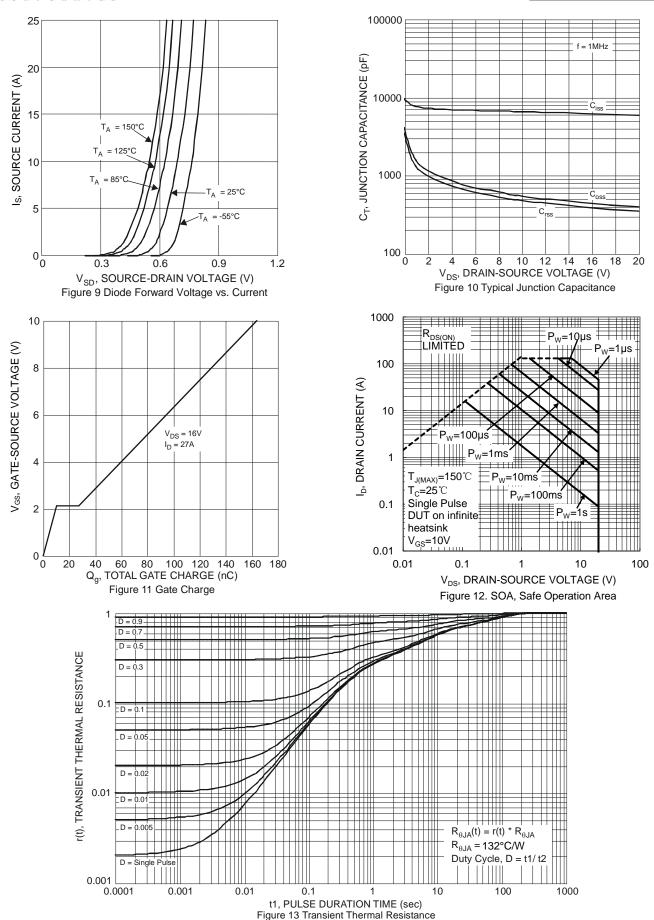


Figure 8 Gate Threshold Variation vs. Junction Temperature





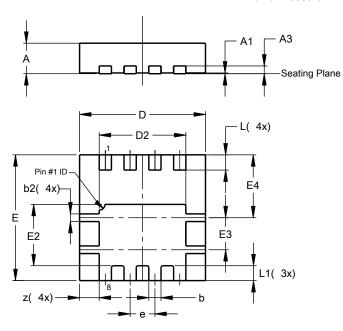




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI3333-8

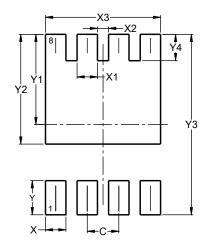


PowerDI3333-8						
Dim	Min	Min Max				
Α	0.75	0.85	0.80			
A1	0.00	0.05	0.02			
A3	-	-	0.203			
b	0.27	0.37	0.32			
b2	0.15	0.25	0.20			
D	3.25	3.35	3.30			
D2	2.22	2.32	2.27			
E	3.25	3.35	3.30			
E2	1.56	1.66	1.61			
E3	0.79	0.89	0.84			
E4	1.60	1.70	1.65			
е	_	_	0.65			
L	0.35	0.45	0.40			
L1	_	_	0.39			
Z	_	_	0.515			
All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI3333-8



Dimensions	Value (in mm)		
С	0.650		
Х	0.420		
X1	0.420		
X2	0.230		
Х3	2.370		
Υ	0.700		
Y1	1.850		
Y2	2.250		
Y3	3.700		
Y4	0.540		



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